11-6625-2962-14+P

TM 11-6625-2962-14&P

TECHNICAL MANUAL

OPERATOR'S, ORGANIZATIONAL, DIRECT SUPPORT AND GENERAL SUPPORT MAINTENANCE MANUAL (INCLUDING REPAIR PARTS AND SPECIAL TOOLS LIST)

FOR

VOLTMETER ME-510/U

(DANA MODEL 5900)

(NSN 6625-00-045-4643)

HEADQUARTERS, DEPARTMENT OF THE ARMY
MAY 1981

D101.11:

READEX

5 SAFETY STEPS TO FOLLOW IF SOMEONE IS THE VICTIM OF ELECTRICAL SHOCK

1. DO NOT TRY TO PULL OR GRAB THE INDIVIDUAL
2. IF POSSIBLE , TURN OFF THE ELECTRICAL POWER
3. IF YOU CANNOT TURN OFF THE ELECTRICAL POWER, PULL, PUSH, OR LIFT THE PERSON TO SAFETY USING A WOODEN POLE OR A ROPE OR SOME OTHER INSULATING MATERIAL
4. SEND FOR HELP AS SOON AS POSSIBLE
5. AFTER THE INJURED PERSON IS FREE OF CONTACT WITH THE SOURCE OF ELECTRICAL SHOCK, MOVE THE PERSON A SHORT DISTANCE AWAY AND IMMEDIATELY START ARTIFICIAL RESUSCITATION

FOR YOUR SAFETY

Before undertaking any maintenance procedure, whether it be a specific troubleshooting or maintenance procedure described herein or an exploratory procedure aimed at determining whether there has been a malfunction, read the applicable section of this manual and note carefully the WARNING and CAUTION notices contained therein.

The equipment described in this manual contains voltages hazardous to human life and safety and which is capable of inflicting personal injury. The cautionary and warning notes are included in this manual to alert operator and maintenance personnel to the electrical hazards and thus prevent personal injury and damage to equipment.

If this instrument is to be powered from the AC Mains through an autotransformer (such as a Variac or equivalent) ensure that the instrument common connector is connected to the ground (earth) connection of the power mains.

Before operating the unit ensure that the protective conductor (green wire) is connected to the ground (earth) protective conductor of the power outlet. Do not defeat the protective feature of the third protective conductor in the power cord by using a two conductor extension cord or a three-prong/two-prong adapter.

Maintenance and calibration procedures contained in this manual sometimes call for operation of the unit with power applied and protective covers removed. Read the procedures carefully and heed Warnings to avoid "live" circuit points to ensure your personal safety.

WARNING

The use of compressed air for cleaning can present a hazard. The compressed air source should limit the nozzle pressure to no more than 29 PSI. Wear goggles and be careful of other personnel in the area as small objects may be propelled during cleaning by compressed air. Take care not to inhale dust particles that may contain contaminants injurious to the respiratory system.

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TECHNICAL MANUAL
No. 11-6625-2962-14&P

HEADQUARTERS
DEPARTMENT OF THE ARMY
WASHINGTON, DC, 22 May 1981

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REPORTING OF ERRORS

You can improve this manual by recommending improvements using DA Form 2028-2 located in the back of the manual. Simply tear out the self-addressed form, fill it out as shown on the sample, fold it where shown, and drop it in the mail.

If there are no blank DA Forms 2028-2 in the back of your manual, use the standard DA Form 2028 (Recommended Changes to Publications and Blank Forms) and forward to the Commander, US Army Communications and Electronics Materiel Readiness Command, ATTN: DRSEL-ME-MQ, Fort Monmouth, NJ 07703.

In either case a reply will be furnished direct to you.

This manual is an authentication of the manufacturer's commercial literature which, through usage, has been found to cover the data required to operate and maintain this equipment. Since the manual was not prepared in accordance with military specifications, the format has not been structured to consider levels of maintenance.

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SECTION 0
INSTRUCTIONS

0-1. SCOPE.

This manual contains instructions for the operations and maintenance of Voltmeter ME-510/U. Throughout this manual Voltmeter ME-510/U is referred to as Model 5900 Digital Multimeter or DVM.

0-2. INDEXES OF PUBLICATIONS.

a. DA Pam 310-4. Refer to the latest issue of DA Pam 310-4 to determine whether there are new editions, changes, or additional publications pertaining to the equipment.

b. DA Pam 310-7. Refer to DA Pam 310-7 to determine whether there are modification work orders (MWO's) pertaining to the equipment.

0-3. FORMS AND RECORDS.

a. Reports of Maintenance and Unsatisfactory Equipment. Maintenance forms, records, and reports which are to be used by maintenance personnel at all levels of maintenance are listed in and prescribed by TM 38-750.

b. Report of Packaging and Handling Deficiencies. Fill out and forward SF 364 (Report of Discrepancy (ROD) as prescribed in AR 735-11-2/DLAR 4140.55/NAVMATINST 4355.73/AFR 400-54/MCO 4430.3E.

0-4. REPORTING OF EQUIPMENT IMPROVEMENT RECOMMENDATIONS (EIR)

EIR's will be prepared using DA Form 2407, Maintenance Request. Instructions for preparing EIR's are provided in TM 38-750, The Army Maintenance Management System. EIR's should be mailed directly to Commander, US Army Communications and Electronics Materiel Readiness Command, ATTN: DRSEL-ME-MQ, Fort Monmouth, New Jersey 07703. A reply will be furnished directly to you.

0-5. ADMINISTRATIVE STORAGE.

Administrative storage of equipment issued to and used by Army activities shall be in accordance with TM 740-90-1.

0-6. DESTRUCTUON OF ARMY ELECTRONICS MATERIEL.

Destruction of Army electronics materiel to prevent enemy use shall be in accordance with TM 750-244-2.

1.1 INTRODUCTION.

1.2 The Model 5900 Digital Multimeter is a five-decade instrument with a sixth digit providing 60% overrange. The basic instrument is equipped for dc and dc/dc ratio measurements on five ranges. With the addition of the optional AC Converter, a-c and ac/dc ratio measurements on four ranges are available. The Ohms Converter, also optional, adds ohms measurements on eight ranges. Complete measurement capability of a fully equipped instrument is tabulated in table 1.1.

Table 1.1 - Measurement Capability

	FUNCTION		
Range	DC & DC/DC RATIO (Basic 5900)	Ohms (Model 52 Ohms Converter)	AC & AC/DC RATIO (Model 32 or 33 AC Converter)
.1V	X		
1V	X		X
10V	X		X
100V	X		X
1000V	X		X
10Ω		X	
.1 KΩ		X	
1 KΩ		X	
10 KΩ		X	
100 KΩ		X	
1000 KΩ		X	
10 MΩ		X	
100 MΩ		X	

1.3 Range can be selected manually or automatically (autorange). In AUTO range, the proper range for a particular measurement is selected automatically (full scale is defined as "100000" on any range). The instrument "upranges" at 160% of full scale and "downranges" at 15% of full scale. Polarity selection is also automatic and is displayed on the readout.

1.4 Two operating modes are provided. In Hold mode (RATE control on EXT), a measurement is held (displayed) until a single reading is commanded by an external command. The new measurement is then held until the next external command. In Periodic mode (RATE control CW), measurements are made automatically at the rate of approximately four per second.

1.5 The basic Model 5900 includes an analog output voltage that is proportional to the parameter being measured (except ratio). The voltage, at 20 volts maximum, is available at a rear panel connector.

1.6 Also included as standard equipment is a solid-state isolated BCD output. TTL-compatible output levels of the reading, function, range, etc., plus a print command are provided. An additional line enables a new reading to be commanded externally. An optional isolated remote programming unit (Model 42) allows all operating commands to be made externally.

1.7 OPTIONS.

1.8 All optional accessories having model numbers are plug-in circuit boards that may be added at any time. A calibrated accessory board can be installed without affecting the d-c calibration of the basic instrument. An instrument shipped without PCB accessories will not be equipped with a Function Switching PCB assembly (Accessory Card, Option 12). This board must be added when accessory boards are installed. Analog accessories are identified in table 1.1.

1.9 Model 42 Remote Programming.

1.10 The Model 42 Remote Programming accessory allows the selection of function, range, filter, read command, etc., to be made externally. Auto range selection is also provided and appropriate timeouts are generated internally when ranging takes place. Remote Programming "overrides" all manual control settings to prevent erratic selections. Complete isolation of the programming unit is achieved by the use of photo-couplers and pulse transformers.

1.11 Rear Input Options (-1, -1B, -1S, -1SB).

1.12 Two rear input options are available for the Model 5900 DMM. The option designated -1 or -1B consists of connector J204 on the back panel with input lines ± INPUT, ± CURRENT, and GUARD wired in parallel with the front panel input terminals; the option designated -1S or -1SB is the same as the -1 or -1B except that the front or rear inputs are selectable by a switch on the front panel.

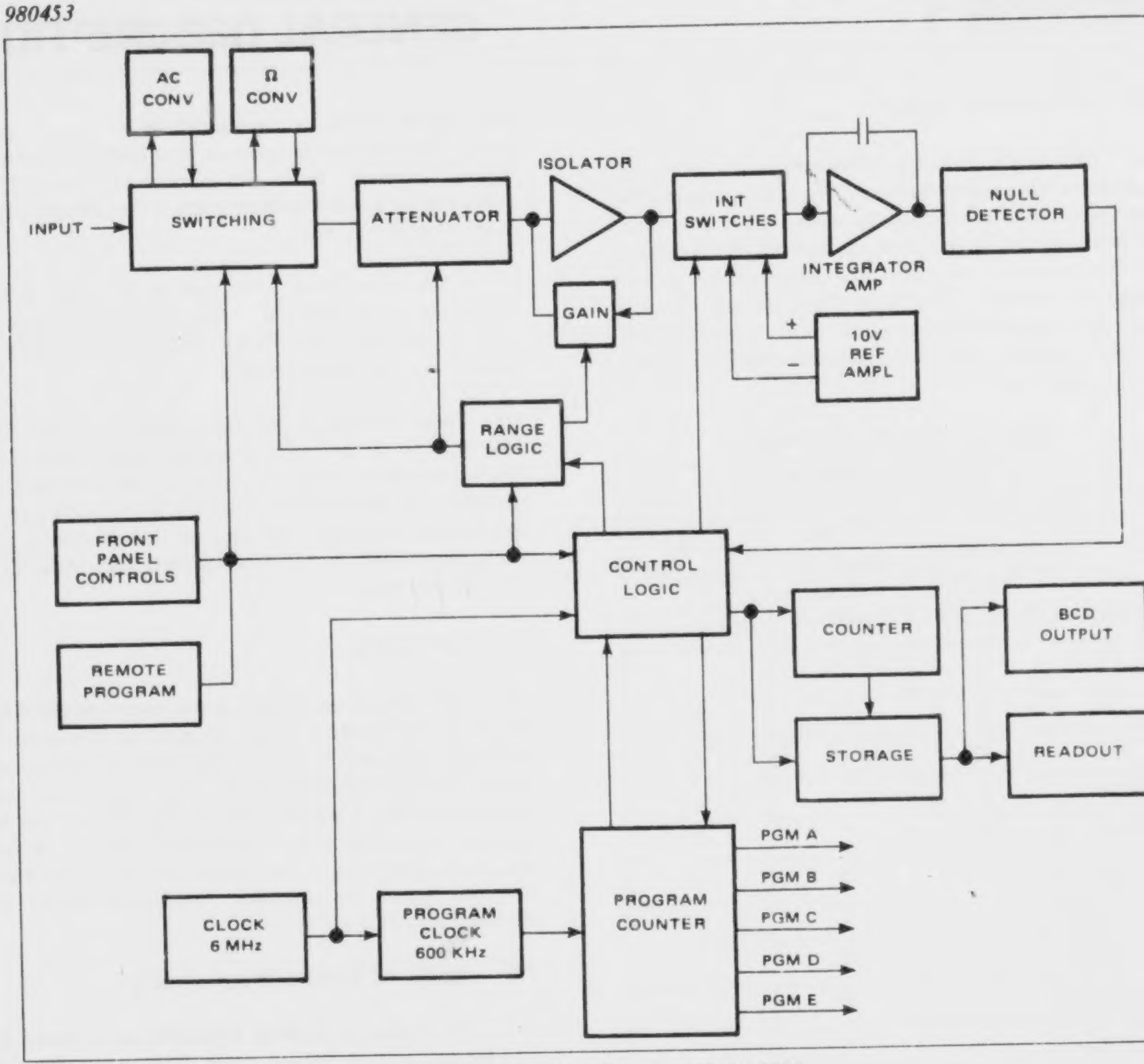

AC CONV
Ω CONV
INPUT
SWITCHING
ATTENUATOR
ISOLATOR
GAIN
INT SWITCHES
INTEGRATOR AMP
NULL DETECTOR
+
–
10V REF AMPL
RANGE LOGIC
FRONT PANEL CONTROLS
REMOTE PROGRAM
CONTROL LOGIC
COUNTER
STORAGE
BCD OUTPUT
READOUT
CLOCK 6 MHz
PROGRAM CLOCK 600 KHz
PROGRAM COUNTER
PGM A
PGM B
PGM C
PGM D
PGM E

Figure 1.1 - Block Diagram - Model 5900

1.13 Rack-Mounting Flanges (403402).

1.14 Rack-Mounting Flanges are used where the instrument is to be installed in a relay-rack or cabinet.

1.15 High-Voltage Probe (641).

1.16 The High-Voltage Probe extends the voltage range of the instrument up to 10,000 volts (or 7500V rms). It is an insulated probe containing a 1000:1 voltage divider.

1.17 Current Shunt Set (651).

1.18 The Current Shunt Set consists of six precision shunt assemblies with values selected to produce a voltage drop that, measured in millivolts, has a numerical value equal to the current flow in milliamps or microamps.

1.19 MODEL 55 GENERAL PURPOSE IN
FACE BUS.

1.20 The Model 55 Microprocessing GPIB enab
Dana Series 5000 and 5900 Digital Multimeters
Series 6900 Precision Voltmeters to be interface
other instrumentation and control devices utiliz
"standard interface bus" structure. The Model 55
processing GPIB is completely self-contained in
inch high, rack mountable, piggyback package.
compliant with IEEE Std. 488-1975, "IEEE S
Digital Interface for Programmable Instrumentatio
connectors and cabling to connect the Model 55 v
5900 are provided.

1.21 ELECTRICAL DESCRIPTION.

1.22 The Model 5900 DMM is a dual slope int
instrument consisting of three main functional area

conditioning, integrating, and control/display. A block diagram of the instrument is shown in figure 1.1.

1.23 The signal conditioning section includes the Switching p-c board, AC Converter, Ohms Converter, Attenuator, and Isolator. The function of these circuits is to convert the incoming signal to 10 VDC, full scale into the integrator.

1.24 The Integrating section consists of the Integrator amplifier, Null Detector and ± reference voltages. The function of these circuits is to convert the conditioned input signal to an equivalent time period and to transmit this time period to the display portion of the DMM.

1 Dual slope integration operates as follows in a sequence of program (PGM) states:

a. Signal Integration (PGM-A). The integrator capacitor charges to a voltage proportional to the input voltage during a 16-2/3 msec sampling period.

b. Reference Integrate (PGM-C). During this period, the integrator capacitor discharges at a constant current. The time that the integrator requires to discharge (full discharge detected by the Null Detector) is measured by the counter. The data in the counter at the end of PGM-C is proportional to the input voltage.

c. Strobe (PGM-D). At this time, data in the counter is strobed into the storage latches and displayed – the print pulse is inhibited, however if an uprange or downrange command is generated by the Autorange logic. If a range change is required, the counter is reset and the program returns to PGM-A.

d. Reset (PGM-E). At PGM-E, all internal logic is reset in preparation for the next reading.

1.26 An additional control state, PGM-B, occurs after PGM-A and is a delay to allow for propagation time of the counter.

1.27 The Control/Display section generates the control signals necessary to operate the signal conditioning and integrating circuits.

1.28 MECHANICAL DESCRIPTION.

1.29 The ohms measurement option consists of a single printed-circuit board. The AC options both consist of two boards. The accessory boards plus the Digitizer, Isolator, and Function Switching board all plug into the Main Logic board called the Logic and Interconnection assembly. This board also carries much of the instrument logic.

1.30 The Function Switching board is used only when either or both of the options (AC and Ohms) are installed. With no options installed, the Function Switching board is replaced with the Switching Bypass board. The Switching Bypass merely connects the + Input (from input connector) directly to the Isolator input and the – Input to ground.

1.31 At the rear edge of the Logic and Interconnection assembly is a PCB connector that extends to the rear panel and serves as the BCD output connector J201. If the optional Remote Program board is installed, it is mounted on stand-offs above the Logic and Interconnection board with the PROGRAM INPUT connector (J202) available at the rear panel above the BCD OUTPUT connector.

1.32 The POWER input connector J203, the power transformer, and power transistors for the power supply are mounted on the rear panel of the instrument. Other power supply components are mounted on the Logic and Interconnection assembly. Also mounted on the rear panel, in addition to J201 and J202, is the rear INPUT connector J204, the ANALOG OUTPUT connector and common, the EXTernal REFerence connector and common, and the line fuse F201.

1.33 A dimensional outline of the Model 5900 is shown in figure 1.2.

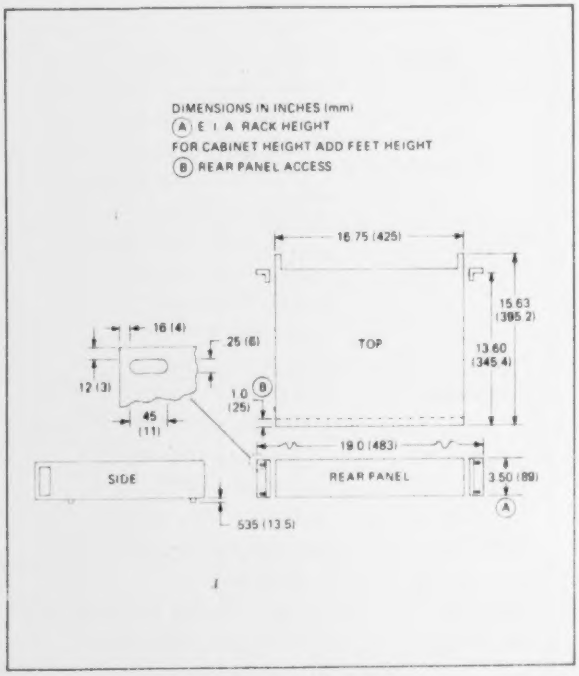


Figure 1.2 - Dimensions

1.34 SPECIFICATIONS.

1.35 Specifications are listed in table 1.2.

Table 1.2 - Specifications

GENERAL	
Conversion Technique:	Delayed Dual Slope
Signal Integration Time:	(60 Hz Standard) 16-2/3 msec 1-2/3 msec in Superfast
Reference Integration Time:	0 – 26-2/3 msec. 0 – 2-2/3 msec in Superfast
Polarity Selection:	Automatic
Ranging:	Autoranging is standard, upranges at 160% F.S. and downranges at 15% F.S.
Read Rate:	3-1/3 rdg per sec to hold
Maximum Common Mode Voltage:	1000V peak or DC Guard to case with logic common tied to case. 250V analog common to guard
Display:	5 full decades plus overrange digit (LED .4 inches high) plus decimal point and function annunciator
Overrange Indication:	Maximum reading is 160000 and "NO" annunciator is illuminated
Filter:	4-pole active
Analog Signal Output:	Signal input scaled and buffered 0 to ±16V. DC voltage is available for driving a recorder or as an accurate voltage source (Internal reference only)
Power Requirement:	100 - 240V (selectable) 50 - 60 Hz Standard 400 Hz Optional 40 watts maximum
Weight:	16 lbs. (7.3 Kg)
Dimensions:	3.5" x 14" x 16.75" (89 mm x 356 mm x 425 mm)
Temperature Range:	Operating 0°C - 50°C Storage –40°C - 70°C
Humidity Operating:	<75%; 25°C - 40°C <50%; 40°C - 50°C
Fan:	None required
Vibration, Operating:	0.025" double amplitude to 50 Hz for 15 minutes
F.S. on Full Scale:	Defined as "100000" reading on any range

DC VOLTS (Included in Basic Instrument)	
Ranges:	±.100000V. ±1.00000V. ±10.0000V. ±100.000V. ±1000.00V
Resolution:	0.001% of range 1 μV on 0.1V range
Overrange:	60% except 1000V range (1100 maximum input)
Maximum Input Voltage:	1100 VDC or 1400V peak AC on all ranges
Accuracy:	24 hours, 23°C ± 1°C (no adjustments) 10 Volt Range ±0.001% of full scale 1, 100, 1000 Volt Ranges ±(0.001% of reading +0.001% of full scale) .1 Volt Range ±(0.002% of reading +0.005% of full scale) 90 days, 23°C ± 5°C (After zeroing on 0.1V Range) 10 Volt Range ±(0.001% of reading +0.001% of full scale) 1, 100, 1000 Volt Ranges ±(0.002% of reading +0.001% of full scale) .1 Volt Range ±(0.003% of reading +0.005% of full scale) 1 year, 23°C ± 5°C (After zeroing on 0.1V Range) 10 Volt Range ±(0.003% of reading +0.001% of full scale) 1, 100, 1000 Volt Ranges ±(0.004% of reading +0.001% of full scale) 0.1 Volt Range ±(0.005% of reading +0.005% of full scale)
Temperature Coefficient:	0°C to 50°C 10 Volt Range ±(1.5 ppm of reading +.5 ppm of full scale)/°C 1, 100 Volt Range ±(2.5 ppm of reading +1 ppm of full scale)/°C 0.1 Volt Range ±(4 ppm of reading +5 ppm of full scale)/°C 1000 Volt Range ±(3 ppm of reading +1 ppm of full scale)

Table 1.2 - Specifications (continued)

DC VOLTS (continued)	
Non-Linearity:	0°C to 50°C All ranges: less than or equal to .5 digit non-linearity to 160% of full scale
Input Impedances:	0.1V-10V > 10,000 MΩ 100V-1000V > 10 MΩ
Input Bias Current:	10 pA at time of calibration (±100 pA for 30 days at 25°C)
Input Bias Current Temperature Coefficient:	±5 pA/°C
Normal Mode Rejection:	
Unfiltered	48 dB at every multiple of 60 Hz
Filtered	100 dB at 60 Hz
Common Mode Rejection:	(with 100Ω unbalance in either lead)
Unfiltered	140 dB at DC, 120 dB at 61 Hz and below
Filtered	140 dB DC to 61 Hz 126 dB 61 Hz to 100 kHz
Settling Time:	
Unfiltered	Settles to within 0.01% in 5 msec with 10 KΩ source, 10 msec on 100V range
Filtered	Settles to within 0.01% in 450 msec with 10 KΩ source

DATA OUTPUT (Included in the Basic Instrument)	
Information:	All BCD outputs of display, 4-bit Range code, function flags, polarity flags, special flags, and logic supplies
Logic Type:	Standard TTL levels, 8-4-2-1 BCD
Input Commands:	$\overline{\text{Superfast}}$ $\overline{\text{Data Disable}}$ $\overline{\text{System Direct Command}}$
Maximum Read Rate:	26 per sec to full scale, 120 per sec to full scale in Superfast, 60 Hz operation
Isolation:	Data output common may be floated up to 200V peak from power line common

AC VOLTS (Option 33 AC Converter, Averaging)	
Ranges:	1.00000V, 10.0000V, 100.000V, 1000.00V RMS
Resolution:	0.001% of range 10 μV on 1V range
Overrange:	60% except 1000V range (1000V RMS maximum input)
Maximum Input Voltage:	1000V RMS or 1400V peak AC decreasing to 20V RMS at 1 MHz. $2x10^7$V Hz maximum on any range
Accuracy:	24 hours, 23°C ± 1°C
$V_{in} \leq 500$V RMS	20 Hz to 30 Hz (filtered) ±(0.2% of reading +0.002% of full scale) 30 Hz to 60 Hz (filtered) ±(0.1% of reading +0.002% of full scale) 60 Hz to 100 Hz (filtered) ±(0.05% of reading +0.002% of full scale) 100 Hz to 5 kHz (filtered) ±(0.02% of reading +0.002% of full scale) 100 Hz to 300 Hz (no filter) ±(0.1% of reading +0.002% of full scale) 300 Hz to 5 kHz (no filter) ±(0.02% of reading +0.002% of full scale) 5 kHz to 100 kHz (both) ±(.04% of reading +0.005% of full scale) 100 kHz to 300 kHz (both) ±(0.1% of reading +0.01% of full scale) 300 kHz to 1 MHz (both) ±(0.5% of reading +0.05% of full scale) 90 days, 23°C ± 5°C add ±0.01% of reading to 24 hour specification 6 months, 23°C ± 5°C add ±0.02% of reading to 24 hour specification 1 year, 23°C ± 5°C add ±0.03% of reading to 24 hour specification
$V_{in} > 500$V RMS	f < 5 kHz Add ± .1% of reading f ≥ 5 kHz Add ± .2% of reading

AC VOLTS continued on page 1-6

Table 1.2 - Specifications (continued)

AC VOLTS (continued)	
Temperature Coefficient:	0°C to 50°C 50 Hz to 20 kHz (filtered) ±(0.003% of reading +0.0005% of full scale)/°C 20 kHz to 100 kHz (both) ±(0.005% of reading +0.002% of full scale)/°C 100 kHz to 1 MHz (both) ±(0.01% of reading +0.01% of full scale)/°C
Input Impedances:	1 MΩ ± 0.1% in series with 0.22 μF shunted by less than 100 pF to common
Common Mode Rejection:	(with 100Ω unbalance in either lead, DC to 60 Hz) 1 Volt Range 120 dB 10 Volt Range 100 dB 100 Volt Range 80 dB 1000 Volt Range 60 dB
Settling Time:	
Unfiltered	Settles to within rated accuracy in 200 msec
Filtered	Settles to within rated accuracy in 600 msec
(Zero to full scale or full scale to 10% of full scale)	

REMOTE PROGRAMMING (Option 42)	
Input Information:	Provides isolated programming of all DMM functions and ranges
Logic Type:	Standard TTL levels, 8-4-2-1 BCD
Functions:	Function lines are programmed by a closure to ground (low TTL level)
Ranges:	Range lines are 8-4-2-1 coded positive true logic
Command Modes:	Direct Command Timeout Command Superfast

OHMS (Option 52)	
Ranges:	10.0000Ω, .100000 KΩ, 1.00000 KΩ, 10.0000 KΩ, 100.000 KΩ, 1000.00 KΩ, 10.0000 MΩ, 100.000 MΩ
Resolution:	0.001% of range 100 μΩ on 10Ω range
Overrange:	60% on all ranges
Maximum Input Voltage:	±500V peak on all ranges
Measurement Scheme:	True 4-wire
Accuracy:	24 hours, 23°C ± 1°C 10 ohm Range ±(0.003% of reading +0.005% of full scale) .1, 1, 10, 100, 1000 kilohm Range ±(0.002% of reading +0.001% of full scale) 10 megohm Range ±(0.01% of reading +0.001% of full scale) 100 megohm Range ±(0.02% of reading +0.001% of full scale) 90 days, 23°C ± 5°C 10 ohm Range ±(0.005% of reading +0.005% of full scale) .1, 1, 10, 100, 1000 kilohm Range ±(0.003% of reading +0.001% of full scale) 10 megohm Range ±(0.03% of reading +0.001% of full scale) 100 megohm Range ±(0.03% of reading +0.001% of full scale)
Temperature Coefficient:	0°C to 50°C 10 ohm Range ±(8 ppm of reading +5 ppm of full scale)/°C .1, 1 kilohm Ranges ±(7 ppm of reading +1 ppm of full scale)/°C 10, 100, 1000 kilohm Ranges ±(6 ppm of reading +1 ppm of full scale)/°C 10 megohm Range ±(30 ppm of reading +1 ppm of full scale)/°C 100 megohm Range ±(50 ppm of reading +3 ppm of full scale)/°C

Table 1.2 - Specifications (continued)

OHMS (continued)	
Open Circuit Voltage:	10Ω-1 KΩ 2.5V maximum 10 KΩ-100 MΩ 22V maximum
Current Through Unknown:	10 ohm Range 10.1 mA 100 ohm Range 11.1111 mA 1 kilohm Range 1.11111 mA 10 kilohm Range 1 mA 100 kilohm Range 100 μA 1000 kilohm Range 10 μA 10 megohm Range 1 μA 100 megohm Range 100 nA
Normal Mode Rejection:	
Unfiltered	48 dB at every multiple of 60 Hz
Filtered	100 dB at 60 Hz
Settling Time: to rated accuracy	10Ω-10 MΩ 30 msec 100 MΩ 300 msec
Ohms Guard:	Use "Analog-Out" low on rear panel when in ohms

SUPERFAST	
Ranges & Functions:	All ranges and functions
Command:	Must be commanded through rear printer connector (J201), or through programmer option (J202).
Display:	Four digit display with 60% overrange
Maximum Read Rate:	(60 Hz standard) 98 readings/sec. minimum at full overrange. 120 readings/sec. at full scale
Accuracy:	Standard accuracy for range and function plus .03% of reading plus .03% of full scale

3-WIRE RATIO (Included in Basic Instrument)	
3-Wire (Standard)	ac/dc, dc/dc
Readout:	$\frac{(10V)}{(Ref\ V)} \times \frac{(Input)}{(F.S.\ of\ range)} \times 10$
Reference Voltage Range:	+1V to +10.5V
Maximum Input Voltage:	Same as function
Accuracy:	24 hours, 23°C ± 1°C DC, AC – same as respective 24 hour, 23°C ± 1°C spec. times $\frac{(10V)}{(Ref\ V)}$
Reference Input Impedance:	1000 MΩ
Reference Settling Time:	10 msec to 0.01% accuracy

4-WIRE RATIO (Option 62)	
4-Wire (Option 62)	ac/dc, dc/dc
Readout:	$\frac{(10V)}{(Ref\ V)} \times \frac{(Input)}{(F.S.\ of\ range)} \times 10$
Reference Voltage Range:	+1V to +10.5V
Maximum Input Voltage:	Same as function
Accuracy:	24 hours, 23°C ± 1°C DC, AC – same as respective 24 hour, 23°C ±1°C spec. times $\frac{(10V)}{(Ref\ V)} \times 2$
Reference Input Impedance:	(Both leads with respect to –sig. input) 1000 MΩ min. + lead 3.3x10 MΩ ± 10% – lead
Reference Input to Sig. Common Voltage:	±15V max. (either input)
Reference Settling Time:	50 msec to 0.01% of rated accuracy
Reference Common Mode Rejection:	100 dB

Table 1.2 - **Specifications** (continued)

AC VOLTS TRUE RMS (Option 32)	
Ranges:	1.00000V, 10.0000V, 100.000V, 1000.00V RMS
Resolution:	0.001% of range 10 μV on 1V range
Overrange:	60%, (within maximum input voltage limit)
Maximum Input Voltage:	1000V DC/RMS or 1500V peak, decreasing to 20V RMS at 1 MHz. 2x10^7 VHz maximum on any range
Maximum Crest Factor: **(Pulses at 1 kHz Repetition Rate)**	7:1 at full-scale $7 \times \sqrt{\frac{F.S.}{V_{input}}}$ for other voltages
Accuracy: AC Mode NOTE Accuracy is defined only for values between .1% and 160% of full scale (full scale specified as 100,000 counts).	90 days, 23°C ± 5°C 20 Hz – 30 Hz (filtered) ±(.5% of reading +.04% of full scale) 30 Hz – 50 Hz (filtered) ±(.2% of reading +.04% of full scale) 50 Hz to 100 Hz (filtered) ±(.1% of reading +.04% of full scale) 100 Hz – 20 kHz (filtered) ±(.07% of reading +.04% of full scale) 200 Hz to 20 kHz (no filter) ±(.07% of reading +.04% of full scale) 20 kHz – 50 kHz (both) ±(.1% of reading +.1% of full scale) 50 kHz – 100 kHz (both) ±(.4% of reading +.2% of full scale) 10, 100, 1000V range 100 kHz – 300 kHz ±(3% of reading +.5% of full scale) 1V range 100 kHz – 300 kHz ±(5% of reading +1% of full scale)

Accuracy: *(con't)*	
AC Mode (continued)	6 months, 23° ± 5°C 1, 10, 100, 1000V ranges 20 Hz – 50 kHz Add ±.01% reading ± .01% full scale to the 90 day spe 50 kHz – 100 kHz Add ±.02% reading ±.02% full scale to the 90 day spe 100 kHz – 300 kHz Add ±1% reading ± .1% ful scale to the 90 day spec.
DC Mode	Add ±.02% of full scale to AC specification
For inputs greater than 500V add .1% of reading to above spec For inputs greater than 150V, Filter should be "IN".	
Temperature Coefficient:	0°C to 50°C AC Mode (to 20 kHz) ±(.004% of reading +.003% of full scale)/°C DC Mode (to 20 kHz) ±(.004% of reading +.005% of full scale)/°C
Input Impedances: AC Mode	1 Megohm ±0.1% in series with 0.22 μF shunted by less than 100 pF to common
In dc mode, the .22 μF capacitor is shorted	
Common Mode Rejection: With 100Ω unbalance in either lead. DC to 60 Hz	Range — CMR 1V — 120 dB 10V — 100 dB 100V — 80 dB 1000V — 60 dB
Settling Time:	(to within .1% of range)
	0 to Full Scale Step
Filter Out	80 msec
Filter In	350 msec
	Full Scale to 10% FS Step
Filter Out	100 msec
Filter In	400 msec

Table 1.2 - Specifications (continued)

50 Hz OPERATION (Option 04)	
For instruments modified for operation at 50 Hz line frequency, the specifications are the same as 60 Hz operation except as follows:	
Signal Integration Time:	20 msec 2 msec in Superfast
Reference Integration Time:	0 - 32 msec 0 - 3.2 msec in Superfast
Normal Mode Rejection:	
Unfiltered	48 dB at every multiple of 50 Hz
Filtered	100 dB at 50 Hz

Table 1.3 - Model 55 GPIB Specifications

MODEL 55 GPIB SPECIFICATIONS	
Output Information:	Numeric data, polarity, function, and special flags
Data Output Format:	DC, AC, Ratio: same as front panel display with no leading zero suppression. Ohms: readout expressed as kohms
Input Information:	Functions, ranges, and filter
Command Modes:	Direct Command Timeout Command Superfast
Electrical Compatibility:	IEEE Std. 488-1975
Information Format:	7-bit ASCII code
Power Requirement:	20 Watts maximum
Line Voltage:	100/120/220/240V ±10% 48 – 440 Hz
Weight:	3.2 Kg (7 lb.)
Dimensions:	44mm x 356mm x 425mm (1.75" x 14" x 16.75")

SECTION 2 INSTALLATION & OPERATION

2.1 UNPACKING AND INSPECTION.

2.2 The Model 5900 DMM is packed in a molded plastic-foam form within a cardboard carton for shipment. The plastic form holds the DMM securely in the carton and absorbs any reasonable external shock normally encountered in transit. Prior to unpacking, examine the exterior of the shipping carton for any signs of damage. Carefully remove the DMM from the carton and inspect the exterior of the instrument for any signs of damage. If damage is found, notify the carrier immediately.

2.3 Included with the instrument in the packing container are the instruction manual, power cord, and rear input and BCD output mating connectors. With instruments equipped with remote programming, a mating connector for that accessory is included.

2.4 BENCH OPERATION.

2.5 Each Model 5900 is equipped with a tilt bail or "kickstand" to enable the front of the instrument to be elevated for convenient bench use. The tilt bail is attached to the two front supporting "feet" at the bottom of the instrument. For use, the bail is pulled down to its supporting position.

2.6 RACK MOUNTING.

2.7 The instrument can be mounted in a standard 19-inch rack with the optional rack-mounting flanges (403402, includes attaching hardware). To install the flanges, proceed as follows:

a. With instrument on its side, remove four Phillips-head screws holding bottom cover. Remove cover. Remove screws holding feet (and bail) in place. Replace bottom cover.

b. Place one of the supplied screws through each of the two holes in the mounting flange (figure 2.1). Thread a securing nut onto each screw just enough to attach it to the screw (approximately one turn).

c. Place the mounting flange onto the mounting slot in the instrument side panel so that the securing nuts fit entirely into the slot. Be sure the rack-mount slots on the flange are toward the front of the instrument.

d. Tighten screws. The securing nuts will rotate and hold the flange securely in place.

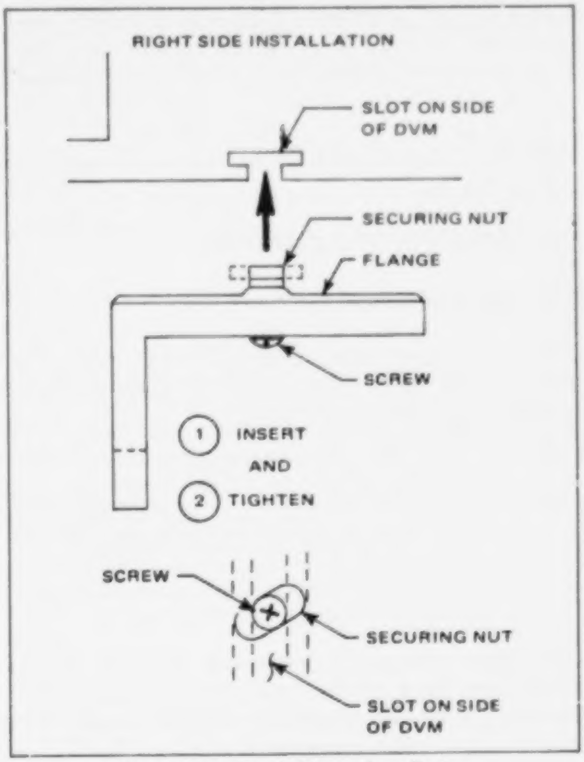


Figure 2.1 - Rack Mount Installation

2.8 POWER CONNECTIONS.

2.9 Standard units operate on either 115 volts or 230 volts, 50 to 60 Hz (400 Hz available). Power consumption is less than 40 watts. Operation on either of the two line voltages is selectable by a slide switch on the rear panel. Operation on 100/200 volts or 120/240 volts is possible by simple rewiring of the power transformer secondary wires:

WARNING

Disconnect the instrument from the AC Power source before attempting to change power connections. Potentially lethal voltages are exposed when covers are removed.

a. For operation on 100/200 volts, cut the brown wire 1" from the transformer and splice it to the red wire on the transformer; cut the blue wire 1" from the transformer and splice it to the violet wire.

b. For operation on 120/240 volts, cut the brown wire 1" from the transformer and splice it to the black wire; cut the blue wire and splice it to the yellow wire on the transformer.

2.10 A standard power cable having a three-pin plug is supplied with the instrument. It connects to POWER connector J203. The ground pin (round) is attached to the instrument case. It is important that this pin be connected to a good quality earth ground.

2.11 Fuse receptacle F201 on the rear panel is equipped with a .5 amp fuse.

2.12 INPUT/OUTPUT CABLING.

2.13 Binding Posts.

2.14 Several connectors on the Model 5900 consist of a pair of binding posts spaced so as to accept standard "banana" plugs. The connectors are:

Front Panel	Rear Panel
± INPUT	± ANALOG OUTPUT
± OHMS CURRENT	± REFerence INput
GUARD	

2.15 Input cables to fit this type of connector can be ordered from Dana (P/N 402190). Figure 2.2 is a wiring diagram of this cable included for assistance to users desiring to construct their own cables.

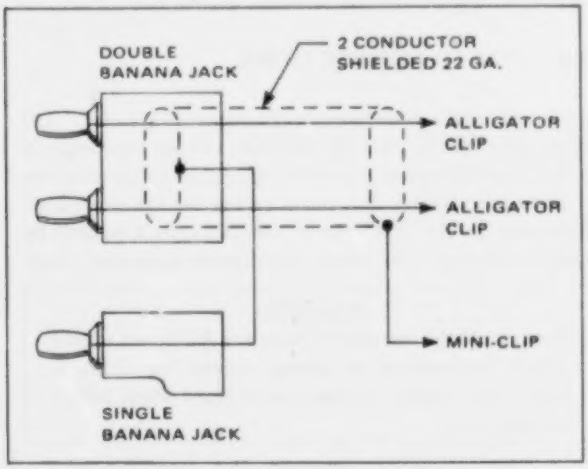


Figure 2.2 - Cabling Diagram, Binding Posts

2.16 Rear Input Connector.

2.17 Instruments equipped with the -1 or -1S rear input option are supplied with J204 7-pin input connector (Dana P/N 600673) and a mating connector (Dana P/N 600616). On instruments equipped with -1B or -1SB J204 is Dana P/N 600259 and the mating connector is Dana P/N 600260. The instrument accepts inputs applied to this connector or inputs applied to the front-panel binding posts. The rear-panel input lines are wired in parallel with the front-panel input lines. It is recommended that the cable for the mating connector be constructed as shown in figure 2.3 using two two-conductor shielded cables. Other configurations may be desirable depending on the ohms measuring method to be used (see paragraph 2.28).

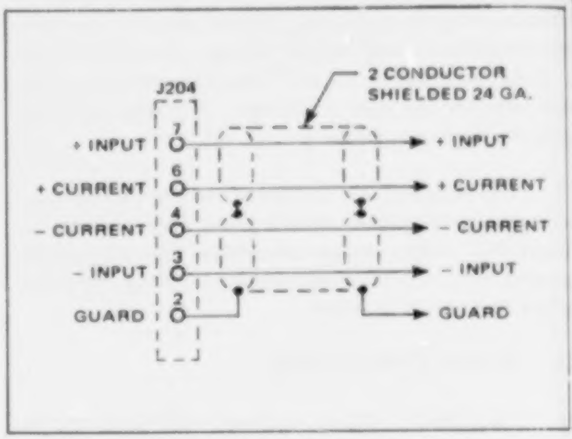


Figure 2.3 - Cabling Diagram, Rear Input

2.18 MANUAL OPERATION.

2.19 Controls.

2.20 All operating controls are located on the front panel of the instrument. They are shown in figure 2.5 and their operation described in table 2.1. Description of Systems operation begins in paragraph 2.44.

2.21 DISPLAY.

2.22 The display consists of 6 LED decimal readout devices with moving decimal point. The decimal point moves in conjunction with the range switch or automatically in auto range. Maximum usable readout with overrange is 159999. Overload is indicated by a NO and 160000 readout. A non-compatible range and function is indicated by a NO. However, mechanical interlocks are provided to prevent illegal combinations from the front panel. Figure 2.4 illustrates the readout, NO indicator, polarity sign and the annunciator.

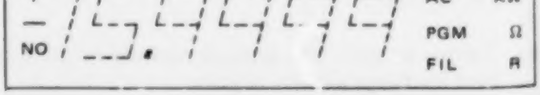


Figure 2.4 - Readout

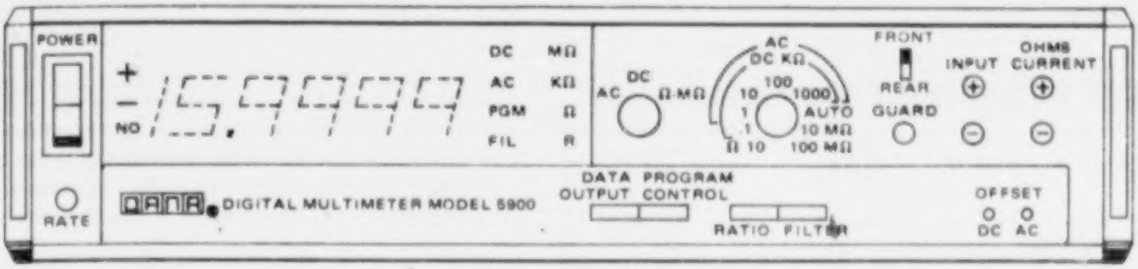


Figure 2.5 - Front Panel

Table 2.1 - Operating Controls

Control	Position	Function
Power (rocker switch)	ON (up)	Applies power to instrument
	OFF (down)	Removes power from instrument
Function Select (rotary switch)	AC†	Selects the measurement of AC voltages on the 1, 10, 100, and 1000 volt ranges (max. input, 1000V RMS)
	DC	Selects the measurement of DC voltages on the .1, 1, 10, 100, and 1000 volt ranges (max. input, 1100V)
	ΩMΩ*	Selects the measurement of resistance on the 10 ohm range; on the .1, 1, 10, 100, and 1000 kilohms ranges; or on the 10 or 100 Megohm ranges
Range Select (rotary switch)	AUTO	Selects Auto Range in which the optimum range is selected automatically by internal circuits. Uprange occurs at 160% of full scale; downrange occurs at 15% of full scale
	Other Positions	Enables manual selection of fixed ranges. Ranges permissible for each function are inscribed on the panel
DATA OUTPUT (pushbutton)	Depressed	Enables the print pulse causing BCD data at P201 to be recorded by printer, or other output device. (Output data is present at P201 regardless of the position of this switch.)
PROGRAM CONTROL (pushbutton)	Depressed	Enables the selection of range, function, and mode to be made externally through the remote programming connector and disables all front panel controls (requires programming option)
RATIO (pushbutton)	Depressed	Selects a ratio measurement in which the readout represents the ratio of the input to an external d-c reference voltage (applied at terminals on the rear panel) multiplied by 10: $E_{in}/E_{Ref} \times 10$
FILTER (pushbutton)	Depressed	Adds an active four-pole filter across the input circuit
RATE (pot)	EXT (ccw)	Selects the Hold mode. A new reading is initiated through the remote program input
	CW	Increase periodic read rate to a maximum of four readings/second
FRONT/REAR (Optional) (slide switch)	FRONT	Connects front panel data input terminals to instrument
	REAR	Connects rear panel data input terminals to instrument

*NOTE: Ohms input terminals are open in AC or DC function.

†NOTE: For inputs greater than 150V, Filter should be "IN".

2.23 MEASUREMENT CONNECTIONS.

NOTE

Before taking any measurements, refer to the list of maximum input voltages, table 2.2.

Table 2.2 - Maximum Input Voltage

CAUTION

Do not exceed the following maximum inputs:

DC	1100 VDC or 1000V RMS AC
AC	1000V RMS decreasing to 20V RMS at 1 MHz (see figure 2.7)
Ω	±500V peak between +I and –I (1000V RMS if in DC or AC function.)
RATIO	Input: same as function selected Reference: +10.5V, –0.5V
GUARD	Voltage between GUARD and – INPUT must not exceed 250 volts or damage to the instrument may result

2.24 Basic Voltage Measurement.

2.25 An ac or dc voltage measurement connection recommended to minimize the effects of noise requires a two-conductor shielded cable connected as shown in figure 2.6.

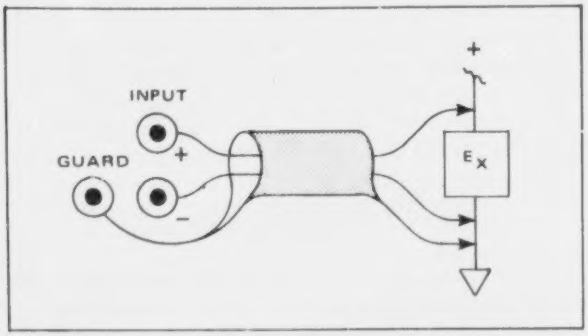


Figure 2.6 - Basic Voltage Measurement Connections

2.26 For all voltage measurements, the GUARD lead and the – INPUT lead are connected to the measurement point nearest ground potential. Somewhat less shielding is achieved by placing a shorting bar between – INPUT GUARD and shorting the single banana plug (shield) to – INPUT side of the double banana plug at the input c nector. This arrangement is adequate for measuring all low voltage (mV) levels and/or in high-noise environme

2.27 When making "floating" voltage measurements (b measurement points above ground potential), do not c nect GUARD to measurement ground without making s that the voltage between GUARD and – INPUT does exceed 250 volts.

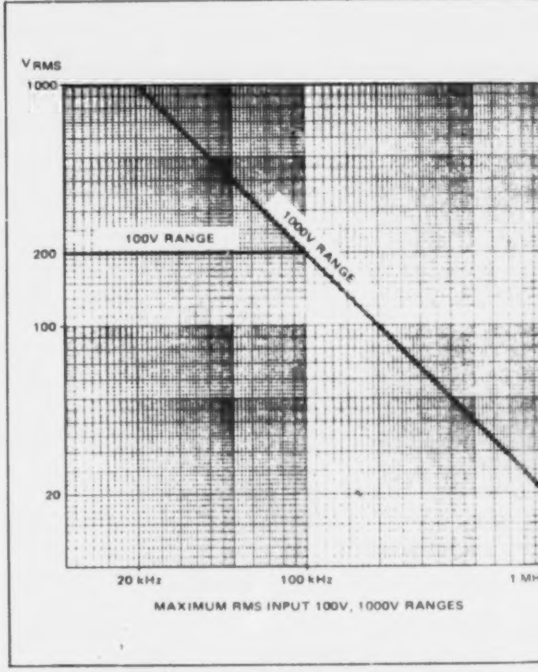


Figure 2.7 - Maximum RMS Input Voltage

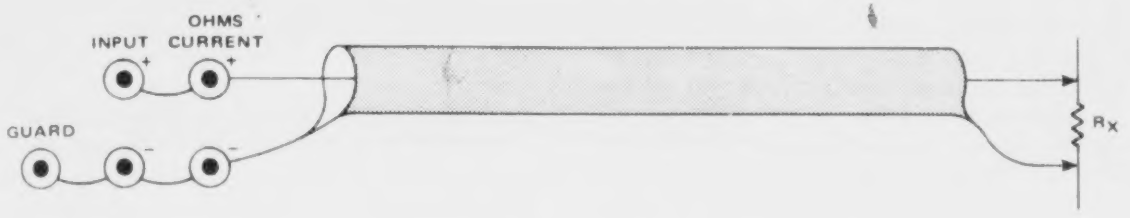
2.28 Ohms Measurement.

2.29 Ohms measurement in the Model 5900 consist the application of a known current through the unkn resistance (R_X) and measuring the ratio of the voltage c across R_X to the drop across an internal "full-scale" resi (E_{R_X}/E_{FS}). Current through R_X is applied through l from the ± OHMS CURRENT terminals. The voltage c is sensed by the ± INPUT terminals.

2.30 TWO-WIRE MEASUREMENTS.

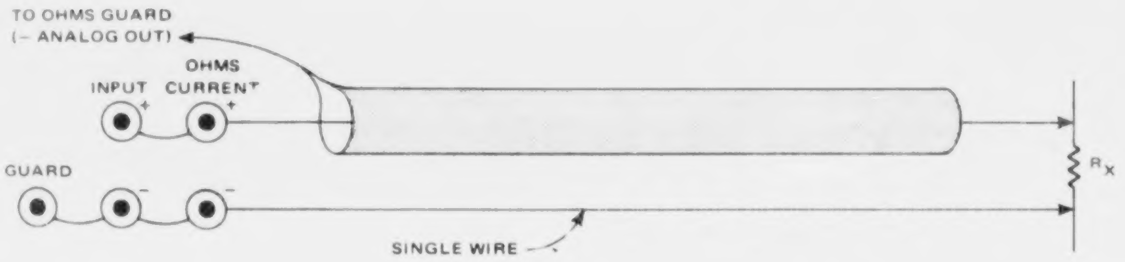
2.31 Connections for a simple two-wire shielded o measurement are shown in figure 2.8a. It consists simpl a single-conductor shielded cable with the conductor ser as both the + CURRENT and + INPUT leads and the sh

(a)


INPUT
OHMS CURRENT
+
+
GUARD
−
−
R_X

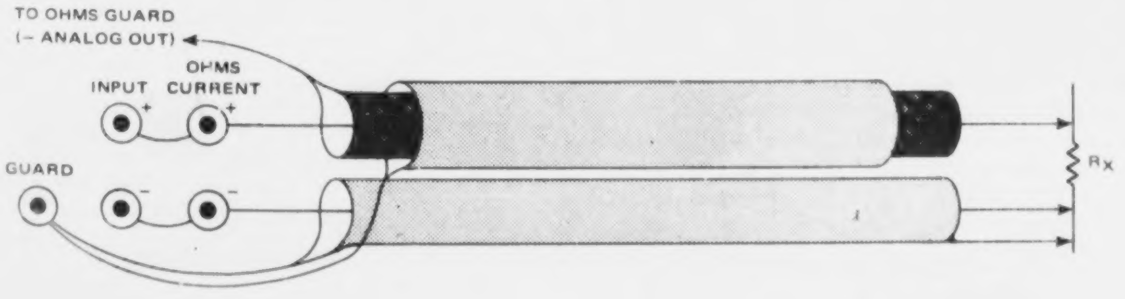
USED FOR NORMAL MEASUREMENTS ON ALL BUT THE 10 and 100Ω RANGES.

(b)


TO OHMS GUARD
(− ANALOG OUT)
INPUT
OHMS CURRENT
+
+
GUARD
−
−
SINGLE WIRE
R_X

USED ON 100 MΩ RANGE WHERE CABLE LEAKAGE IS A PROBLEM.

(c)


TO OHMS GUARD
(− ANALOG OUT)
INPUT
OHMS CURRENT
+
+
GUARD
−
−
R_X

USED ON 100 MΩ RANGE IN HIGH-NOISE ENVIRONMENT; MINIMIZES BOTH NOISE AND LEAKAGE PROBLEMS.

Figure 2.8 - Two Wire Ohms Measurements

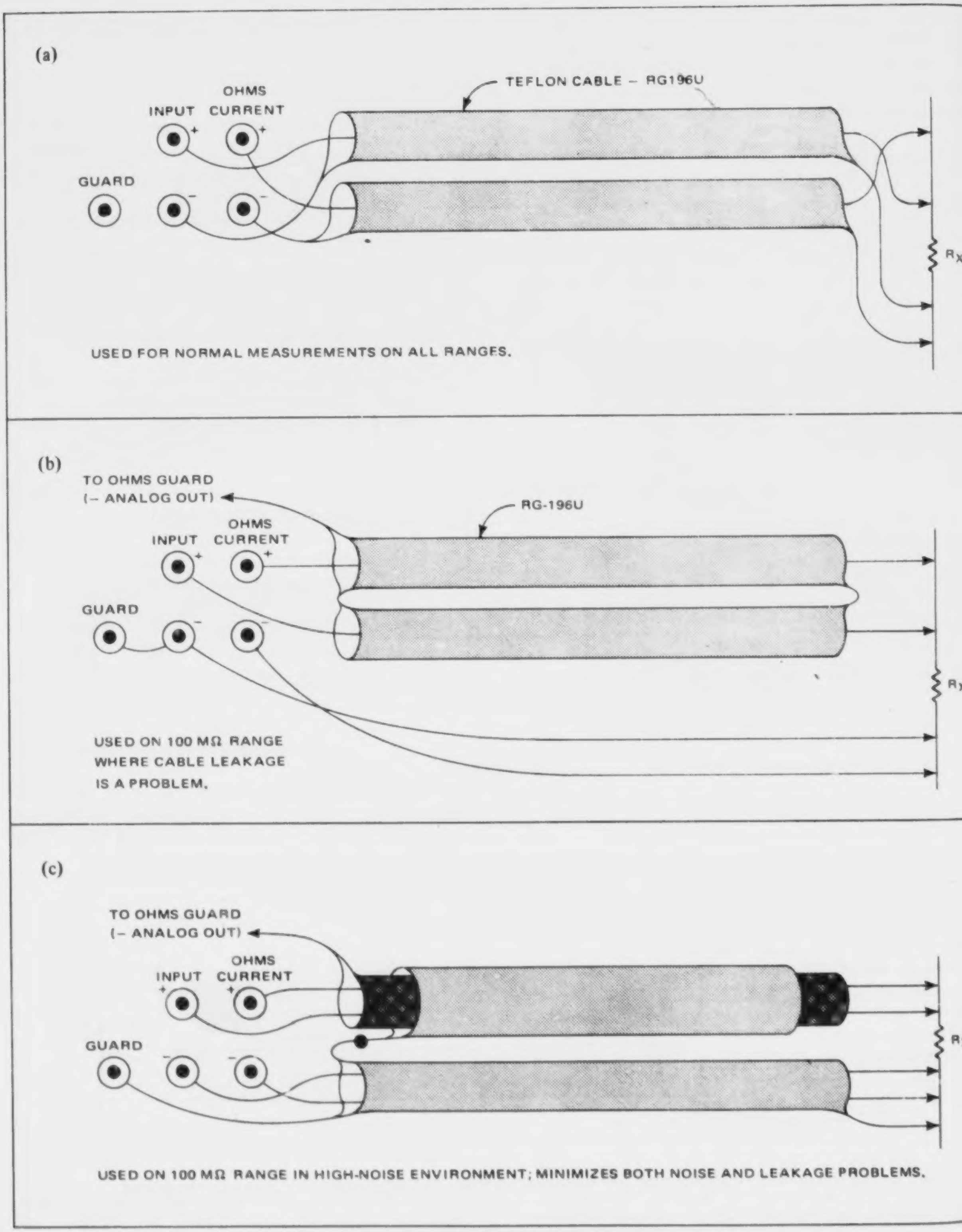

(a)
TEFLON CABLE – RG196U
INPUT
OHMS
CURRENT
+
+
GUARD
–
–
USED FOR NORMAL MEASUREMENTS ON ALL RANGES.
(b)
TO OHMS GUARD
(– ANALOG OUT)
RG-196U
INPUT
OHMS
CURRENT
+
+
GUARD
–
–
USED ON 100 MΩ RANGE
WHERE CABLE LEAKAGE
IS A PROBLEM.
(c)
TO OHMS GUARD
(– ANALOG OUT)
INPUT
OHMS
CURRENT
+
+
GUARD
–
–
USED ON 100 MΩ RANGE IN HIGH-NOISE ENVIRONMENT; MINIMIZES BOTH NOISE AND LEAKAGE PROBLEMS.

Figure 2.9 - Four Wire Ohms Measurement

carrying – CURRENT and – INPUT. While reasonably accurate measurements can be made with this method, shunt leakage problems result from the parallel combinations of R_X and the cable impedance. This causes loss of accuracy, especially at high resistance (100 MΩ range). Also, lead resistance becomes a factor in the 10 and 100 ohms ranges; the four wire measurement system is recommended for these ranges.

2.32 A more accurate two-wire measurement connection is shown in figure 2.8b. The + INPUT and + CURRENT, – INPUT and – CURRENT terminals are again tied together. But now, the positive side is a single-conductor, shielded cable with the shield tied to Ohms Guard. Ohms Guard is the low ANALOG OUTPUT terminal on the rear panel of the Model 5900 when ohms is selected. The negative side is a single wire connected as shown. Guard current is present in the low side, but the leakage problems of the first configuration are eliminated.

2.33 In high noise-level environments, the configuration shown in figure 2.8c is recommended. This method also eliminates error due to shunt leakage, but provides more complete shielding. The positive terminals are tied together and carried in a single-conductor, double-shielded cable with the inner shield tied to Ohms Guard (– ANALOG OUTPUT). The outer shield is tied to GUARD. The negative terminals are tied together and carried in a single-conductor shielded cable with the shield tied to GUARD. This configuration eliminates guard current sensitivity, thereby increasing guarding characteristics.

2.34 FOUR-WIRE MEASUREMENTS.

2.35 In most system applications, the device to be measured is located at a remote location requiring interconnection by cables of lengths from several to possibly hundreds of feet. When measuring low resistance values over long cables, most lead resistance problems can be solved by the use of a four-wire measurement system.

2.36 For high resistance measurements over long cables, other problems are encountered: noise pick-up, leakage resistance, and capacitive loading of the system. These problems can be minimized by proper shielding and the use of ohms guard.

2.37 Figure 2.9a shows a basic shielded four-wire ohms measurement configuration. This method uses two single-conductor shielded teflon cables. The conductors carry the positive sides of the INPUT and CURRENT lines while each shield carries the low side.

2.38 This configuration, although shielded, places the shield capacitance and cable leakage in parallel with R_X. This results in loss of accuracy and slow measurements. In addition, it is very responsive to the triboelectric effect at high resistance measurements.

2.39 Better guarding is achieved by the use of the configuration shown in figure 2.9b. Here again, RG196U teflon dielectric cable (either single-conductor shielded or two-conductor shielded) is used on the positive terminals. The shield(s) are connected to Ohms Guard (low ANALOG OUTPUT terminal). The negative leads are single wires with the – INPUT terminal tied to GUARD.

2.40 This eliminates much of the shunt leakage problem of the previous configuration since guard current now flows through the low side of the measurement circuit. Measurement is much faster since the shield capacity is driven by the guard current.

2.41 A high-noise environment calls for the "super" configuration shown in figure 2.9c. Here, a two-conductor, double-shielded cable is used as the positive leads. The inner shield is tied to Ohms Guard. A two-conductor shielded cable is used as the negative leads. Its shield is tied to GUARD and to the outer shield of the positive cable. The shield is also tied to – CURRENT at the measurement point. This configuration maintains high guarding characteristics while eliminating guard current sensitivity.

2.42 Ratio Measurements.

2.43 Ratio measurements are made by applying a positive d-c voltage to the reference input terminals on the rear panel and an input signal of any function at the front input terminals. For DC/DC or AC/DC ratios, the reference voltage must be within the range of +1V to +10.5V. Input signal limitations (numerator) are the same as those given for conventional measurement of the particular function (table 2.2). The readout is the ratio multiplied by ten: $E_{input}/E_{reference} \times 10$. In the standard instrument the – INPUT terminal is internally connected to the – REF input terminal; in instruments equipped with the option 62 4-wire ratio, both reference inputs are floating (33 MΩ between – REF and – SIGNAL). For bridge applications the 33 MΩ resistor (R16) may be removed to eliminate loading errors.

2.44 SYSTEM CAPABILITIES.

2.45 The 5900 has two system interface connectors designated as J201 (PRINTER OUTPUT) and J202 (PROGRAM INPUT) mounted on the rear panel of the instrument (figure 2.10). The following is a brief description of the capabilities of each connector.

2.46 Printer Output - J201.

2.47 Through this connector the 5900 supplies BCD representations of the decimal display; various flags or indicators of the mode of operation, function and range; and a print command. Provision has also been made for 60 Hz instruments to accept a fast (20 readings per second maximum) or a superfast (101 readings per second minimum) read command. In 50 Hz units, the fast command obtains 17 readings per second, minimum, and the superfast command 93 readings per second.

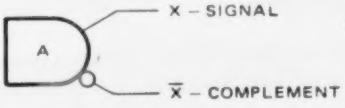
2.48 Program Input.

2.49 Through this connector the 5900 receives externally generated signals that select the function, range mode of operation, and initiate the read commands.

2.50 LOGIC LEVELS AND ELECTRONIC INTERFACE.

2.51 TTL-compatible positive-true logic levels are used in the 5900. In some instances, however, complementary signals are used. These terms are more specifically defined below:

Signals and Their Complements –



WHERE:

X	$\overline{X}$
1	0
0	1

2.52 If the non-inverting output of gate A is defined as signal X, then it follows that the inverting output is $\overline{X}$; in other words, the complement of X is $\overline{X}$. The truth tab shows that the two signals X and $\overline{X}$, are by definition, opposite logic states (see table 2.3).

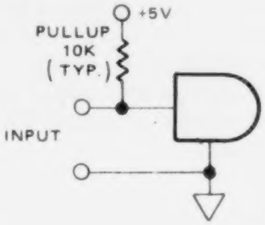
Table 2.3 - Positive True Logic Relationships

Signal	Logic State	Voltage Level of Output Line "X"	Voltage Level of Output Line "$\overline{X}$"
"X"	True or "1"	2.4 - 5.0 Volts	0.0 - 0.4
	False or "0"	0.0 - 0.4 Volts	2.4 - 5.0

2.53 As seen above, if gate A has a true or "1" level o output X, its voltage level is the most positive of the tw ranges present, and output $\overline{X}$ must be in a false or "0 state with the lowest or most negative voltage range prese The reverse would be true for a false or "0" level on outp X.

2.54 Driving the Inputs.

2.55 All inputs are TTL compatible and most are th equivalent of one 7400 series TTL input with a pull-u resistor for contact closure operation.



2.56 TTL Loading Conditions.

2.57 To input a "1" level the pull-up resistor will supp the necessary source current (40 μA) to maintain the min mum 2.4 volts. In fact, the pull-up resistor will maintain

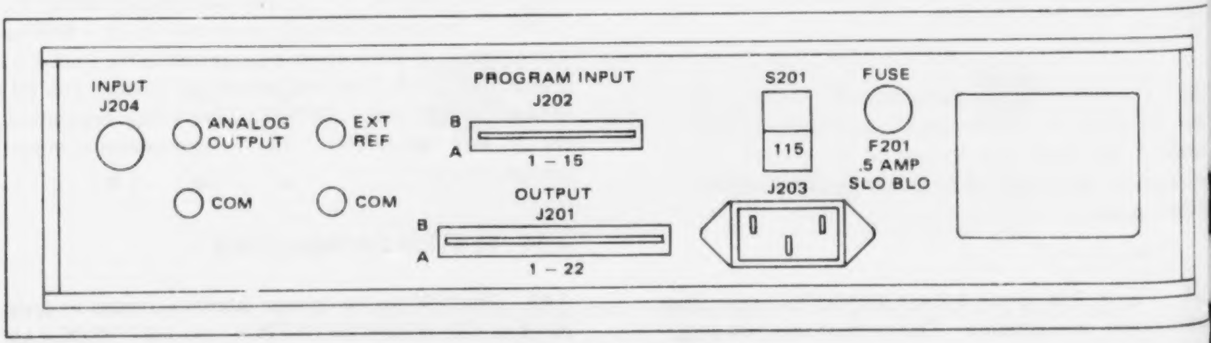
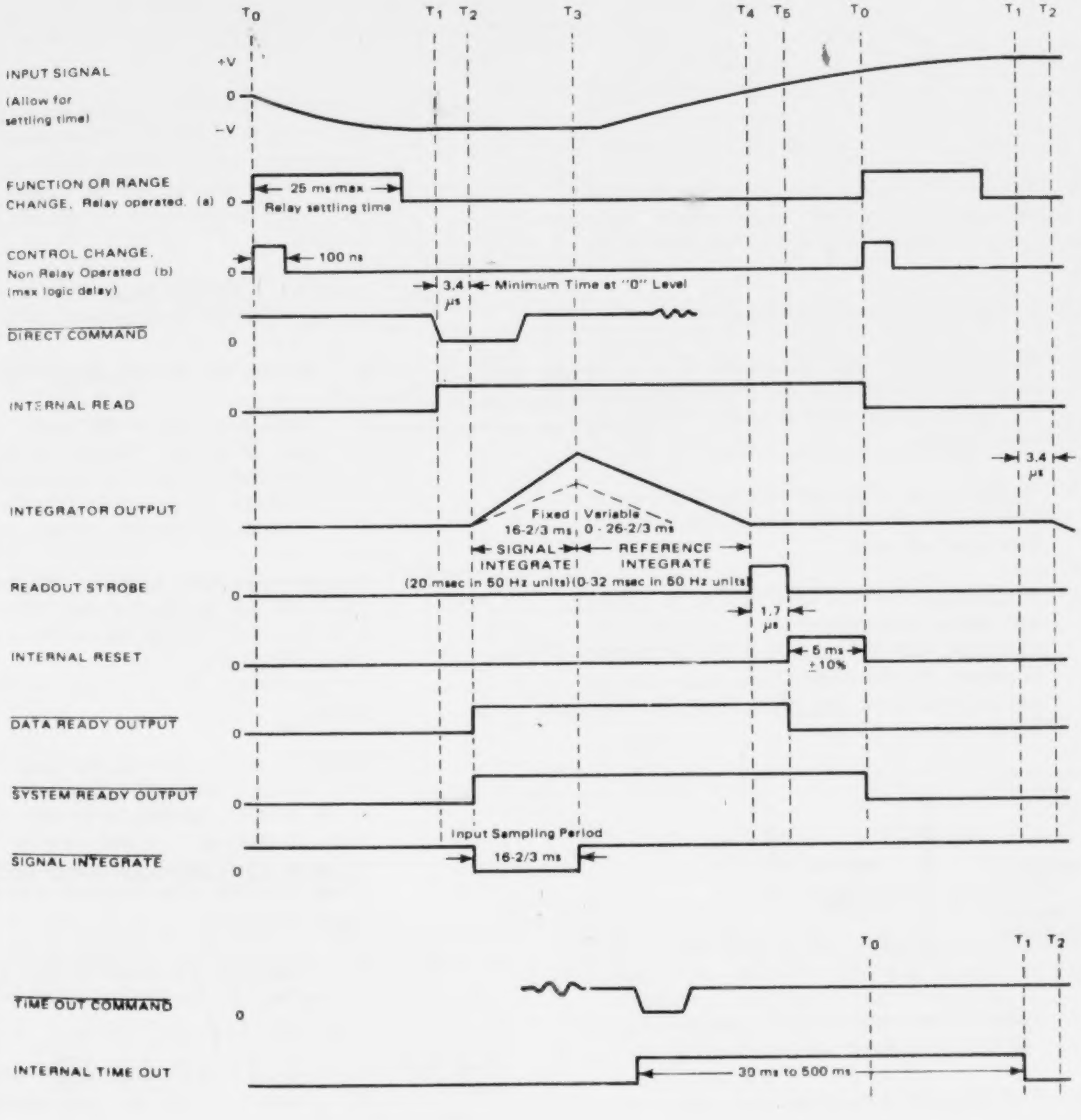


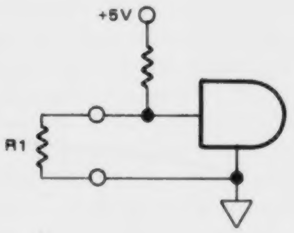
Figure 2.10 - Rear Panel


T0
T1
T2
T3
T4
T5
T0
T1
T2
INPUT SIGNAL
(Allow for settling time)
+V
0
−V
FUNCTION OR RANGE CHANGE. Relay operated. (a)
25 ms max
Relay settling time
CONTROL CHANGE. Non Relay Operated (b) (max logic delay)
100 ns
3.4 μs
Minimum Time at "0" Level
DIRECT COMMAND
INTERNAL READ
3.4 μs
INTEGRATOR OUTPUT
Fixed 16-2/3 ms
Variable 0 - 26-2/3 ms
SIGNAL INTEGRATE
REFERENCE INTEGRATE
(20 msec in 50 Hz units)
(0-32 msec in 50 Hz units)
READOUT STROBE
1.7 μs
INTERNAL RESET
5 ms ±10%
DATA READY OUTPUT
SYSTEM READY OUTPUT
Input Sampling Period
16-2/3 ms
SIGNAL INTEGRATE
T0
T1
T2
TIME OUT COMMAND
INTERNAL TIME OUT
30 ms to 500 ms
0

(a) Such as: Function, Range, Filter and Ratio.
(b) Such as: Super Fast, Data Disable, Program Storage System Control and Hold

Figure 2.11 - Measurement Sequence

one level as long as the input source resistance (R1) to ground is greater than 15K ohms.



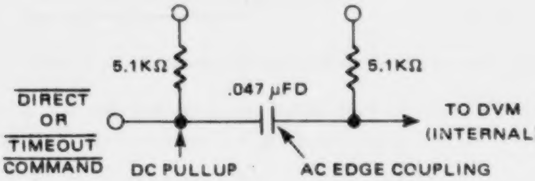
2.58 To input a "0" level, at least 2.0 ma of current must be sinked maintaining the input voltage below 0.4 volts. This requires a resistance to ground of 200 ohms or less.

2.59 Exceptions to Input Loading Conditions.

a. Program Storage Input (J202, pin B-15) is the equivalent of 3 TTL inputs and requires a minimum 5.8 ma sinking current, or 68 ohms or less to common.

b. Maximum input voltage level, referenced to common, must not exceed 5.5 volts peak. Otherwise, gate destruction will occur.

c. The Direct and Time Out Commands are AC coupled with pull-up resistors to +5 volts. These inputs are compatible with TTL outputs or contact closures to ground. The AC coupling does require that rise and fall times be less than 100 μseconds. This input circuit is illustrated below:



d. Digital output common can be floated as high as 200 VDC above power line ground.

2.60 TTL Output Capabilities.

2.61 The 5900 electrical outputs are specified to drive two TTL inputs such as described in the TTL loading section. Summary:

False: 0 to +0.4V
True: +2.4 to +5.0V
Fan out: 2 minimum
Maximum Capacitance Load: 500 pF

2.62 Timing Sequence.

2.63 The standard remote mode of operation of the 5900 is to initiate a reading sequence with each Direct Command received through the programmer, providing that sufficient time has been allowed between commands for the reading to be completed. This reading sequence is illustrated in figure 2.11.

$T_1 - T_0$ During this period the input signal must finish settling to within the desired accuracy. Any control changes involving the 25 msec relay settling time (a) can be completed; other logic control inputs (b) can also be changed.

$T_2 - T_1$ The Direct Command signal, which is AC coupled, must meet the following conditions:

a. Rise and fall times less than 100 μsec.

b. Signal must stay in the logical "0" state for at least 3.4 μsec. If these conditions are met, the internal read command is sustained at T_2 and the signal integrate period is started.

$T_3 - T_2$ The period of signal integration lasts for 16-2/3 msec (60 Hz line frequency; 20 msec in 50 Hz units). During this time the integrator charges to a voltage proportional to the input voltage. This is the input sampling period.

$T_4 - T_3$ During this period, the integrator is isolated from the input signal, and is discharged at a precise current. The time the integrator requires to discharge to a level equal to its voltage at T_2 is proportional to the input voltage. This time is measured by an internal counter and stored.

$T_5 - T_4$ This 1.7 μsec period is required to strobe the new reading from the internal counter into the readout latches.

$T_0 - T_5$ This 5 mseconds (±10%) is required to reset the internal logic for the next reading.

$T_1 - T_0$ If the next read command is a Direct Command, this period must be made long enough to allow for the condition covered in the first cycle; however, if the next command is a Timeout Command, this period can approach zero since the necessary timeout to satisfy these conditions are automatically programmed.

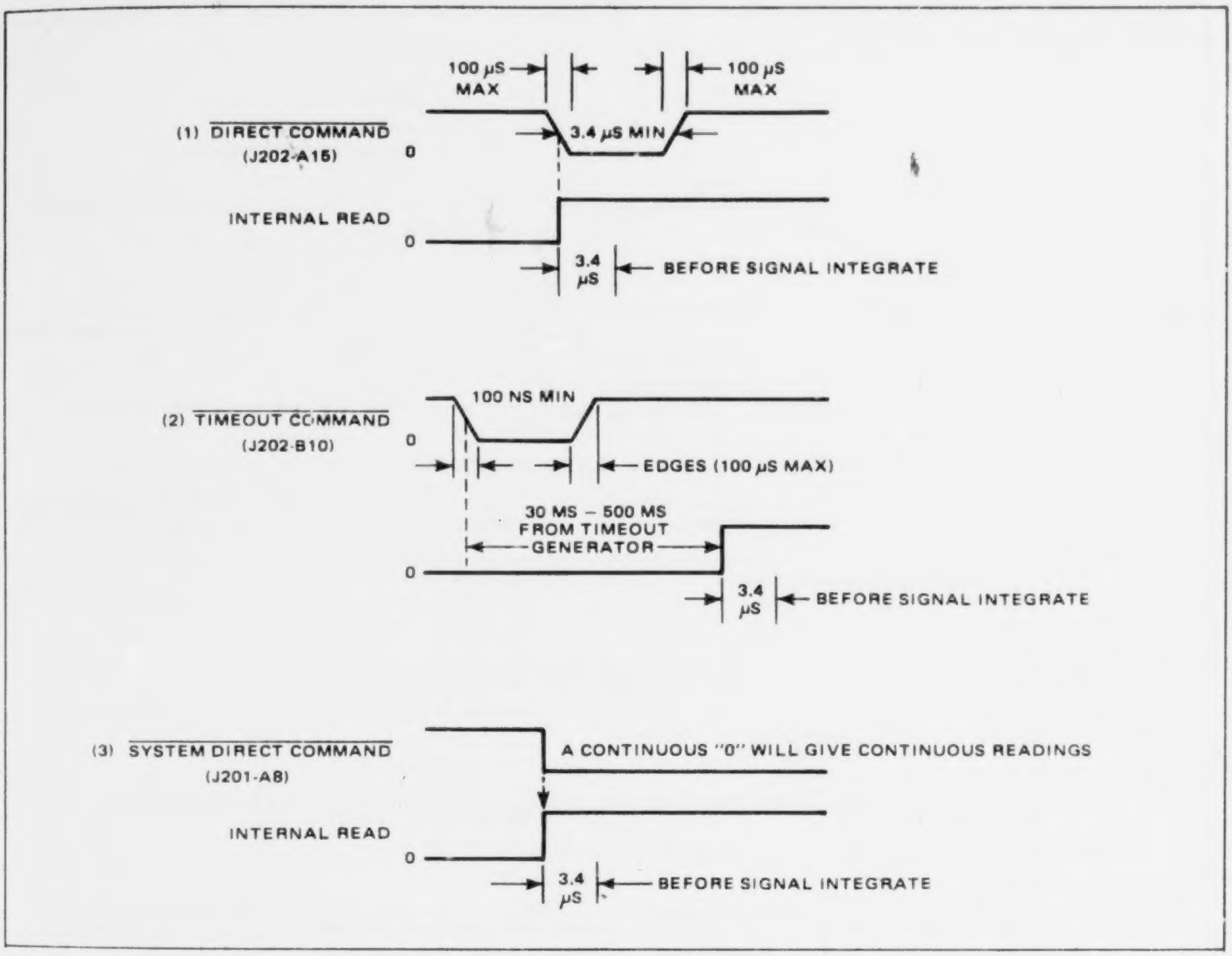

100 µS MAX
100 µS MAX
(1) DIRECT COMMAND (J202-A15)
0
3.4 µS MIN
INTERNAL READ
0
3.4 µS
BEFORE SIGNAL INTEGRATE
(2) TIMEOUT COMMAND (J202-B10)
0
100 NS MIN
EDGES (100 µS MAX)
30 MS – 500 MS FROM TIMEOUT GENERATOR
0
3.4 µS
BEFORE SIGNAL INTEGRATE
(3) SYSTEM DIRECT COMMAND (J201-A8)
A CONTINUOUS "0" WILL GIVE CONTINUOUS READINGS
INTERNAL READ
0
3.4 µS
BEFORE SIGNAL INTEGRATE

Figure 2.12 - Command Timing

2.64 Other Read Command Options.

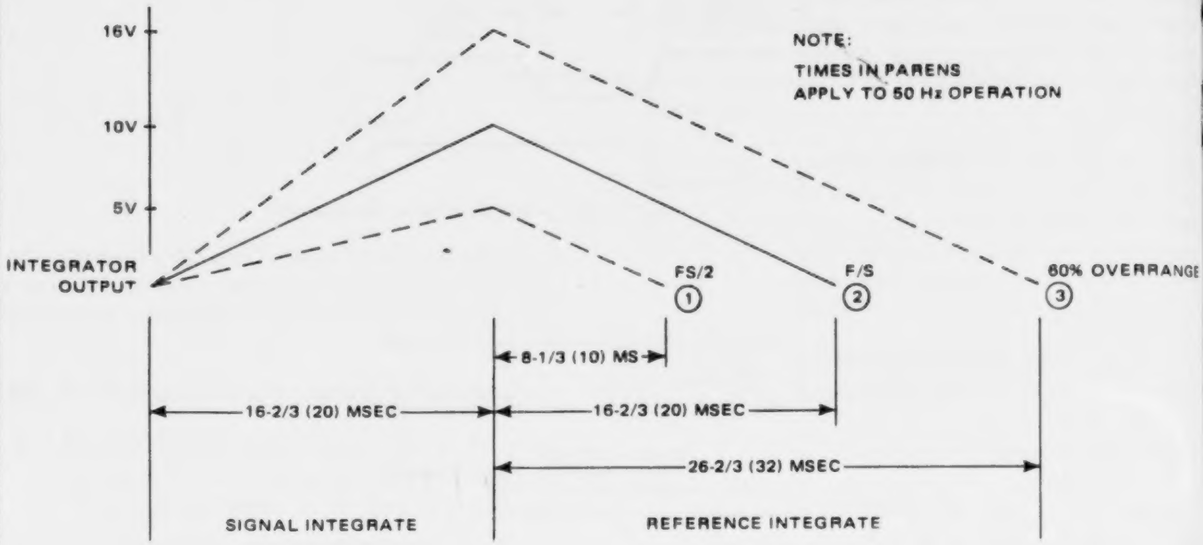
2.65 In addition to the Direct Command, there are two other programmable read commands, as illustrated in figure 2.12.

a. Time Out Command: Again, this is an AC coupled input which must have rise and fall times of less than 100 µseconds but must remain in a "0" state for at least 0.1 µsecond. The timeouts given in table 2.7 for various combinations of ranges and functions ranging from 30 mseconds to 500 mseconds will be automatically inserted before the internal read command is generated. If this command is wired to the Data Ready Output on J201 - pin A11, fully automatic reading with timeouts is achieved.

NOTE

The 5 msec internal delay is not adequate for settling time on the 100 VDC, 1000 KΩ, 10 MΩ, 100 MΩ ranges, or any AC range. Therefore, the timeout command, providing timeout delays listed in table 2.7, must be used to initiate accurate readings on these ranges unless a fixed range and function have been programmed and the input has been present longer than the timeout period.

b. System Direct Command: While the other two read commands were AC coupled and programmed through the Program Input Connector (J202), this command is DC coupled and programmed through


16V
10V
5V
INTEGRATOR OUTPUT
NOTE:
TIMES IN PARENS
APPLY TO 50 Hz OPERATION
FS/2
①
F/S
②
60% OVERRANGE
③
8-1/3 (10) MS
16-2/3 (20) MSEC
16-2/3 (20) MSEC
26-2/3 (32) MSEC
SIGNAL INTEGRATE
REFERENCE INTEGRATE

MINIMUM READ RATES CORRESPONDING TO INTEGRATION PERIODS (ABOVE).

V_{IN} = 5 VOLTS (HALF SCALE ON 10V RANGE)

50 Hz:

$$\frac{1000}{20\text{ MS} + 10\text{ MS} + 5.5\text{ MSEC}} = 28.2\text{ READINGS/SEC}$$

60 Hz:

$$\frac{1000}{16\text{-}2/3\text{ MS} + 8\text{-}1/3\text{ MS} + 5.5\text{ MS}} = 32.8\text{ READINGS/SEC}$$

② V_{IN} = 10V (FULL SCALE ON 10V RANGE)

50 Hz:

$$\frac{1000}{20\text{ MS} + 20\text{ MS} + 5.5\text{ MS}} = 22.0\text{ READINGS/SEC}$$

60 Hz:

$$\frac{1000}{16\text{-}2/3\text{ MS} + 16\text{-}2/3\text{ MS} + 5.5\text{ MS}} = 25.7\text{ READINGS/SEC}$$

③ V_{IN} = 16V (FULL SCALE & MAX. OVERRANGE) ON 10V RANGE:

50 Hz:

$$\frac{1000}{20\text{ MS} + 32\text{ MS} + 5.5\text{ MS}} = 17.4\text{ READINGS/SEC}$$

60 Hz:

$$\frac{1000}{16\text{-}2/3\text{ MS} + 26\text{-}2/3\text{ MS} + 5.5\text{ MS}} = 20.5\text{ READINGS/SEC}$$

Figure 2.13 - Minimum Read Rate vs. Input

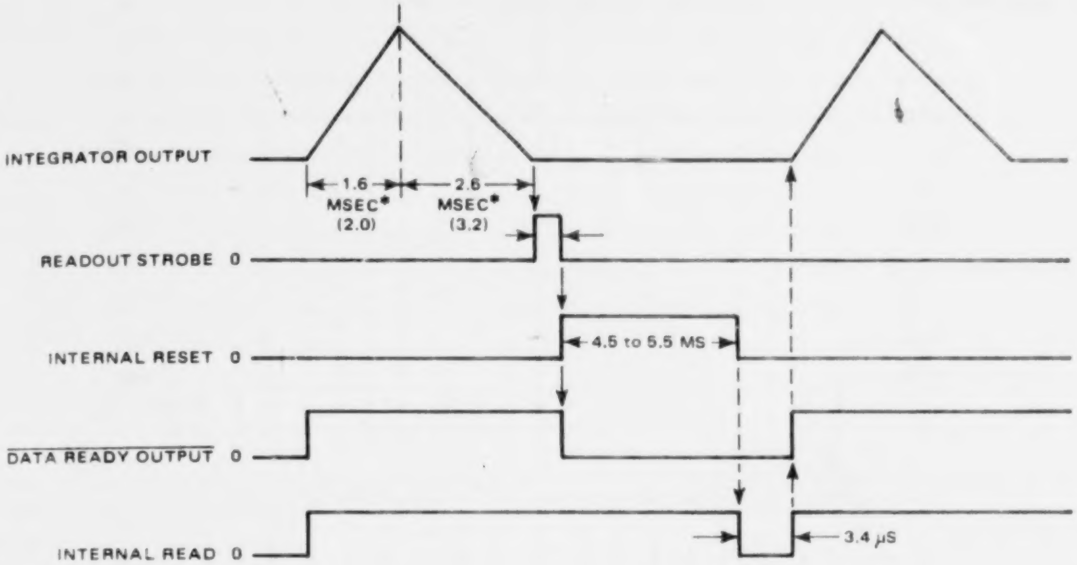
the Printer Output Connector (J201). The System Direct Command must remain in the "0" state for at least 3.4 μseconds to generate the internal read command. This delay, as in the Direct Command, is to prevent noise from triggering the readings. If this command is tied to ground, the 5900 will recycle at its maximum reading rate with no timeouts.

2.66 Reading Rates.

2.67 In figure 2.13, integrator operation with three different input signal levels is illustrated: half scale, full scale, and 160% of full scale (full scale is defined as 100000 on an range). The figure shows that the maximum reading rate a function of the input signal. The signal integrate perio and internal reset remain fixed while the reference integrat period can vary from 0 to 32 mseconds. Therefore th maximum read rate could vary from 17.4 to 45 reading pe second.

2.68 Superfast.

2.69 The Superfast reading mode (programmed throug either the PRINTER OUT or PROGRAM INPUT connector



*NUMBERS IN PARENS ARE TIMES IN 50-Hz UNITS.

50 Hz:
2.0 MSEC
3.2 MSEC
5.5 MSEC
10.7 MSEC = MAXIMUM RECYCLE TIME (93.4 READINGS/SEC MIN)

60 Hz:
1.67 MSEC
2.67 MSEC
5.50 MSEC
9.84 MSEC = MAXIMUM RECYCLE TIME (101.6 READINGS/SEC MIN)

Figure 2.14 - Superfast Read Rate (Worst Case)

increases the minimum reading rate from 20.5 to 102 readings per second (50 Hz: 17.4 to 93 r/s). This is done at the expense of losing the least-significant digit which is reset to zero (blanked out on readout). The signal integration period is reduced from 16-2/3 msec to 1-2/3 msec (50 Hz: 20 msec to 2 msec). This and the resulting reference integrate period reduce the maximum recycle time from 48.8 msec to 9.8 msec (50 Hz: 57.5 msec to 10.7 msec), thereby yielding the 107 reading per second figure (93 r/s with 50 Hz). Timing changes are shown in figure 2.14.

2.70 PRINTER OUTPUT.

2.71 The printer output connector is a double-edged PCB connector (extension of Interconnection and Logic board) with pins A1 through A22 on the bottom edge and pins B1 through B22 on the top edge. Pin assignments are shown in figure 2.15. All outputs are referenced to digital ground pin B1.

2.72 Numerical Data

2.73 Numerical data appears as positive true, four-line BCD code, as shown in figure 2.15. The designator of each line identifies the digit and weight. For example: Pins A18, 19, 20, and 21 are designated 1_2, 1_8, 1_4, and 1, consecutively. The 1 indicates these lines correspond to the units or least significant display; the 2, 8, 4, and 1 subscripts indicate the binary weight of each line.

CAUTION

True output lines are not short-circuit proof. Accidental grounding may damage the output circuitry.

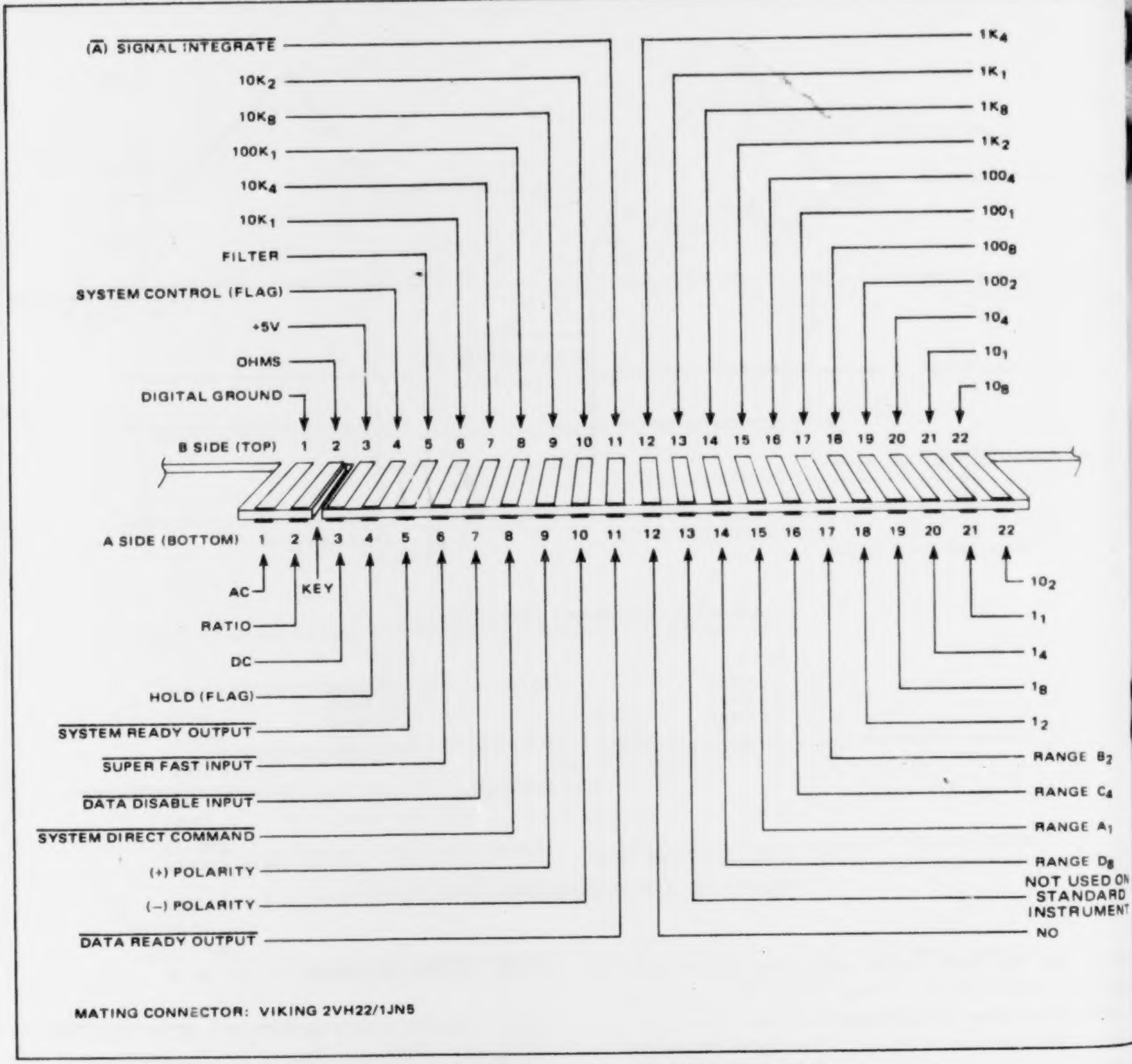

(A) SIGNAL INTEGRATE
$10K_2$
$10K_8$
$100K_1$
$10K_4$
$10K_1$
FILTER
SYSTEM CONTROL (FLAG)
+5V
OHMS
DIGITAL GROUND
$1K_4$
$1K_1$
$1K_8$
$1K_2$
100_4
100_1
100_8
100_2
10_4
10_1
10_8
B SIDE (TOP) 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22
A SIDE (BOTTOM) 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22
KEY
AC
RATIO
DC
HOLD (FLAG)
SYSTEM READY OUTPUT
SUPER FAST INPUT
DATA DISABLE INPUT
SYSTEM DIRECT COMMAND
(+) POLARITY
(−) POLARITY
DATA READY OUTPUT
10_2
1_1
1_4
1_8
1_2
RANGE B_2
RANGE C_4
RANGE A_1
RANGE D_8
NOT USED ON STANDARD INSTRUMENT
NO
MATING CONNECTOR: VIKING 2VH22/1JN5

Figure 2.15 - Pin Assignments J201 PRINTER OUT

2.74 Polarity is indicated in positive true format on pin A9 (positive) and pin A10 (negative). The positive polarity line is true when the function output is AC or .1K through 1K OHMS ranges. Negative polarity is true with Ohms on the 10K through 100M ranges. If the instrument overranges, the polarity bit is not updated since no axis-crossing has occurred.

2.75 Function Data.

2.76 Function outputs appear on pins A1, 2, 3 and B2 in their true format. For example: with AC function selected, AC is true and is indicated by a true level on the corr sponding line.

2.77 Range Data.

2.78 Range data appears in four-line BCD code on p A14 through A17. Range codes are described in table 2

2.79 "NO" Indication.

2.80 The NO line (pin A12) is the same as t NO indicator on the readout. The line is true if a functi

Table 2.4 - Range Codes (Printer Output)

Range	A (Pin A15)	B (Pin A17)	C (Pin A16)	D (Pin A14)	Dec Value
10Ω	0	1	0	0	2
0.1V 0.1 KΩ	1	1	0	0	3
1V 1 KΩ	0	0	1	0	4
10V 10 KΩ	1	0	1	0	5
100V 100 KΩ	0	1	1	0	6
1000V 1000 KΩ	1	1	1	0	7
10 MegΩ	0	0	0	1	8
100 MegΩ	1	0	0	1	9

or range is selected for which the particular instrument is not equipped. Overrange is indicated by a true NO line plus a numerical data output of 160000.

2.81 Status Output Lines.

2.82 The following outputs indicate the status of the conversion process within the instrument.

a. DATA READY. This line (pin A11) remains true during the signal and reference integration periods plus any overrange time, if required. The line drops to the false level to indicate to the printer that the measurement is complete and output data can be printed (Printer Command). With front-panel operation, DATA READY is enabled only when the DATA OUTPUT switch (on front panel) is depressed. Minimum false level time is 4.5 mseconds.

b. HOLD FLAG. A true level on this line (pin A4) indicates that the instrument is in the Hold mode. A reading can be initiated by one of the following commands:

  1. SYSTEM DIRECT COMMAND (J201-A8)
  2. DIRECT COMMAND (J202-A15)
  3. TIMEOUT COMMAND (J202-B10)
  4. RATE Control (front panel)

c. SYSTEM READY. This line (pin A5) drops to a false level to indicate that the instrument can now initiate a new reading at the first available read command.

d. SIGNAL INTEGRATE. This line (pin B11) becomes true at the end of the signal integration period. After this time, the input signals may be changed in preparation for the next reading. The input signal need remain constant only while the instrument is in Signal Integrate, indicated by this line in the false state. For example, the 1-2/3 msec sample time in Superfast could be used in slow sample and hold applications.

2.83 Input Control Lines.

a. DATA DISABLE. A contact closure to ground or a false logic level applied to this line (pin A7) inhibits the DATA READY output (Print) pulse.

b. SYSTEM CONTROL. A contact closure to ground or a false logic level on this line (pin B6) disables all front panel operating controls. Operation of the instrument is then under control of the Remote Program input. This command duplicates operation of the PROGRAM CONTROL switch on the front panel. It is necessary that one of these commands be initiated during Autorange to inhibit extra DATA READY pulses.

c. SUPERFAST. A contact closure to ground or a false logic level applied to this line (J201-A6 or J202-A7) decreases the conversion time of the instrument while sacrificing the least-significant digit. This mode is described in paragraph 2.68. Because of the superfast read rate, do not use this mode with Autorange.

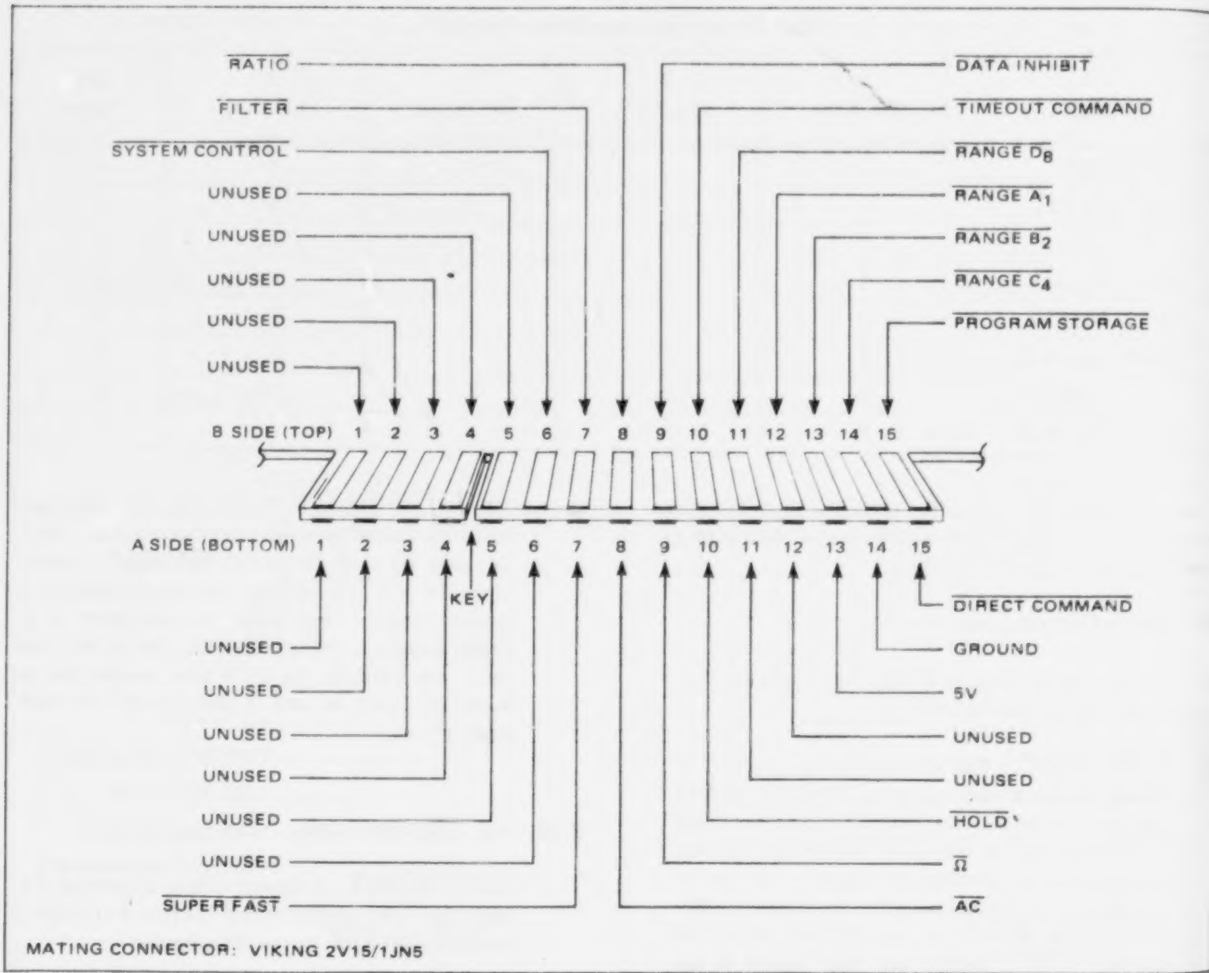

RATIO
FILTER
SYSTEM CONTROL
UNUSED
UNUSED
UNUSED
UNUSED
UNUSED
DATA INHIBIT
TIMEOUT COMMAND
RANGE D_8
RANGE A_1
RANGE B_2
RANGE C_4
PROGRAM STORAGE
B SIDE (TOP) 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
A SIDE (BOTTOM) 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
KEY
UNUSED
UNUSED
UNUSED
UNUSED
UNUSED
UNUSED
SUPER FAST
DIRECT COMMAND
GROUND
5V
UNUSED
UNUSED
HOLD
Ω
AC
MATING CONNECTOR: VIKING 2V15/1JN5

Figure 2.16 - Pin Assignments J202 PROGRAM INPUT

2.84 SYSTEM DIRECT COMMAND.

2.85 To externally initiate a measurement with this command, the instrument must be in the Hold mode. Then, a contact closure to ground or a false logic level to this line (pin A8) generates a read command. Minimum false level time is 3.4 μsec. Continuous readings at the maximum read rate are obtained by tying this line to ground. Read commands delayed by 5 msec ± 10% (for settling time) are then generated automatically.

2.86 REMOTE PROGRAMMING.

2.87 The instrument accepts commands made through PROGRAM INPUT connector J202 on the rear panel. Pin assignments of J202 are shown in figure 2.16. Commands are made by a switch closure from the appropriate pin t ground or by standard TTL logic levels as described earlie

2.88 SYSTEM CONTROL.

2.89 A contact closure to ground or a false logic lev applied to pin B6 disables all front panel operating control Operation of the instrument is then under control of th remote program input. This line duplicates the PROGRA CONTROL switch on the front panel.

2.90 Function Programming.

2.91 The desired function is selected by applying ground or false logic level to the appropriate pin (table 2.5

Table 2.5 - Function Programming

$\overline{\text{DC}}$	NC
$\overline{\text{AC}}$	pin A8
$\overline{\text{OHMS}}$	pin A9
$\overline{\text{RATIO}}$	pin B8
$\overline{\text{FILTER}}$	pin B7

2.92 The 5 msecond internal delay is not adequate for settling time on the 100 VDC, 1000 Kilohm, 10 Megohm, 100 Megohm, and all AC ranges. Therefore, the timeout command, providing timeout delays listed in table 2.7, must be used to initiate readings on these ranges unless the input is fixed with range and function predetermined.

2.93 Range Programming.

2.94 Range programming is selected by applying false logic levels in BCD code to the four range lines described in table 2.6 below. With no lines programmed, Autorange is automatically selected.

2.95 + Five Volts.

2.96 This voltage, +5 volts ± 5%, from the logic power supply is available at pin A13 for external use. Current output is .1A, maximum.

2.97 Hold.

2.98 The Hold line parallels the operation of the ccw position of the front panel RATE potentiometer and is selected by a contact closure or a false logic level on pin A10. Hold is required when using either of the three read commands.

2.99 Read Commands.

2.100 Either of two read command lines can be selected by a contact closure to ground or by a negative logic level applied to the appropriate pin. Pin A15, $\overline{\text{DIRECT COMMAND}}$, commands a new measurement if applied after a five millisecond reset delay, and if the command is present for 3.4 μseconds. Pin B10, $\overline{\text{TIMEOUT COMMAND}}$, starts a new measurement after five milliseconds plus a timeout delay to allow for internal settling time of the measured signal. $\overline{\text{TIMEOUT COMMAND}}$ may be commanded before the previous 5 msec delay since the timeout generator stores the reading.

2.101 Timeouts.

2.102 Timeout periods for each function are listed in table 2.7. In Autorange, the indicated delays are taken following each range change.

2.103 Data Inhibit.

2.104 A contact closure or false logic level on pin B9 inhibits $\overline{\text{DATA READY OUTPUT}}$ (Print pulse) from being generated.

2.105 Program Storage.

2.106 A false level (equivalent to three TTL inputs) on pin B15 will store all the programmed inputs except the Direct and Timeout commands as they existed on the negative edge of this command (see diagram below).

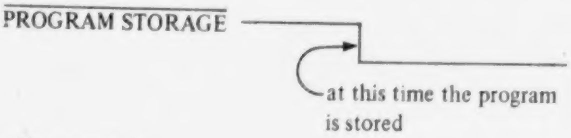

$\overline{\text{PROGRAM STORAGE}}$
at this time the program is stored

Table 2.6 - Range Codes (Programmer)

Range	$\overline{\text{A}}$ (Pin B12)	$\overline{\text{B}}$ (Pin B13)	$\overline{\text{C}}$ (Pin B14)	$\overline{\text{D}}$ (Pin B11)	Dec Value
AUTO	1	1	1	1	0
10Ω	1	0	1	1	2
0.1V 0.1 KΩ	0	0	1	1	3
1V 1 KΩ	1	1	0	1	4
10V 10 KΩ	0	1	0	1	5
100V 100 KΩ	1	0	0	1	6
1000V 1000 KΩ	0	0	0	1	7
10 MegΩ	1	1	1	0	8
100 MegΩ	0	1	1	0	9

2.107 SUPERFAST.

2.108 A contact closure to ground or a false logic level applied to pin A7 decreases the signal integrate and the reference integrate times (see paragraph 2.68). This provides the maximum reading rate in the Direct COMMAND mode of operation. Because of the high reading rate, Superfast must be programmed with a fixed range rather than AUTORANGE.

Table 2.7 - Timeouts

DC	30 msec
1 Ohm to 1 Megohm	30 msec
10 Meg*	30 msec
100 Megohm	300 msec
Filter	470 msec (plus function timeout)
AC(fixed range, no filter)	180 msec
AC+Filter(&Autorange)	650 msec
*Use filter for <.01% error	

Table 2.8 - Maximum Input Voltage

CAUTION

Do not exceed the following maximum inputs.

DC	1000 VDC or RMS AC All ranges
AC	1000 RMS to 20 kHz decreasing 20 dB/ decade to 20V RMS at 1 MHz
RATIO	Input: same as function selected Reference: +10.5V, –0.5V
OHMS	±500V DC or Peak AC
GUARD	Voltage between GUARD AND – INPUT must not exceed 250 volts or damage to the instrument may result

2.109 ADDING/REMOVING ACCESSORIES.

2.110 The AC and Ohms options may be added or removed at any time in the field without modification to the basic instrument. Note that the switching board (403625 is required whenever the plug-in options are used. Acces to the mounting connectors is by removal of the top cover held down by captive screws in each of the four corners and by removal of shield, mounted by four flat head screws

2.111 When an option is added or removed from the instrument, a jumper corresponding to the option is removed or added to allow proper operation of the NO circuitry. The two jumpers (W1 Ohms and W2 AC) are located on the display board, shown partially in figure 2.17. When an option is added, the corresponding jumper is added; conversely, when an accessory is removed, the jumper is removed.

2.112 The AC RMS (Option 32) allows for either AC or DC coupled measurements. The selector switches are located on the AC RMS converter as shown in figure 5.8.

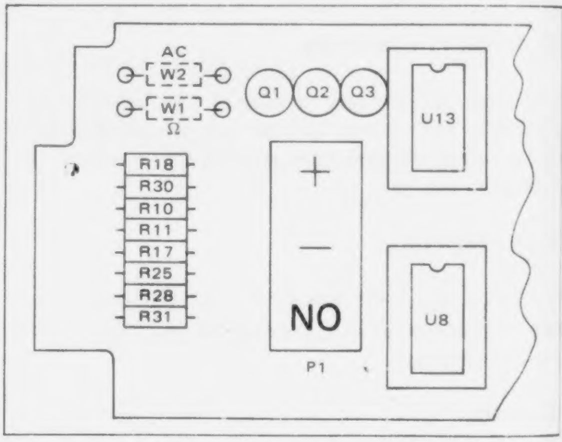


Figure 2.17 - Jumper Location

2.113 MODEL 55 GENERAL PURPOSE INTERFACE BUS.

2.114 The Model 5900 DMM may be interfaced with other instrumentation and control devices through use of the Model 55 General Purpose Interface Bus (GPIB). Information and the specifications for the GPIB are presented in paragraph 1.19 and Table 1.2 in Section 1 of this manual.

2.115 The programming information and detailed electrical interface connection information is presented in the Model 55 Instruction Manual, Dana Document Number 980498

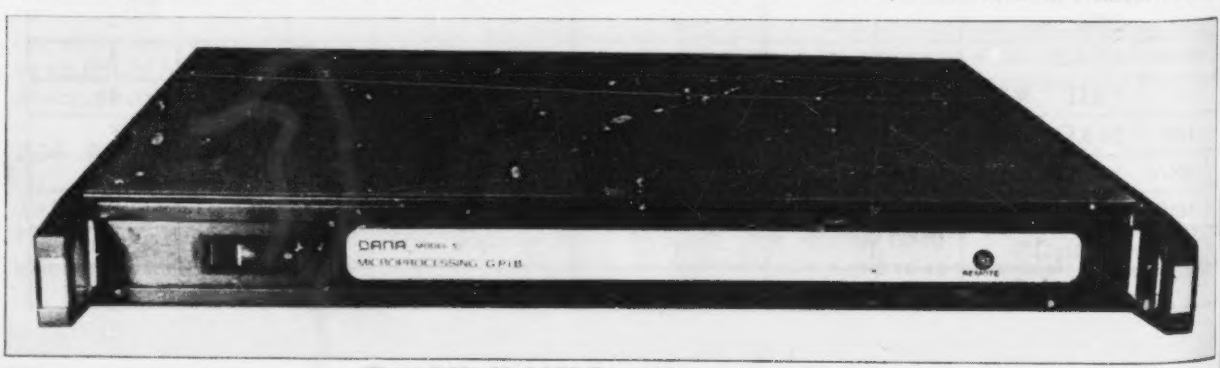


Figure 2.18 - Model 55 General Purpose Interface Bus

SECTION 3 SPECIFICATION TESTS

3.1 GENERAL.

3.2 This section contains procedures that compare the operation of the instrument against the published specifications found at the front of this manual. It is intended to be used for incoming inspection and as a periodic check to determine if recalibration of the instrument is warranted.

3.3 The procedures provide sufficient checks to verify proper operation and that the instrument is within the 90 day accuracy limits. Covers of the instrument are not removed for any of the tests. The required ambient temperature of the environment is 23° ± 5°C.

3.4 REQUIRED EQUIPMENT.

3.5 In Table 3.1 is a list of equipment necessary for checking the instrument. The equipment in this table, with the exception of those in the OTHER category, is the same as required for recalibration and is explained in detail in Section 5.

3.6 PROCEDURE.

3.7 Allow one hour for warmup. Convert the instrument and the test equipment as shown in the figure supplied with each accuracy check. Select the controls and inputs as called out in the tables and monitor the instrument readout for the indicated values. If the instrument is equipped with the rear panel selectable input option, set the FRONT/REAR switch to the FRONT position for all procedures presented in this section.

Table 3.1 - Required Equipment

Function	Qty	Item	Minimum Use Specifications	Suggested Equipment
DC	(1)	Saturated Standard Cell Bank (6 cells)	1 ppm, certified	EPPLEY 106
	(2)	DC Voltages Sources	0.1 ppm resolution	FLUKE 332B
	(2)	Voltage Dividers, Adjustable	0.1 ppm linearity	FLUKE 720A
	(2)	Null Detector/μ Voltmeters	1 μV sensitivity	FLUKE 845AR
AC	(1)	Thermal Transfer Standard	50 ppm	HOLT 6A, With corrections
	(1)	AC Voltage Source	1 ppm resolution	HP745A/746A
Ω	(8)	Resistance Standards		
		10Ω	10 ppm	ESI SR1 with corrections
		100Ω	5 ppm	ESI SR1 with corrections
		1 KΩ	5 ppm	ESI SR1 with corrections
		10 KΩ	5 ppm	ESI SR1 with corrections
		100 KΩ	5 ppm	ESI SR1 with corrections
		1 MΩ	5 ppm	Fabricated
		10 MΩ	20 ppm	ESI SR1 with corrections
		100 MΩ	80 ppm	Fabricated
OTHER	(1)	Momentary Switch, SPST	–	–
	(2)	1.5 volt cells w/screwtype binding posts	–	–
	(1)	Insulated Adjustment tool	–	JFD5284
	(1)	100Ω, 10, 100 Kilohm, 1 Megohm 1/4 Watt 5% Carbon Resistors	5%	–
	(1)	1 μFD non polar capacitor	–	–

pages tightly bound

Table 3.2 - DC Range Check (Low Ranges)

DVM		INPUT SIGNAL		NOMINAL READING	TOLERANCE	NOTE
FUNCTION	RANGE	DC VOLTAGE STANDARD	DIVIDER SETTING			
DC	.1V	10.00000	.01000	.100000	.099992 – .100008	23°C ± 5°C (After zeroing)
	1V	10.00000	.10000	1.00000	0.99997 – 1.00003	

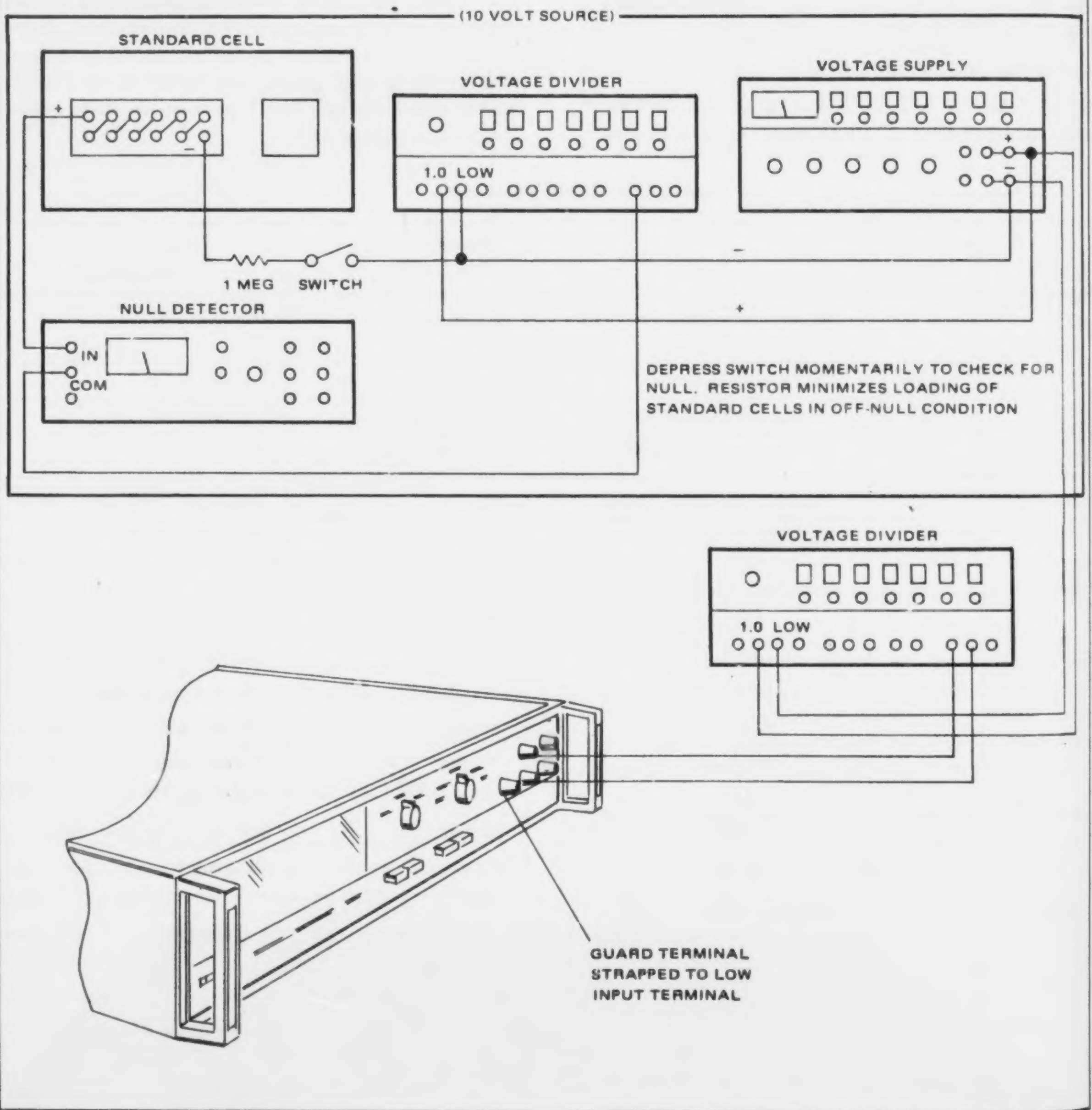

(10 VOLT SOURCE)
STANDARD CELL
+
–
VOLTAGE DIVIDER
1.0 LOW
VOLTAGE SUPPLY
+
–
1 MEG
SWITCH
–
+
NULL DETECTOR
IN
COM
DEPRESS SWITCH MOMENTARILY TO CHECK FOR NULL. RESISTOR MINIMIZES LOADING OF STANDARD CELLS IN OFF-NULL CONDITION
VOLTAGE DIVIDER
1.0 LOW
GUARD TERMINAL STRAPPED TO LOW INPUT TERMINAL

Table 3.3 - DC Range Check (High Ranges)

DVM		INPUT SIGNAL				
FUNCTION	RANGE	DC VOLTAGE STANDARD		NOMINAL READING	TOLERANCE	NOTE
DC	10	10.0000V		10.0000	9.9998 – 10.0002	23°C ± 5°C (After zeroing)
	100	100.000V		100.000	99.997 – 100.003	
	1000	1000.00V		1000.00	999.97 – 1000.03	

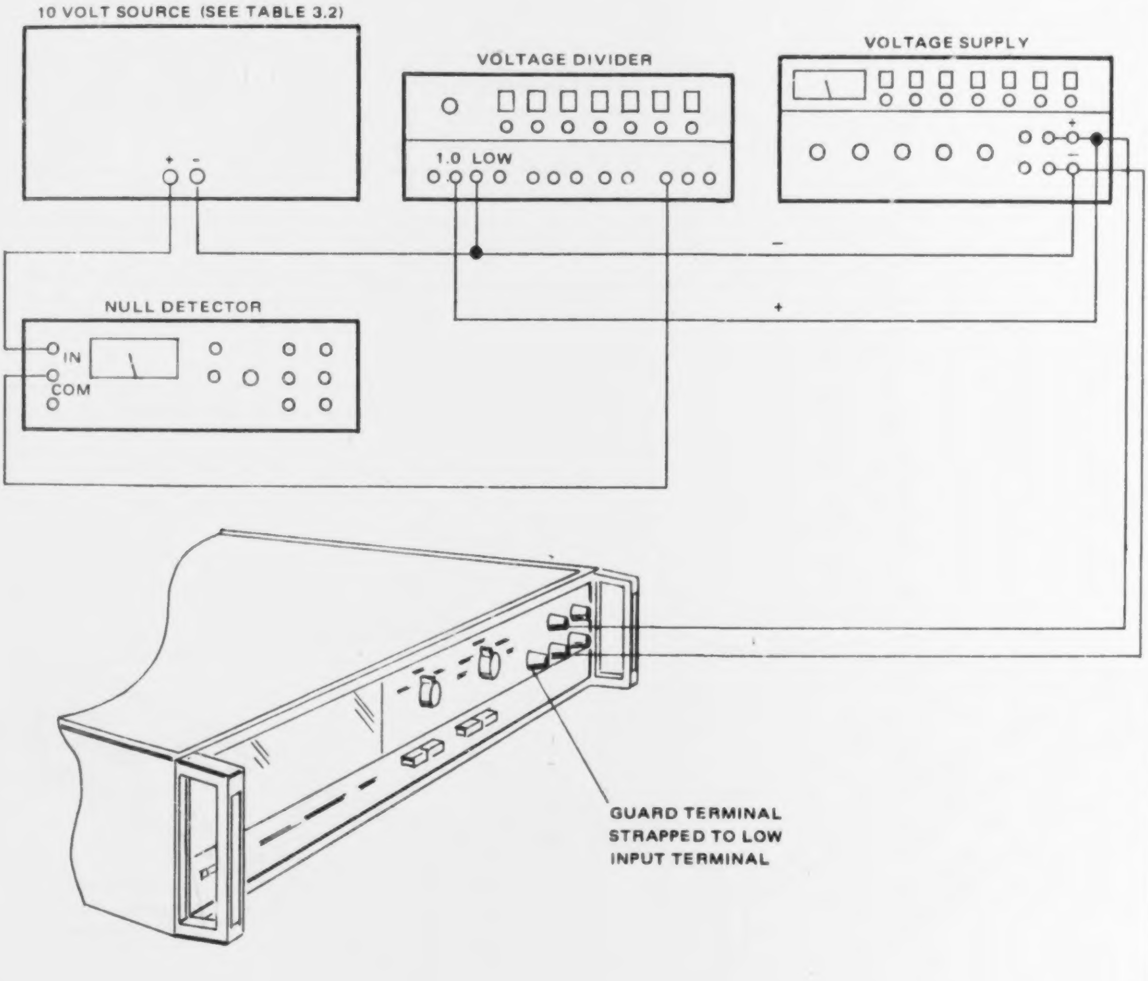

10 VOLT SOURCE (SEE TABLE 3.2)
+
-
VOLTAGE DIVIDER
1.0 LOW
VOLTAGE SUPPLY
+
-
-
+
NULL DETECTOR
IN
COM
GUARD TERMINAL STRAPPED TO LOW INPUT TERMINAL

Table 3.4 - 3-Wire Ratio Check

DVM		INPUT SIGNAL				
FUNCTION	RANGE	DC VOLTAGE STANDARD		NOMINAL READING	TOLERANCE	NOTE
DC RATIO	10	+ 2.00000V		10.0000	9.9990 – 10.0010	23°C ± 5°C (After zeroing)
		+10.00000V		10.0000	9.9998 – 10.0002	

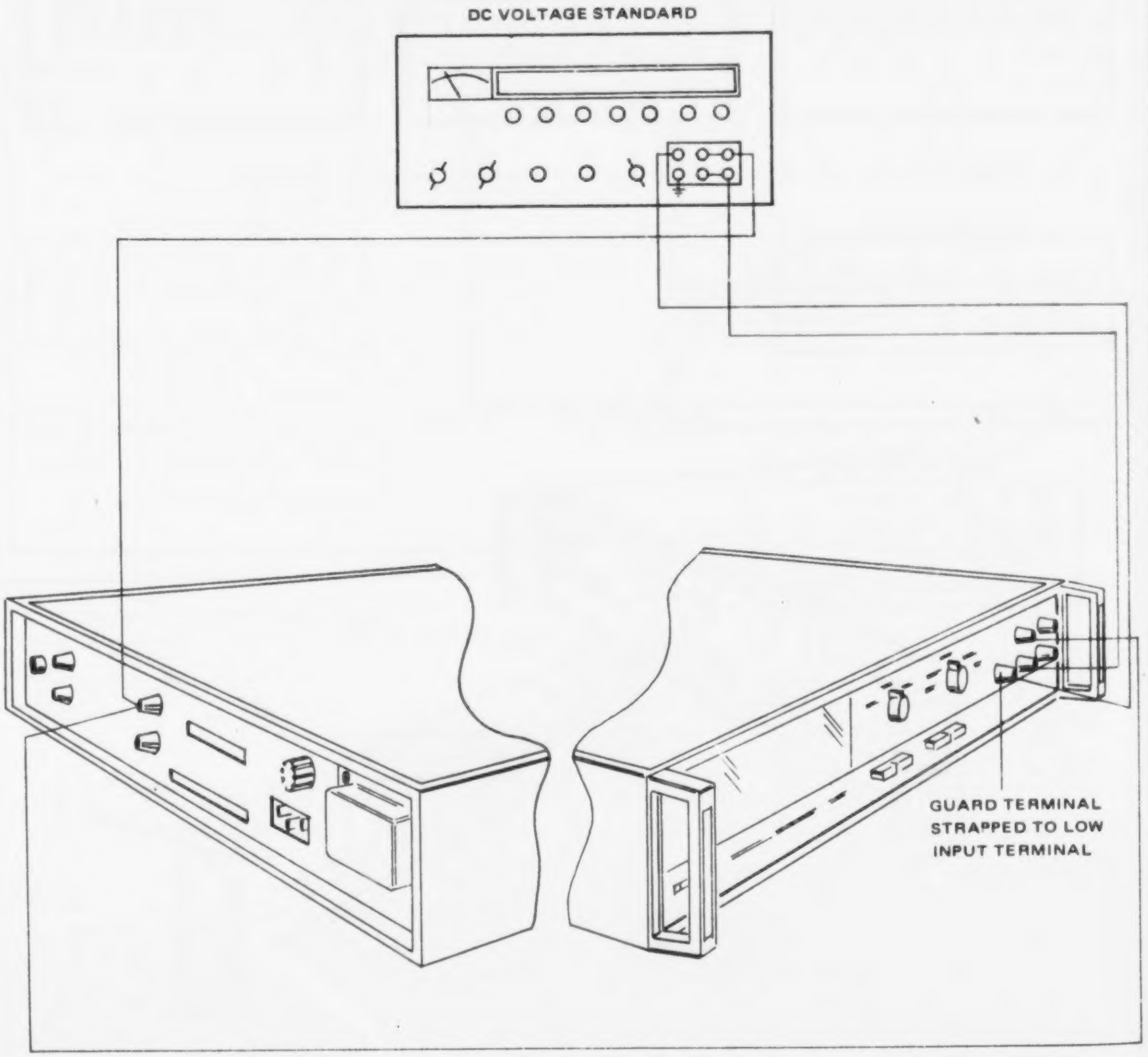

DC VOLTAGE STANDARD
GUARD TERMINAL STRAPPED TO LOW INPUT TERMINAL

Table 3.5 - 4-Wire Ratio Check

DVM		INPUT SIGNAL		NOMINAL READING	TOLERANCE	NOTE
FUNCTION	RANGE	DC VOLTAGE STANDARD				
DC	10	+ 2.00000V		+10.0000	9.9980 – 10.0020	23°C ± 5°C (After zeroing)
RATIO	10	+10.00000V		+10.0000	9.9996 – 10.0004	
		–10.00000V		+10.0000	9.9996 – 10.0004	

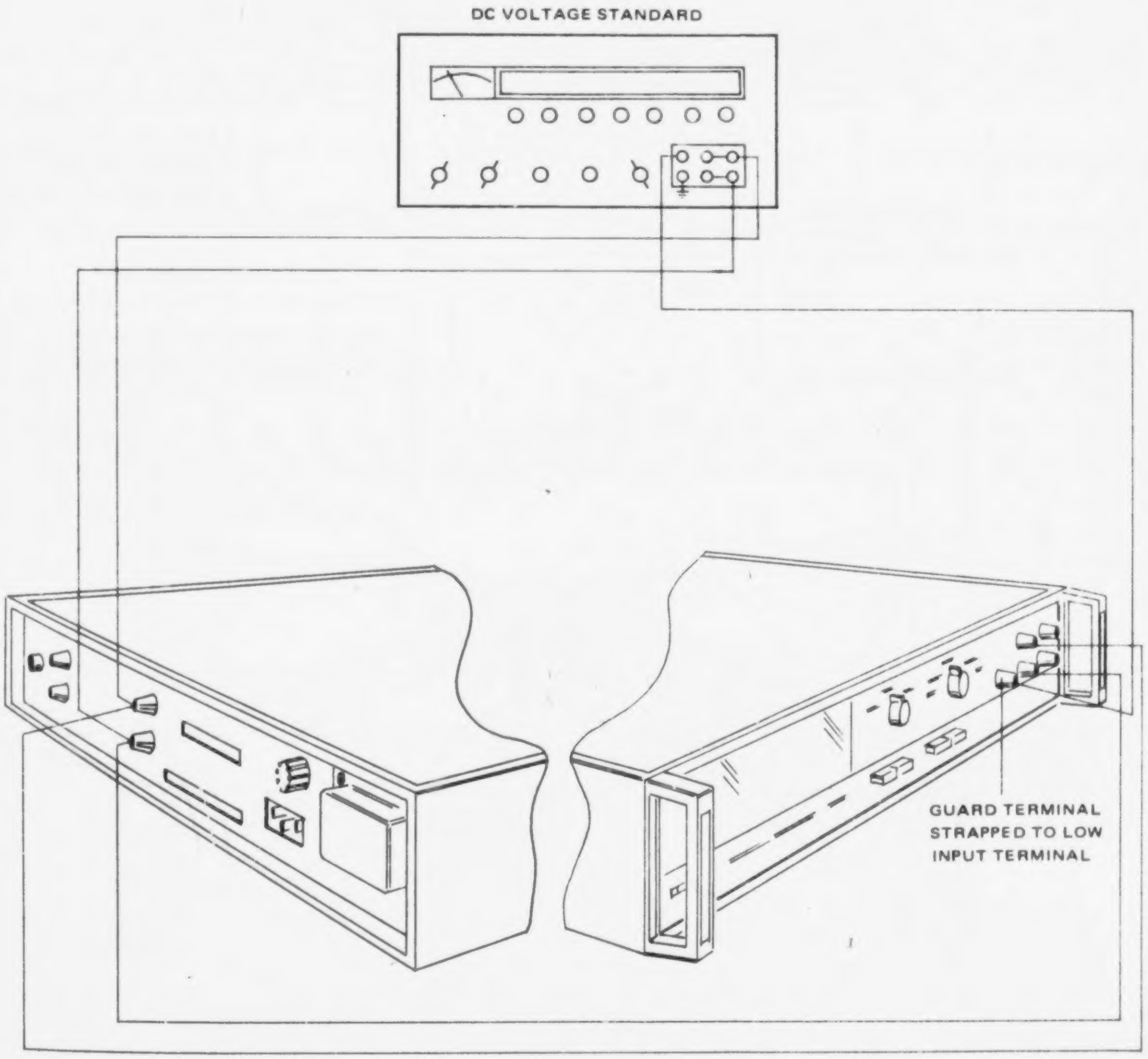

DC VOLTAGE STANDARD
GUARD TERMINAL STRAPPED TO LOW INPUT TERMINAL

Table 3.6 - DC Input Resistance

DVM		INPUT SIGNAL		DVM DISPLAY	TOLERANCE	NOTE
FUNCTION	RANGE	DC VOLTS*	S1			
DC	0.1	.100000	Closed	.100000		Rs
			Open		.099989 – .100001	1 Meg
	1	1.000000	Closed	1.00000		
			Open		0.99989 – 1.00001	1 Meg
	10	10.00000	Closed	10.0000		
			Open		9.9989 – 10.0001	1 Meg
	100	100.0000	Closed	100.000		
			Open		99.989 – 100.001	1 K
	1000	1000.000	Closed	1000.00		
			Open		999.89 – 1000.01	1 K
						*Adjust the DC Voltage Standard to produce the DVM DISPLAY reading.

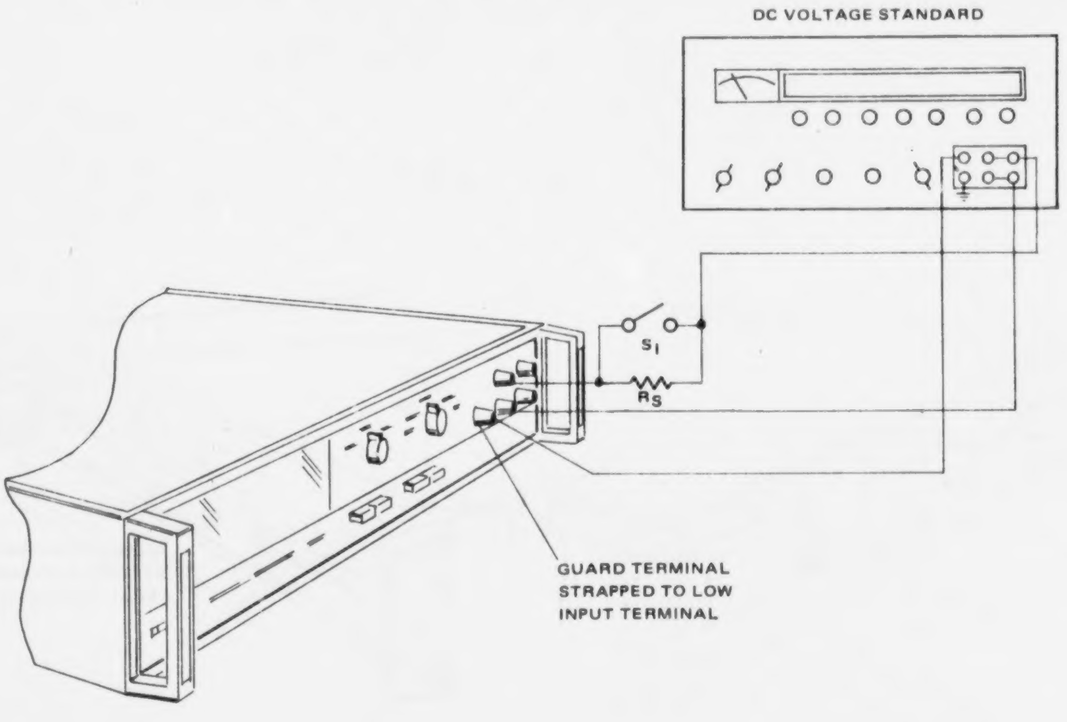

DC VOLTAGE STANDARD
S_1
R_S
GUARD TERMINAL STRAPPED TO LOW INPUT TERMINAL

Table 3.7 - Model 33 AC Converter Range Check

DVM		INPUT SIGNAL				
FUNCTION	RANGE	DC VOLTAGE STANDARD	AC	NOMINAL READING	TOLERANCE	NOTE
AC	1	1.000000	1V @ 400 Hz	1.00000	0.99968 – 1.00032	Refer to figure 5.3 for additional information on use of AC source equipment
		1.000000	1V @ 50 kHz	1.00000	0.99945 – 1.00055	
	10	10.00000	10V @ 400 Hz	10.0000	9.9968 – 10.0032	
		10.00000	10V @ 50 kHz	10.0000	9.9945 – 10.0055	
	100	100.0000	100V @ 400 Hz	100.000	99.968 – 100.032	
		100.0000	100V @ 50 kHz	100.000	99.945 – 100.055	
	1000	1000.000	1000V @ 400 Hz	1000.00	999.48 – 1000.52	
		500.00	500V @ 50 kHz	500.00	499.70 – 500.30	

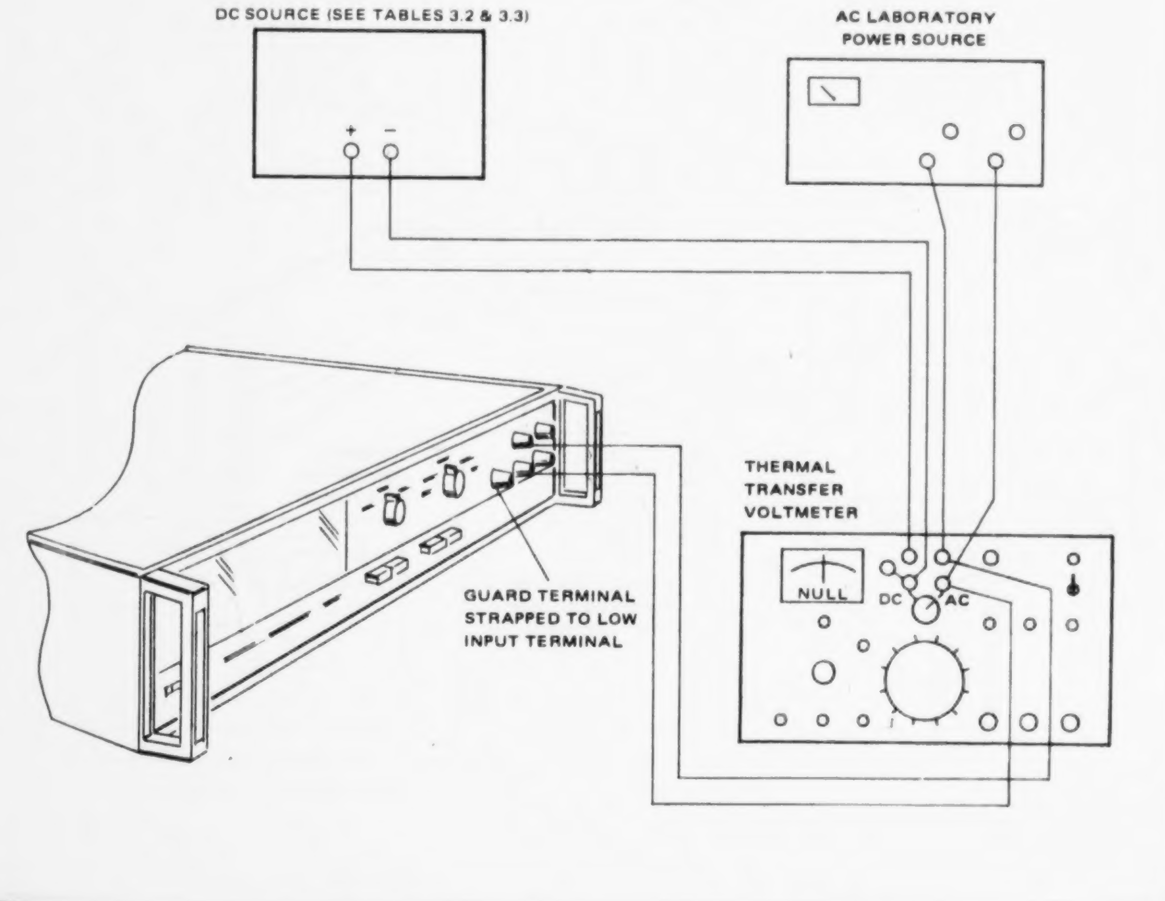

DC SOURCE (SEE TABLES 3.2 & 3.3)
+
–
AC LABORATORY POWER SOURCE
THERMAL TRANSFER VOLTMETER
NULL
DC
AC
GUARD TERMINAL STRAPPED TO LOW INPUT TERMINAL

Table 3.8 - Model 32 AC Converter Range Check

DVM		INPUT SIGNAL				
FUNCTION	RANGE	DC VOLTAGE STANDARD	AC	NOMINAL READING	TOLERANCE	NOTE
AC	1	1.000000	1V @ 400 Hz	1.00000	0.99910 – 1.00090	Same as table 3.7
		1.000000	1V @ 50 kHz	1.00000	.99800 – 1.00200	
	10	10.00000	10V @ 400 Hz	10.0000	9.9910 – 10.0090	
		10.00000	10V @ 50 kHz	10.0000	9.9800 – 10.0200	
	100	100.0000	100V @ 400 Hz	100.000	99.910 – 100.090	
		100.0000	100V @ 50 kHz	100.000	99.800 – 100.200	
	1000	1000.000	1000V @ 400 Hz	1000.00	998.80 – 1001.20	
		500.000	500V @ 50 kHz	500.00	498.50 – 501.50	

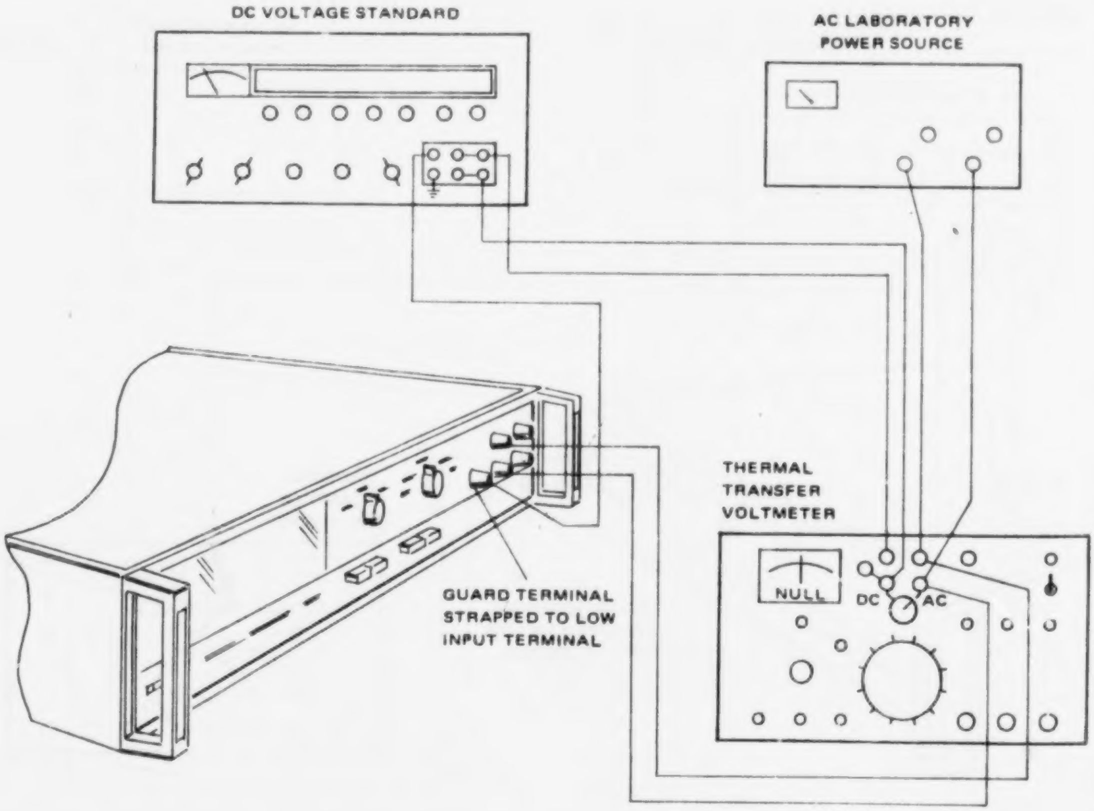

DC VOLTAGE STANDARD
AC LABORATORY POWER SOURCE
THERMAL TRANSFER VOLTMETER
NULL
DC
AC
GUARD TERMINAL STRAPPED TO LOW INPUT TERMINAL

Table 3.9 - Ohms-Megohms Range Check

DVM		INPUT SIGNAL		NOMINAL READING (Same as Standard)	TOLERANCE
FUNCTION	RANGE	NOMINAL STANDARD VALUE	TOLERANCE		
Ω - MΩ	10Ω	10Ω	.001%		±10 digits
	100Ω	100Ω	.0005%		±4 digits
	1 kΩ	1 kΩ	.0005%		±4 digits
	10 kΩ	10 kΩ	.0005%		±4 digits
	100 kΩ	100 kΩ	.0005%		±4 digits
	1 MΩ	1 MΩ	.0005%		±4 digits
	10 MΩ	10 MΩ	.002%		±31 digits
	100 MΩ	100 MΩ	.01%		±51 digits

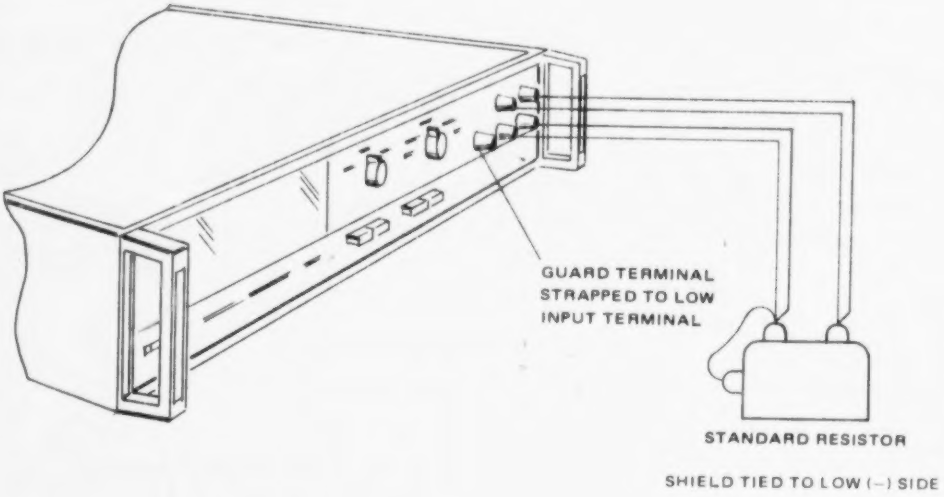
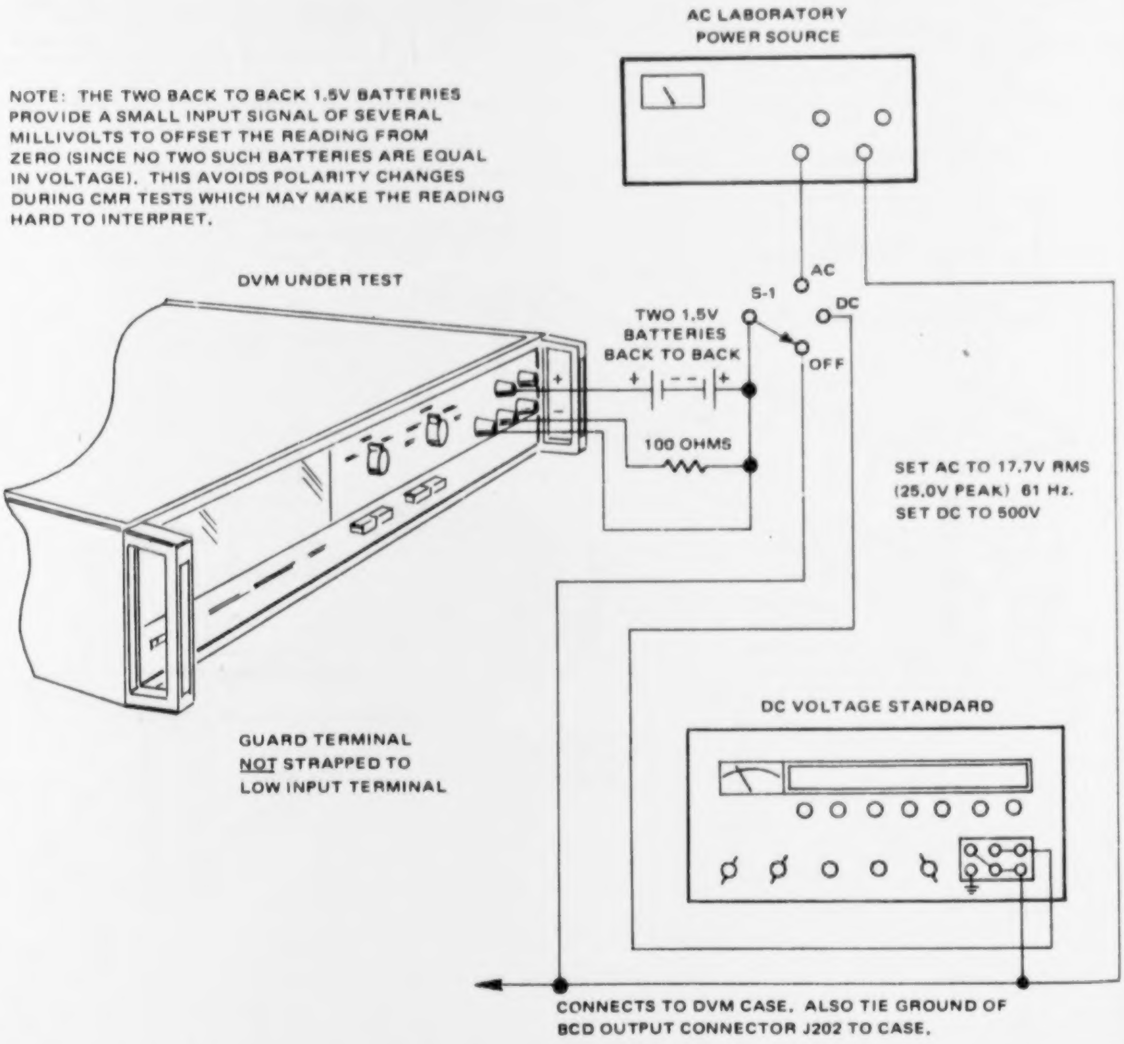

GUARD TERMINAL STRAPPED TO LOW INPUT TERMINAL
STANDARD RESISTOR
SHIELD TIED TO LOW (–) SIDE

Table 3.10 - Common Mode Rejection (In DC Volts Function)

DVM		INPUT SIGNAL		NOMINAL READING See Note 1	TOLERANCE	NOTE
FUNCTION	RANGE	S1				
DCV FILT. OUT	0.1	Off			±1 digit from nominal	1) With switch S-1 in the off position, record the reading displayed on the DMM's readout in the "nominal reading" boxes of the table.
		DC			±5 digits from nominal	
		AC			±25 digits from nominal	
DCV FILT. IN	0.1	DC			±50 digits from nominal	
		AC			±25 digits from nominal	


AC LABORATORY
POWER SOURCE
DVM UNDER TEST
S-1
AC
DC
OFF
TWO 1.5V
BATTERIES
BACK TO BACK
100 OHMS
DC VOLTAGE STANDARD

NOTE: THE TWO BACK TO BACK 1.5V BATTERIES PROVIDE A SMALL INPUT SIGNAL OF SEVERAL MILLIVOLTS TO OFFSET THE READING FROM ZERO (SINCE NO TWO SUCH BATTERIES ARE EQUAL IN VOLTAGE). THIS AVOIDS POLARITY CHANGES DURING CMR TESTS WHICH MAY MAKE THE READING HARD TO INTERPRET.

SET AC TO 17.7V RMS (25.0V PEAK) 61 Hz.
SET DC TO 500V

GUARD TERMINAL NOT STRAPPED TO LOW INPUT TERMINAL

CONNECTS TO DVM CASE. ALSO TIE GROUND OF BCD OUTPUT CONNECTOR J202 TO CASE.

Table 3.11 - Normal Mode Noise Rejection (In DC Volts Function)

DVM		INPUT SIGNAL		NOMINAL READING	TOLERANCE	NOTE
FUNCTION	RANGE	DC	AC			
DC FILT. OUT	10	0.5V	10V*, 60 Hz†	00.5000	±400 digits	
FILT. IN	1V	0.5V	2V*, 60 Hz†	0.50000	± 2 digits	

*peak
†50 Hz (Option 04)

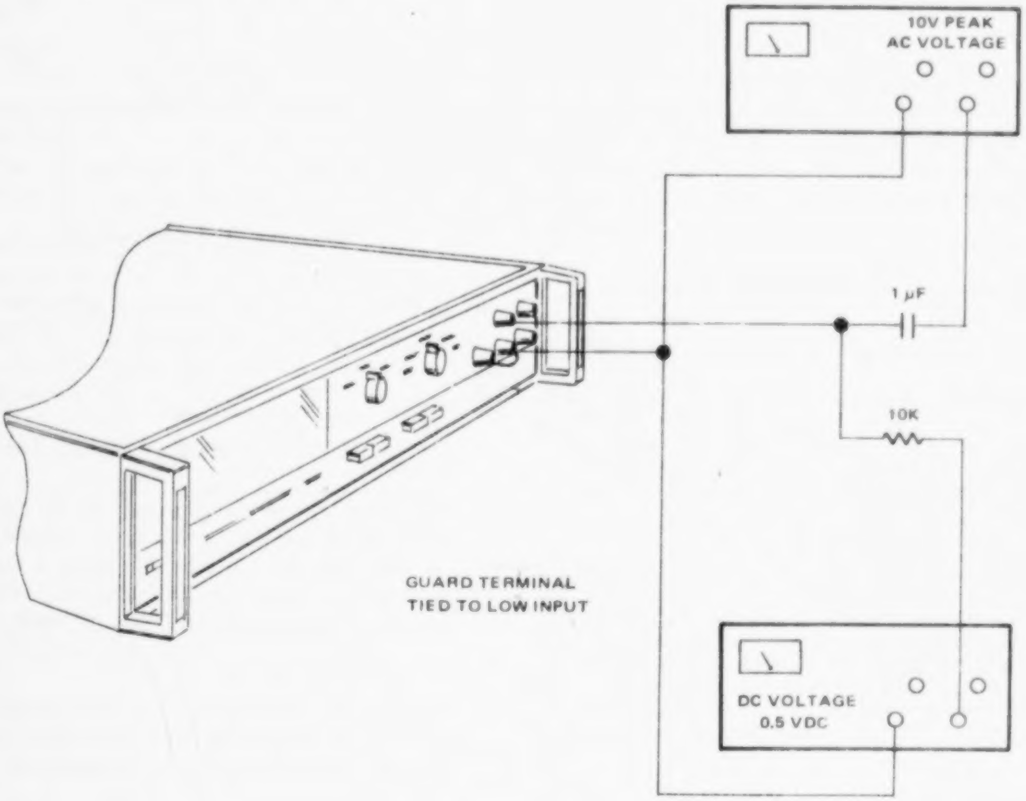
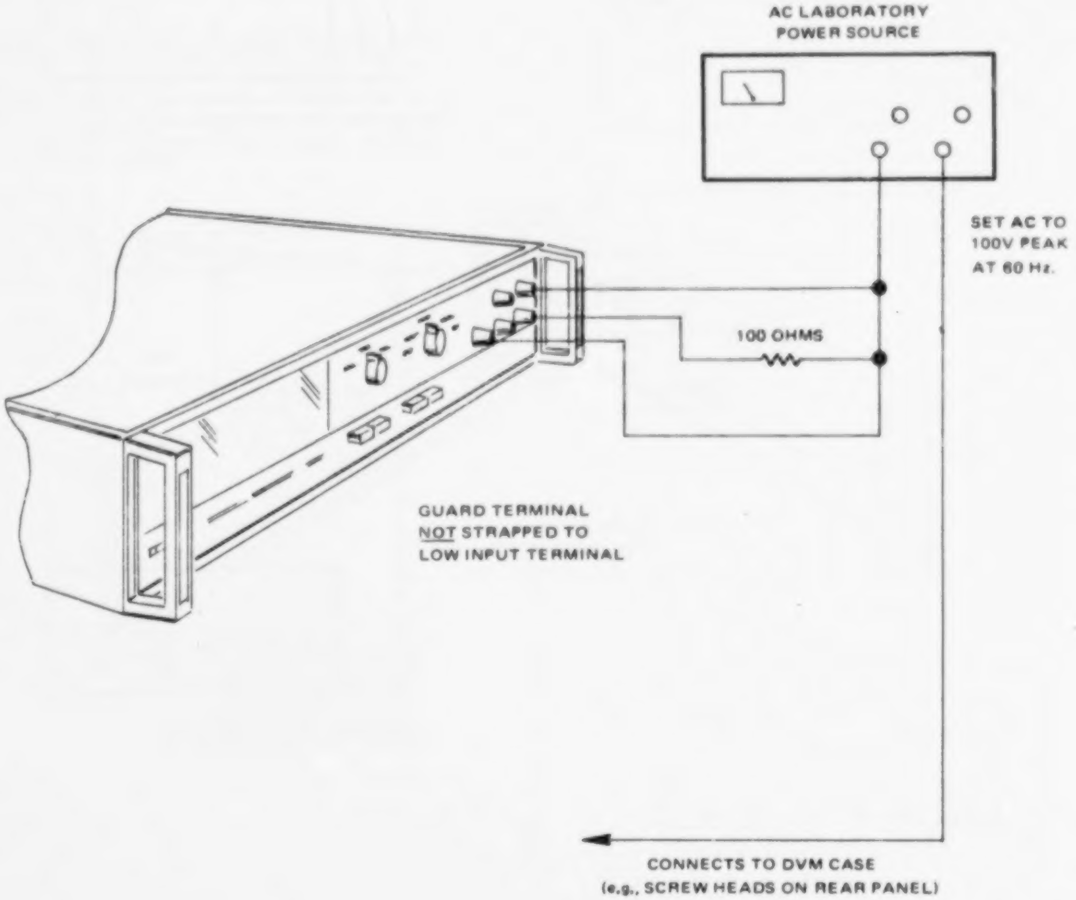

10V PEAK
AC VOLTAGE
1 µF
10K
GUARD TERMINAL
TIED TO LOW INPUT
DC VOLTAGE
0.5 VDC

Table 3.12 - Common Mode Rejection (In AC Volts Function)

DVM		COMMON MODE INPUT SIGNAL		NOMINAL READING (see note)	TOLERANCE	NOTE
FUNCTION	RANGE	VOLTAGE	FREQ.			
AC	1	100V Peak	60 Hz		±10 digits	Enter reading with shorted input in nominal column before beginning
	10	100V Peak	60 Hz		±1 digit	
	100	100V Peak	60 Hz		±1 digit	
	1000	100V Peak	60 Hz		±1 digit	


AC LABORATORY
POWER SOURCE
SET AC TO
100V PEAK
AT 60 Hz.
100 OHMS
GUARD TERMINAL
NOT STRAPPED TO
LOW INPUT TERMINAL
CONNECTS TO DVM CASE
(e.g., SCREW HEADS ON REAR PANEL)

SECTION 4 THEORY OF OPERATION

4.1 GENERAL.

4.2 This section describes the operation of the main circuitry of the Model 5900 DVM, briefly covering the mechanical organization and then the electrical operation of the instrument. The simplified drawings provided in this section are for the purpose of illustration, and supplement the complete schematics of Section 6.

4.3 MECHANICAL DESCRIPTION.

4.4 The Model 5900, shown in figure 4.1, consists of a large, single, printed circuit Logic and Interconnection board (Main Logic) with as many as 12 separate PC boards plugging directly or indirectly (by means of cables) into the Main Logic board. The electronics are housed in a sturdy die cast and stamped aluminum package with the readout, input terminals, and all manual controls located on the front panel. On the back panel is located the power cable input, all data output, optional remote input, analog output, external reference, and optional rear inputs.

4.5 ELECTRICAL DESCRIPTION.

4.6 The instrument is divided into three functional groups. These are the Signal Conditioning section, the Analog-to-Digital Conversion section, and the Display/Control Logic section.

4.7 SIGNAL CONDITIONING SECTION.

4.8 The Signal Conditioning section for a fully equipped instrument consists of: the Switching Board, the Ohms Converter, the Averaging AC Converter or the RMS AC Converter, the Attenuator, and the Isolator. The basic instrument consists of the Switching Bypass Board, the Attenuator, and the Isolator.

4.9 The Signal Conditioning section routes, scales, filters and, when required, converts the input signal into a stable 10 volt full scale dc level for use by the measurement portion of the DVM.

4.10 Switching Board.

4.11 The Switching board is a single printed circuit board and occupies connectors J5 and J6 on the Main Logic board. The Switching board is used with instruments equipped with either the Ohms Converter or either of the available ac converters and is necessary for the generation and isolation of range data and for signal routing required by the options. The interconnection of the Switching board with the options and the other components of the Signal Conditioning section is shown in block diagram form in figure 4.2.

4.12 The control of signal flow through the Switching board, shown in simplified form in figure 4.3, is by means of two relays K1 and K2. The three possible signal routes provided by the board are illustrated in figure 4.4. Referring to this figure, with the dc function selected, neither of the relays is energized and the signal flow is as shown in (a); with the ac function selected, relay K1 is energized and the signal flow is as shown in (b); with ohms function selected, relay K2 is energized and the signal flow is as shown in (c).

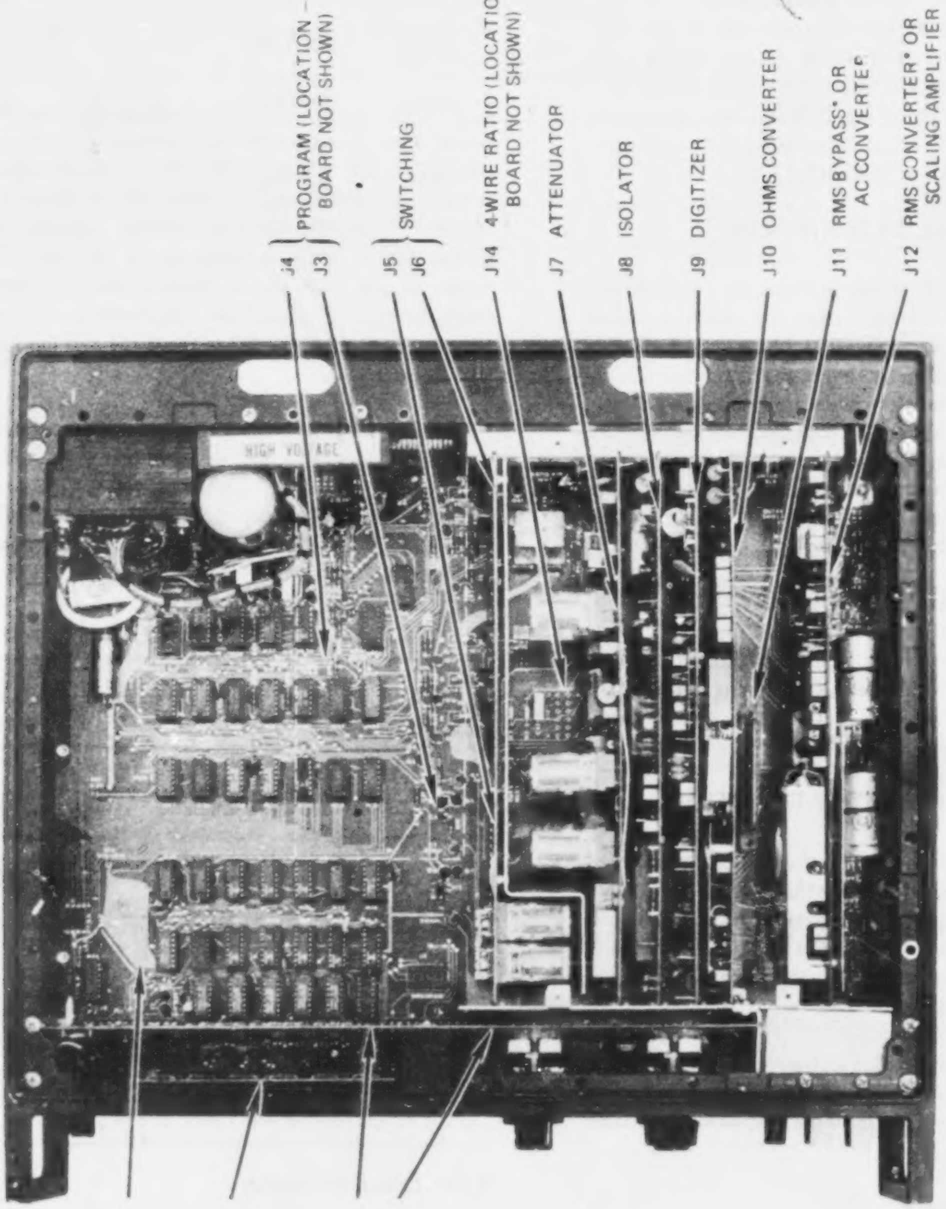
4.13 The range control of the option boards uses analog supply voltages and require isolation from the digital supplies and decoding. This is provided on the Switching board by three Optically Coupled Isolators (OCI) and a BCD to TEN line driver. The decoding of the range logic is given in table 4.1.

Table 4.1 - Switching Board Range Decode

INPUT CODE			WEIGHT	OUTPUT CODE							
RANGE				U2 PIN NO.							RANGE
A_1	B_2	C_4		8	9	13	14	11	10	16	
L	H	L	2	L	H	H	H	H	H	H	.01
H	H	L	3	H	L	H	H	H	H	H	.1
L	L	H	4	H	H	L	H	H	H	H	1
H	L	H	5	H	H	H	L	H	H	H	10
L	H	H	6	H	H	H	H	L	H	H	100
H	H	H	7	H	H	H	H	H	L	H	1000
L	L	L	0	H	H	H	H	H	H	L	10,000

4.14 Ohms Converter.

4.15 The Ohms Converter circuitry is mounted on a single printed circuit board and occupies connector J10 on the Main Logic board. The circuitry, shown simplified in figure 4.5, consists of a high gain amplifier and a relay operated positive current source that is capable of producing any one of eight precise current levels depending on the range selected.



J4 } PROGRAM (LOCATION – BOARD NOT SHOWN)
J3
J5 } SWITCHING
J6
J14 4-WIRE RATIO (LOCATION BOARD NOT SHOWN)
J7 ATTENUATOR
J8 ISOLATOR
J9 DIGITIZER
J10 OHMS CONVERTER
J11 RMS BYPASS* OR AC CONVERTER
J12 RMS CONVERTER* OR SCALING AMPLIFIER
HIGH VOLTAGE
LOGIC AND INTERCONNECTION
READOUT J13
DISPLAY { J1
J2

*INSTRUMENT SHOWN IS EQUIPPED WITH RMS CONVERTER.

Figure 4-1. Mechanical Assembly

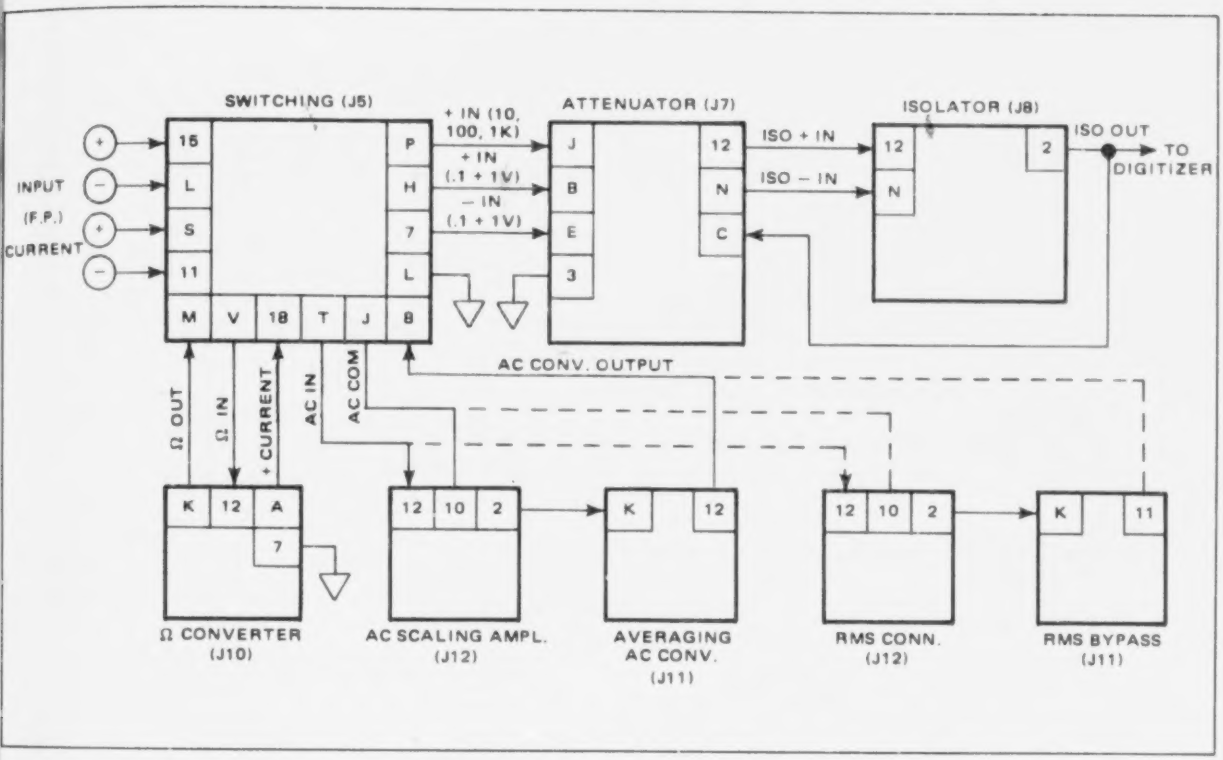

SWITCHING (J5)
15
L
S
11
P
H
7
L
M
V
18
T
J
8
INPUT
(F.P.)
CURRENT
+ IN (10, 100, 1K)
+ IN (.1 + 1V)
– IN (.1 + 1V)
ATTENUATOR (J7)
J
B
E
3
12
N
C
ISO + IN
ISO – IN
ISOLATOR (J8)
12
N
2
ISO OUT
TO DIGITIZER
AC CONV. OUTPUT
Ω OUT
Ω IN
+ CURRENT
AC IN
AC COM
K
12
A
7
Ω CONVERTER (J10)
12
10
2
AC SCALING AMPL. (J12)
K
12
AVERAGING AC CONV. (J11)
12
10
2
RMS CONN. (J12)
K
11
RMS BYPASS (J11)

Figure 4.2 - Signal Flow, Loaded DVM

4.16 The Ohms Converter, in conjunction with the Switching board, the Attenuator, and the Isolator, converts the resistance to be measured into a proportional dc voltage that can be measured by the DVM. The current produced for each range, when applied to a resistance equal to the range selected, generates a voltage at the output of the resistance measurement network of 10 volts.

4.17 The resistance measurement network assumes two forms, depending on the range being measured, as shown in figure 4.6. The basic technique used consists of effectively connecting the resistance to be measured (Rx) as a negative feedback path of an operational amplifier, with the amplifier provided by the Ohms Converter and the input to the operational amplifier formed by the current generator. In this hookup, the current through Rx equals the current generated by the current source. Referring to configuration (a), used for the ranges 10 kilohm through 100 megohm, the voltage developed across Rx is monitored through the – IN front panel connector and buffered by the isolator in a gain-of-one mode. The full scale output for these ranges, as shown in the table, is –10 volts. Referring to figure (b), the isolator is connected in a potentiometric configuration but with the input at an effective ground potential and the reference end of the feedback network tied to the voltage developed across Rx. The low impedance provided by the isolator feedback network is offset by the current compensation circuit (c.c.). This circuit monitors the output potential of the isolator and produces a current drain precisely equal to the current produced by the feedback network. The result is that the feedback network appears to have an extremely high input impedance. The configuration used in (b) reduces normal the gain of the isolator by 1, thereby producing gains of 99 and 9 as shown in the table.

4.18 Scaling Amplifier.

4.19 The Scaling Amplifier, shown simplified in figure 4.7, is used in conjunction with the Averaging AC Converter and occupies position J12. It provides isolation between the signal being measured and the signal converting circuitry and it scales the input signal to a level suitable for the converter (1V RMS output for a full scale input).

4.20 The Scaling Amplifier consists of an AC coupled operational amplifier with four possible gain settings (X1, X.1, X.01, and X.001). The gain control network is controlled by three relays and these are operated from decoded data from the range logic. A change in the roll off of the amplifier is provided when the 1 volt range is selected.

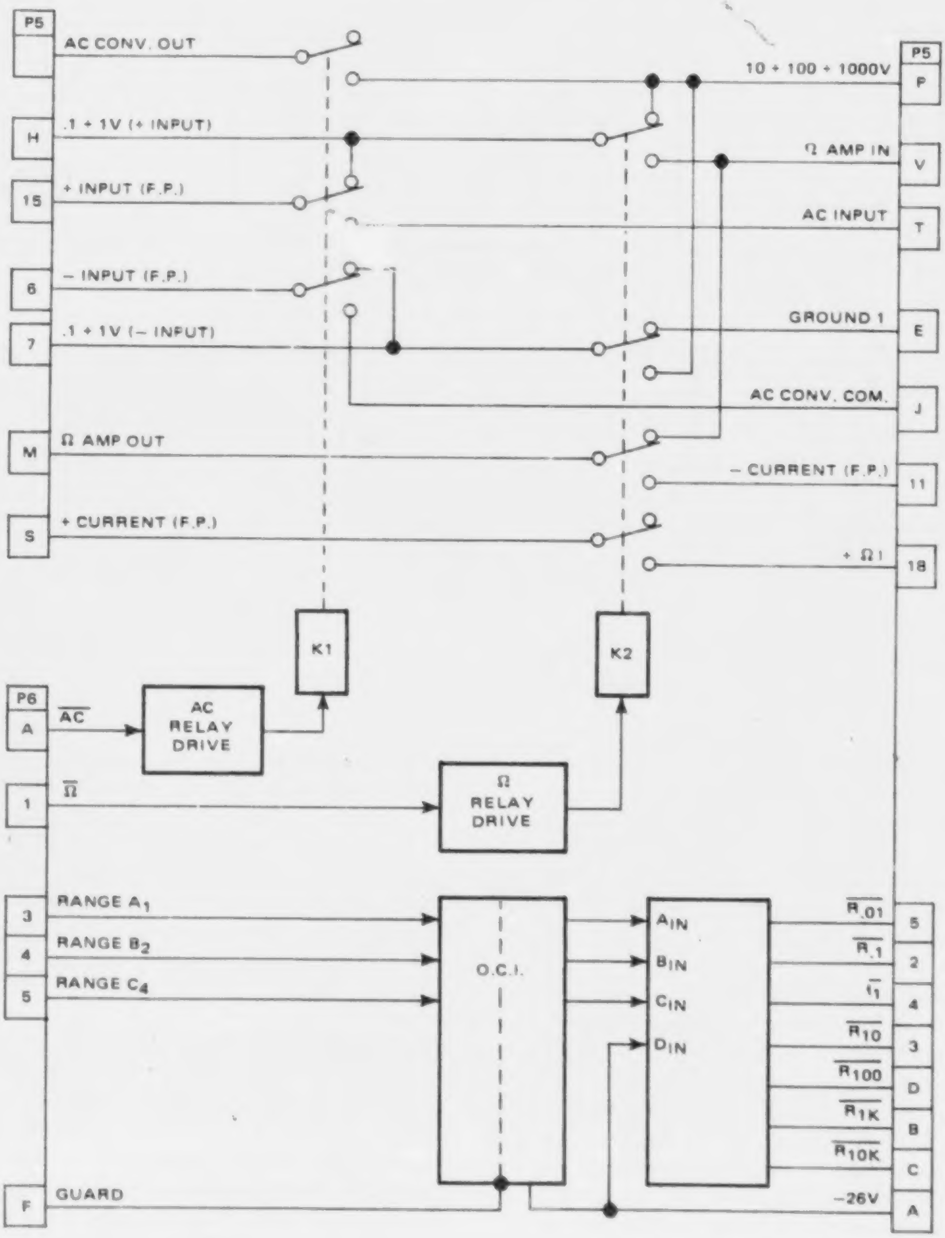
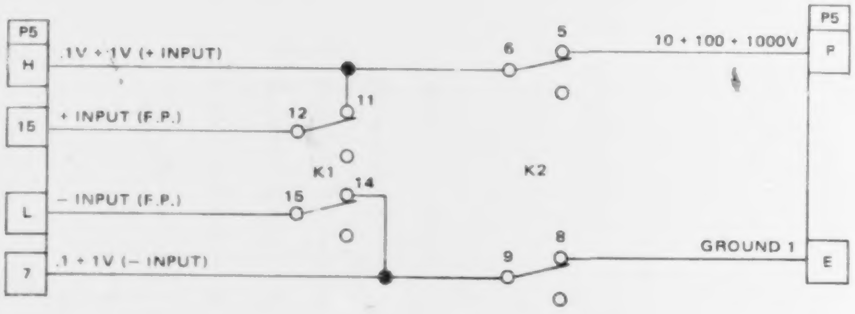
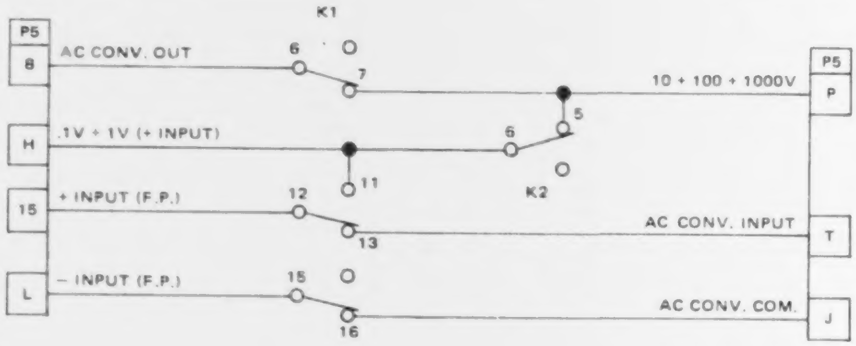

P5
AC CONV. OUT
H
.1 + 1V (+ INPUT)
15
+ INPUT (F.P.)
6
− INPUT (F.P.)
7
.1 + 1V (− INPUT)
M
Ω AMP OUT
S
+ CURRENT (F.P.)
P5
10 + 100 + 1000V
P
Ω AMP IN
V
AC INPUT
T
GROUND 1
E
AC CONV. COM.
J
− CURRENT (F.P.)
11
+ Ω I
18
K1
K2
P6
A
$\overline{AC}$
AC RELAY DRIVE
1
$\overline{\Omega}$
Ω RELAY DRIVE
3
RANGE A_1
4
RANGE B_2
5
RANGE C_4
O.C.I.
A_{IN}
B_{IN}
C_{IN}
D_{IN}
$\overline{R_{.01}}$
5
$\overline{R_{.1}}$
2
$\overline{I_1}$
4
$\overline{R_{10}}$
3
$\overline{R_{100}}$
D
$\overline{R_{1K}}$
B
$\overline{R_{10K}}$
C
−26V
A
F
GUARD

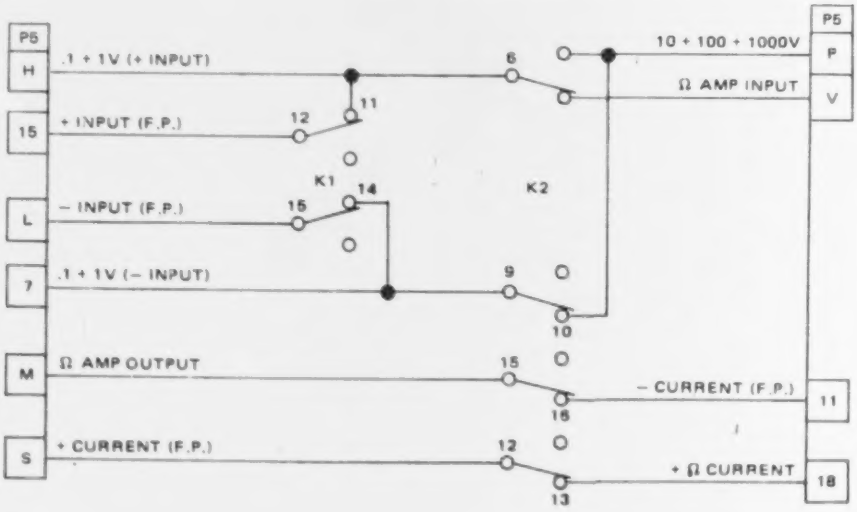
Figure 4.3 - Switching Board


P5
H
.1V + 1V (+ INPUT)
15
+ INPUT (F.P.)
L
– INPUT (F.P.)
7
.1 + 1V (– INPUT)
K1
12
11
15
14
K2
6
5
9
8
P5
10 + 100 + 1000V
P
GROUND 1
E

(a) DC Routing

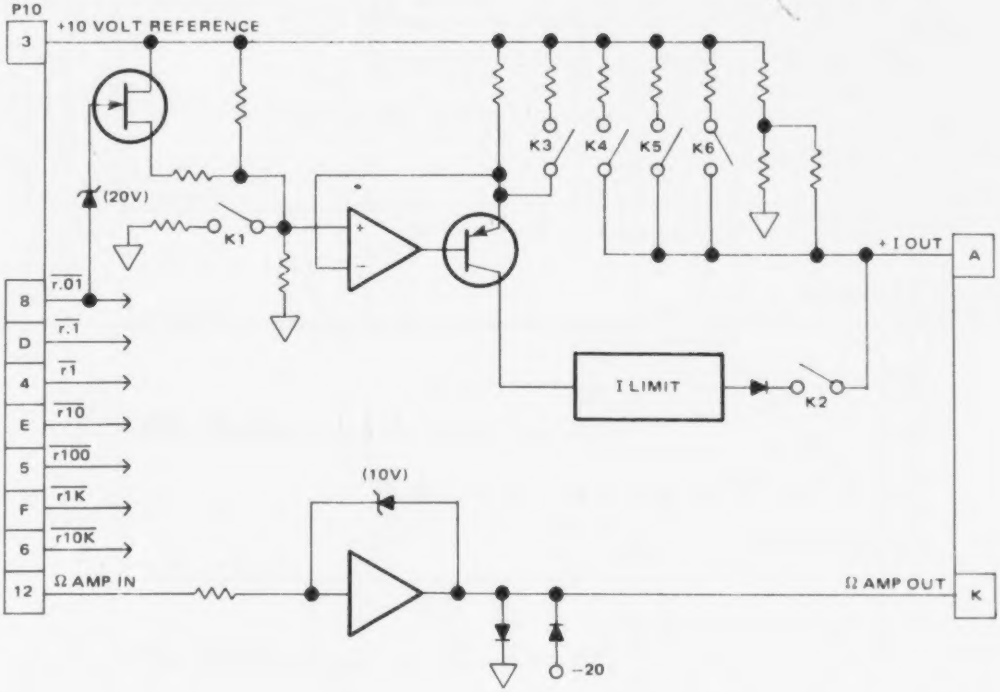

K1
P5
8
AC CONV. OUT
6
7
H
.1V + 1V (+ INPUT)
11
15
+ INPUT (F.P.)
12
13
L
– INPUT (F.P.)
15
16
K2
6
5
P5
10 + 100 + 1000V
P
AC CONV. INPUT
T
AC CONV. COM.
J

(b) AC Routing


P5
H
.1 + 1V (+ INPUT)
15
+ INPUT (F.P.)
L
– INPUT (F.P.)
7
.1 + 1V (– INPUT)
M
Ω AMP OUTPUT
S
+ CURRENT (F.P.)
K1
12
11
15
14
K2
6
9
10
15
16
12
13
P5
10 + 100 + 1000V
P
Ω AMP INPUT
V
– CURRENT (F.P.)
11
+ Ω CURRENT
18

(c) Ω Routing

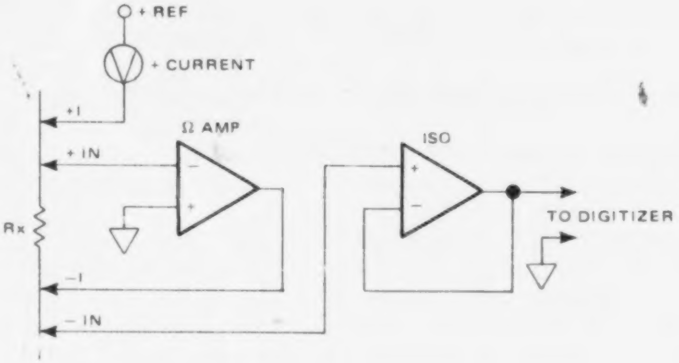
Figure 4.4 - Signal Flow of Switching Board


P10
3
+10 VOLT REFERENCE
(20V)
K1
K3
K4
K5
K6
+
−
+ I OUT
A
8
$\overline{r.01}$
D
$\overline{r.1}$
4
$\overline{r1}$
E
$\overline{r10}$
5
$\overline{r100}$
F
$\overline{r1K}$
6
$\overline{r10K}$
I LIMIT
K2
(10V)
12
Ω AMP IN
Ω AMP OUT
K
−20

RANGE	K1	K2	K3	K4	K5	K6
r.01		▨	▨	▨		
r.1	▨	▨	▨			
r1	▨	▨				
r10		▨				
r100				▨		
r1K					▨	
r10K						▨

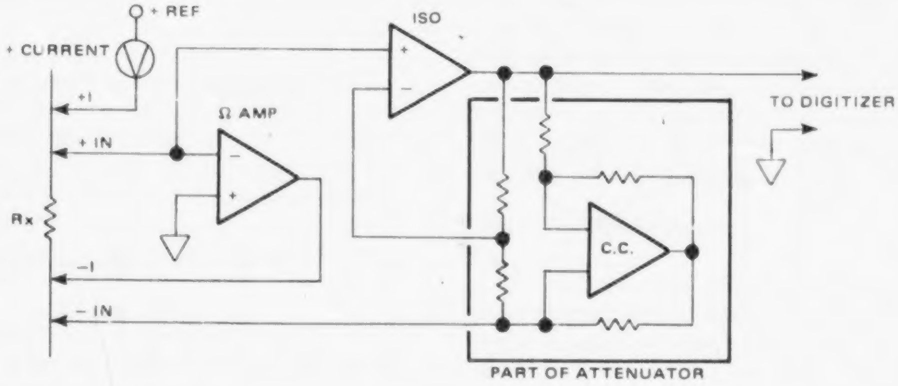
▨ RELAY ENERGIZED

Figure 4.5 - Ohms Converter


+ REF
+ CURRENT
+I
Ω AMP
+ IN
ISO
Rx
TO DIGITIZER
–I
– IN

RANGE	CURRENT	FULL SCALE OUT
10 KΩ	1 mA	–10V
100 KΩ	100 μA	
1000 KΩ	10 μA	
10 MΩ	1 μA	
100 MΩ	100 nA	

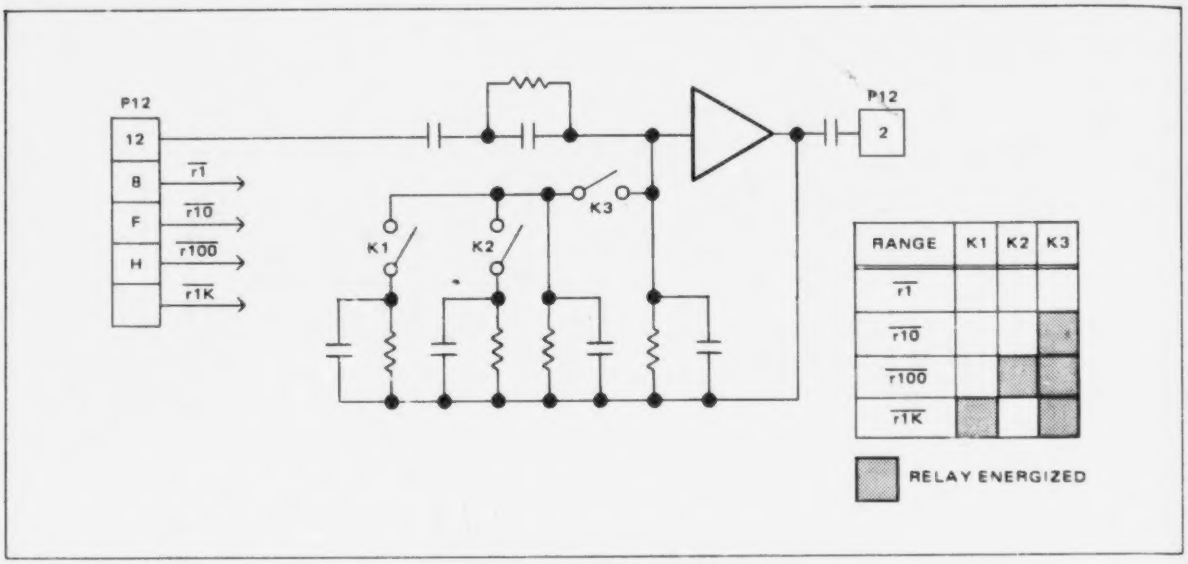
(a)


+ REF
+ CURRENT
ISO
+I
Ω AMP
TO DIGITIZER
+ IN
Rx
C.C.
–I
– IN
PART OF ATTENUATOR

RANGE	CURRENT	GAIN	F.S. OUT
10Ω	10.1 mA	99	+10V
0.1 KΩ	11.1111 mA	9	+10V
1 KΩ	1.11111 mA	9	+10V

(b)

Figure 4.6 - Ohms Measurement Systems


P12
12
8
F
H
$\overline{r1}$
$\overline{r10}$
$\overline{r100}$
$\overline{r1K}$
K1
K2
K3
P12
2

RANGE	K1	K2	K3
$\overline{r1}$			
$\overline{r10}$			▨
$\overline{r100}$		▨	▨
$\overline{r1K}$	▨		▨

▨ RELAY ENERGIZED

Figure 4.7 - Scaling Amplifier

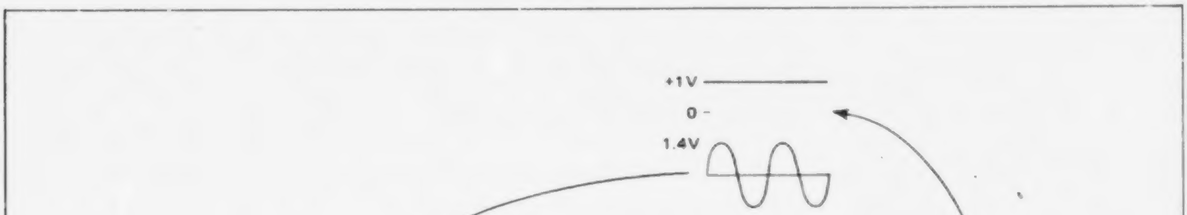

+1V
0 -
1.4V
-2.8V P
5K
P11
K
2.5K
5K
10K
SUMMING AMPLIFIER
10K
P11
12
10K
+20
AC ZERO (F.P.)
C
11.1K
100M
-20
FILTER

Figure 4.8 - Averaging AC Converter

4.21 Averaging AC Converter.

4.22 The Averaging AC Converter receives the scaled input from the Scaling Amplifier and generates a positive dc equivalent for the DVM measurement section. The circuitry is mounted on a single printed circuit board and occupies location J11 on the Main Logic board. The circuitry, shown simplified in figure 4.8, consists of an active rectifier, a summing amplifier, and an active filter circuit.

4.23 Referring to figure 4.8, a full scale input (1.414 volts peak) is shown applied to the input of the active rectifier. The active rectifier consists of an operational amplifier having two polarity selective feedback loops (one positive, one negative), and the closed loop gain of the circuits is set at 2. The output of the negative going feedback loop is fed through a resistor to the summing node of the summing amplifier. Also fed to this summing node and through a resistor is the full wave input signal. The summing amplifier adds the two inputs, averages them and multiplies the signal by 1.11 to produce the dc equivalent of the RMS input, or 1 volt dc. The ripple content is attenuated by the summing amplifier. A full scale input of 100 Hz would result in a ripple at the summing amplifier output of ≈50 millivolts. Further ripple attenuation is provided by the active 3-pole filter. For the same example, the filter would increase the ripple attenuation to ≈5 millivolts.

4.24 RMS AC Converter.

4.25 The circuitry is shown simplified in figure 4.9. The Scaling Amplifier consists of an operational amplifier with a fixed input impedance and four feedback paths. The feedback path for the one volt range is permanently wired into the circuit; the 10, 100, and 1000 volt range feedback paths are connected in parallel to the 1 volt feedback path by relays K1, K2, and K3. The scaling amplifier supplies an accurate 1 volt output on each range selected with full range inputs applied.

4.26 The active rectifier is an operational amplifier with two polarity selective feedback paths (one conducting only with a positive amplifier output and the other conducting only with a negative amplifier output). The voltage developed across the positive conducting leg is applied through a 10K resistor to the summing node of the log amplifier along with the full wave signal through a 20K resistor from the output of the scaling amplifier. These two inputs are combined and, due to the non-linear feedback loop of the log amplifier (consisting of Q14A and Q15A) produce the log of the combined signal at ⓒ. This signal is fed through an identical path (consisting of transistors Q15B and Q14B) to the input of the summing amplifier.

4.27 The summing amplifier converts the signal to a dc level. The ripple content of the dc level is attenuated and the output reduced to provide a dc equivalent of the RMS value of the ac input signal.

4.28 Attenuator.

4.29 The Attenuator board is a single printed circuit board and occupies connector J7 on the Main Logic board. The Attenuator consists of a relay controlled voltage divider used on dc function that provides selectable scaling factors of X1, X.1, and X.01; a relay controlled feedback network used in conjunction with the isolator that provides selectable gain factors of X1, X10, and X100; and current compensation circuitry used in conjunction with the ohms converter.

4.30 Isolator.

4.31 The Isolator is a single printed circuit board and occupies connector J8 on the Main Logic board. The Isolator consists of a high open-loop-gain amplifier, a bootstrap amplifier, and a 3-pole filter. The amplifier is used potentiometrically and operates in conjunction with the attenuator board as shown in figure 4.10.

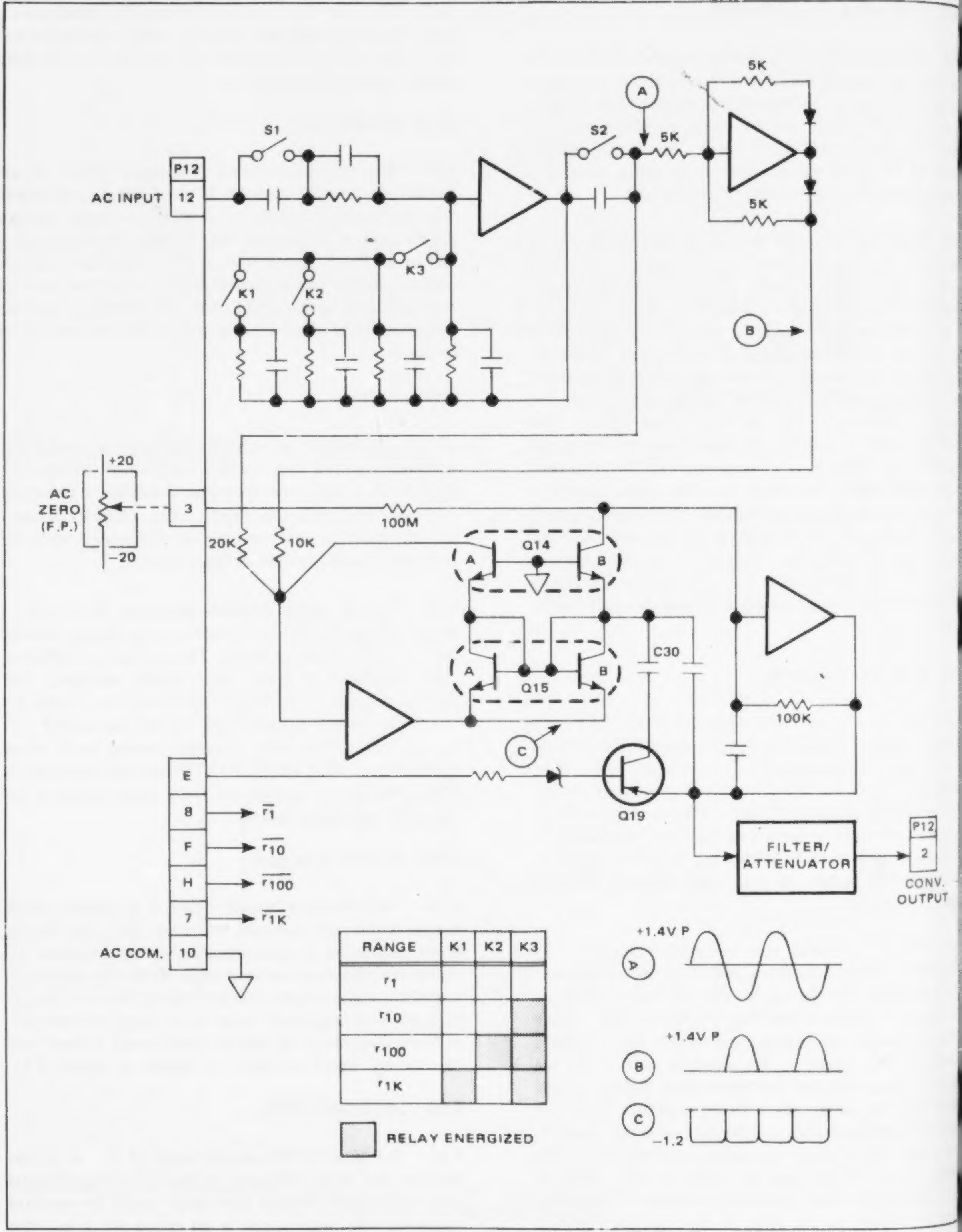
4.32 The bootstrap amplifier generates the + and – supply voltages for the input stages of the isolator, causing them to track the input signal. This results in an effective input impedance of greater than 10,000 megohms. The filter is an active 3-pole Bessel type connected by relay K1 to a point between the gain stage and the post amplifier of the isolator. The filter increases normal mode noise rejection from 10 to 48 dB (at 59 Hz) when K1 is energized. Filter selection is through the front panel switch or the remote programming option.

4.33 Switching Bypass.

4.34 The Switching Bypass board is a printed circuit jumper board and occupies connector J5. The Bypass board replaces the Switching board when the Ohms and AC converter options are not used in the DVM. The signals are routed through the Bypass board in exactly the same manner as in the Switching board when the dc function is selected. A block diagram of the Signal Conditioning section with the Bypass board installed is shown in figure 4.11.

4.35 INTEGRATION.

4.36 The Model 5900 uses an improved version of the standard dual slope integration method of analog-to-digital conversion, called delayed dual slope. As in the standard technique, the input signal is integrated for a set time


S1
S2
A
5K
5K
5K
P12
AC INPUT
12
K3
K1
K2
B
+20
AC
ZERO
(F.P.)
3
-20
20K
10K
100M
Q14
A
B
A
B
Q15
C30
100K
C
E
Q19
8
F
H
7
AC COM.
10
$\overline{r_1}$
$\overline{r_{10}}$
$\overline{r_{100}}$
$\overline{r_{1K}}$
FILTER/
ATTENUATOR
P12
2
CONV.
OUTPUT
+1.4V P
A
+1.4V P
B
C
-1.2

RANGE	K1	K2	K3
r1			
r10			▨
r100		▨	▨
r1K	▨		▨

▨ RELAY ENERGIZED

Figure 4.9 - RMS AC Converter

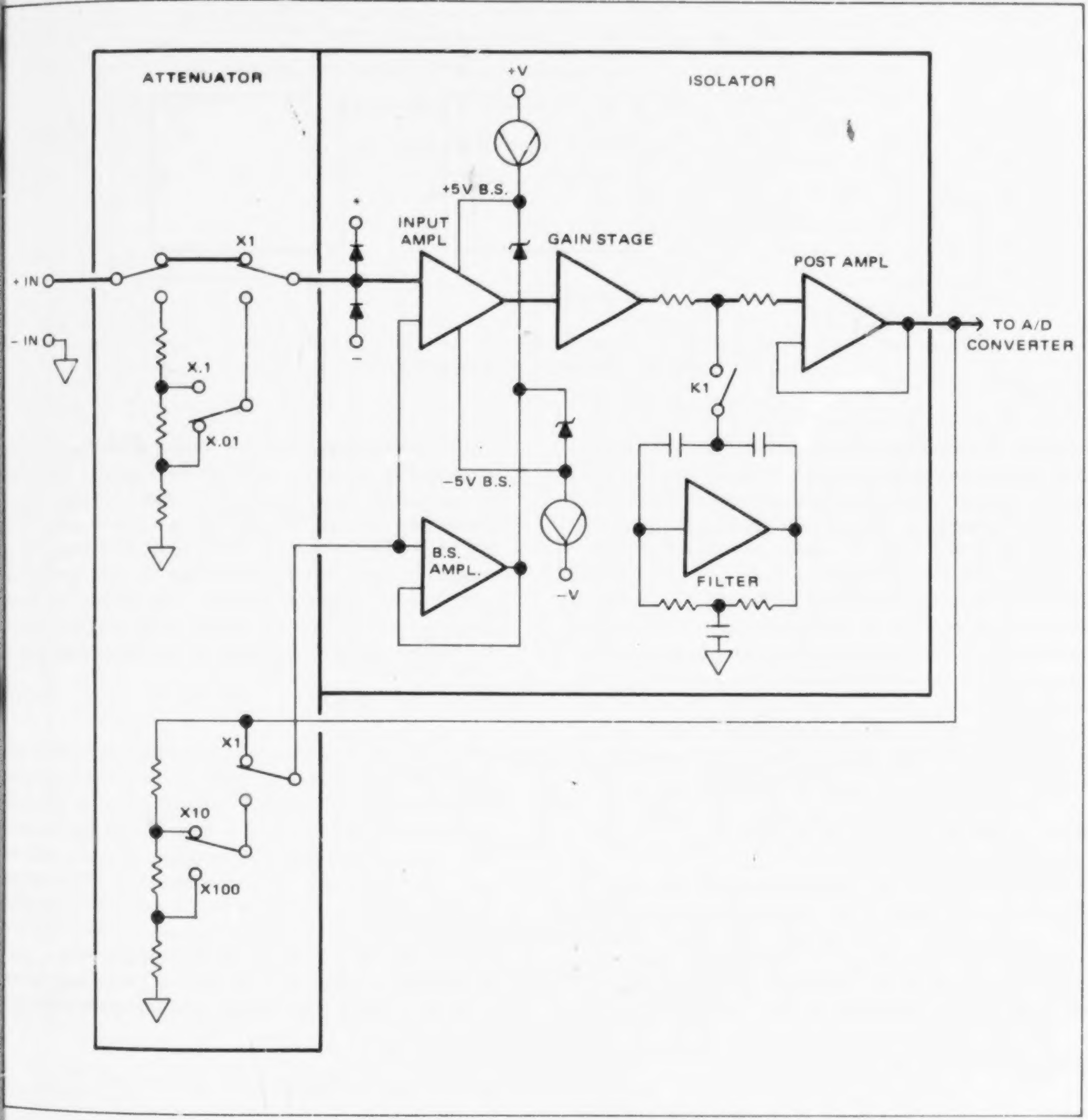

ATTENUATOR
ISOLATOR
+V
+5V B.S.
+
INPUT AMPL
GAIN STAGE
POST AMPL
X1
+IN
-IN
-
X.1
X.01
K1
TO A/D CONVERTER
-5V B.S.
B.S. AMPL.
-V
FILTER
X1
X10
X100

Figure 4.10 - Attenuator/Isolator DC

period to produce a voltage that is directly proportional to he input signal. At the conclusion of this signal integration period, the input signal is removed from the integrator and eplaced by a fixed reference voltage whose polarity is pposite that of the input signal. This causes the integrate utput voltage to discharge at a linear and fixed rate for a ime period that is directly proportional to the voltage developed during the signal integration period. This eference integration time period is measured and displayed in the instrument readout. The transition from signal to reference integration in the A to D sequence, is accompanied by switch noise in the form of spikes. Due to this and normal mode noise which may accompany the input signal, the standard dual slope technique is subject to noise induced errors with input signals at or near zero. The delayed dual slope technique used in the Model 5900 DVM minimizes this source of measurement error by generating a period of non-measurement or delay between the two integration

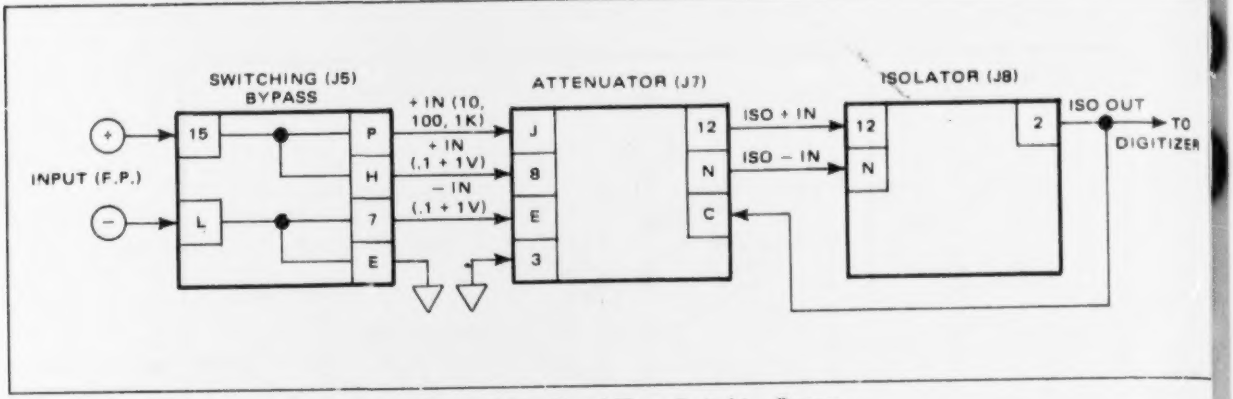


Figure 4.11 - Signal Flow, Switching Bypass

periods. The circuitry used to perform this task include both the Digitizer and portions of the Display/Logic section. In the Digitizer, shown in simplified form in figure 4.12, this is reflected as an adjustment to the basic shape of the integrator output signal as shown in figure 4.13. The 'delay' portion of the integration cycle is initiated immediately following the signal integrate period and is the result of two factors. First, an analog level equal to 180 digits and of a polarity that resists the axis crossing from occurring, is produced at the integrator output by a network called the feed forward circuitry. Second, a capacitor in the feedback network of the gain stage that follows the integrator adds to the total delay period. The total effect is a fixed delay of the axis crossing signal of 200 digits.

4.37 Digitizer.

4.38 The circuitry that performs the A/D conversion is mounted on the single, printed circuit Digitizer board and occupies connector J9 on the Main Logic board. The circuitry includes: an integrator and switching network, a gain stage, null detector, transformer driver, signal and reset logic, switch controls and + and − reference supplies.

4.39 INTEGRATOR.

4.40 The Integrator consists of an operational amplifier with a capacitive feedback path to convert the dc levels applied to its input to a corresponding ramp voltage at the integrator output. The use of a dual FET input stage provides a correspondingly high input impedance and permitting capacitor input coupling during the integration period and allowing auto-zeroing during reset. The feed forward circuitry is a voltage divider network that takes a portion of the reference voltage and applying it to the non-inverting input of the integrator. This results in an equal amount of voltage appearing at the integrator output.

4.41 The integrator switches consist of junction FET controlled by circuitry located on the Digitizer. Referri to figure 4.12, switch S1 conducts during the signal int gration period, switch S2 or S3 during reference integratio and S4 and S5 during reset. The output of the integrator coupled to the following Gain Stage through two FET connected as current generators. The purpose of the generators is to prevent loading of the Integrator whi maintaining reasonable coupling to the gain stage inpu

4.42 GAIN STAGE.

4.43 The gain stage greatly increases the amplitude of th integrator output especially in the area near zero to provi a more clearly defined axis crossing signal for the nu detector to respond to. The gain stage also exhibits low pa filter characteristics which, as previously discussed, adds the total delay of the integration sequence. This reduc the total noise response of the system and reduces the wi band response requirements of the null detector. The c cuit consists of a potentiometric amplifier with a pr grammed gain control, providing gains of 300 for stage inpu of up to 13 mV and decreasing to 14 for signals above th level.

4.44 NULL DETECTOR.

4.45 The Null Detector consists of an inverting, ope loop amplifier with an output swing of 5 volts. The Nu Detector converts the amplified analog signal (ramp) fro the integrator to a digital level (logic 1 ≂ +5 volts and lo 0 ≂ 0 volts). This digital signal, called either the nu detector output or axis signal is used on the Digitizer boa for determining the polarity of the reference to be used f the reference integration and, by way of the transfor driver and transformer T1, the circuitry of the Displa Logic section of the main assembly.

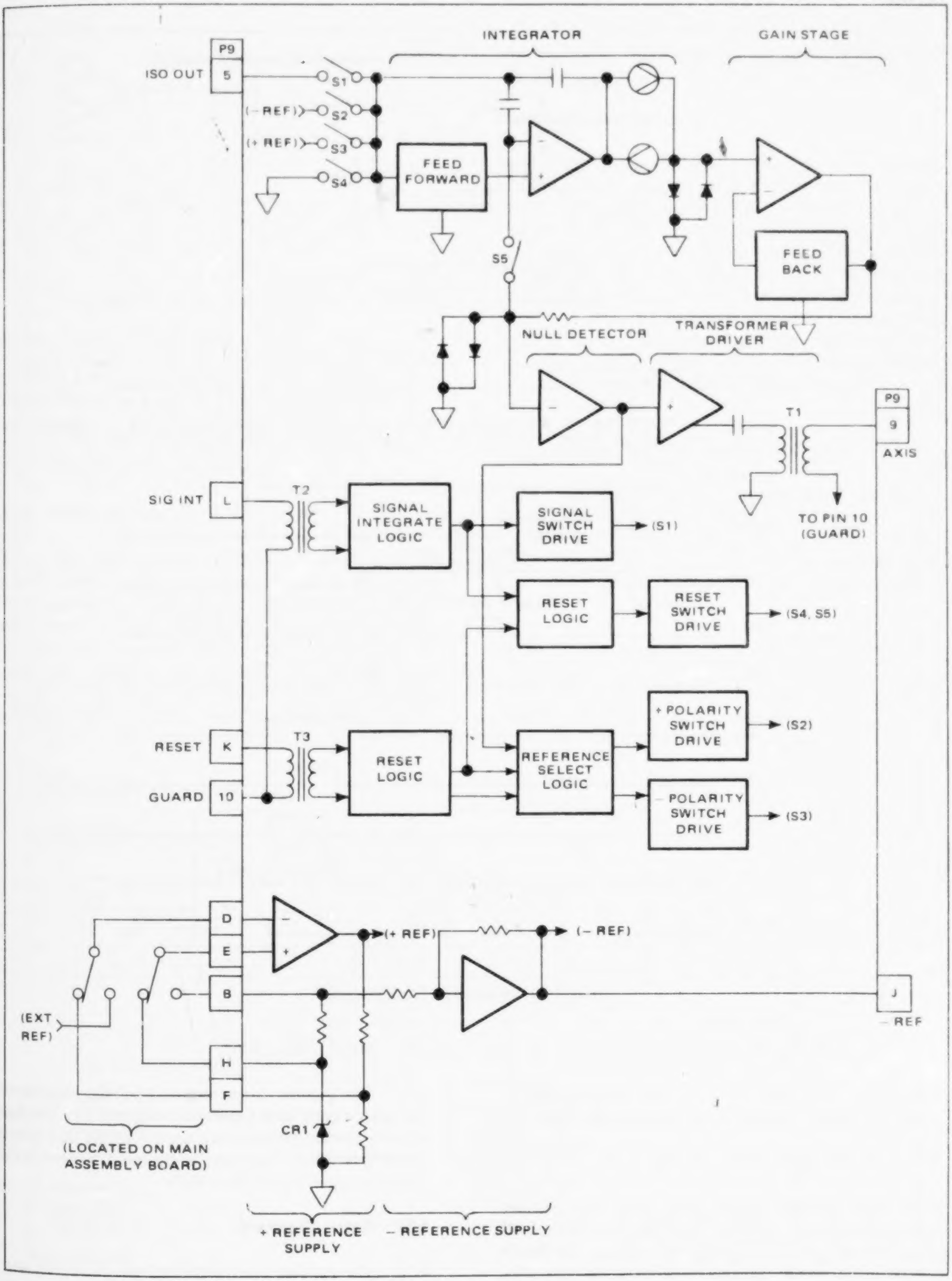

INTEGRATOR
GAIN STAGE
P9
ISO OUT
5
S1
(−REF)
S2
(+REF)
S3
S4
FEED FORWARD
S5
FEED BACK
NULL DETECTOR
TRANSFORMER DRIVER
T1
P9
9
AXIS
TO PIN 10 (GUARD)
SIG INT
L
T2
SIGNAL INTEGRATE LOGIC
SIGNAL SWITCH DRIVE
(S1)
RESET LOGIC
RESET SWITCH DRIVE
(S4, S5)
+ POLARITY SWITCH DRIVE
(S2)
RESET
K
T3
RESET LOGIC
REFERENCE SELECT LOGIC
GUARD
10
− POLARITY SWITCH DRIVE
(S3)
D
E
B
H
F
(+ REF)
(− REF)
J
− REF
(EXT REF)
CR1
(LOCATED ON MAIN ASSEMBLY BOARD)
+ REFERENCE SUPPLY
− REFERENCE SUPPLY

Figure 4.12 - Digitizer

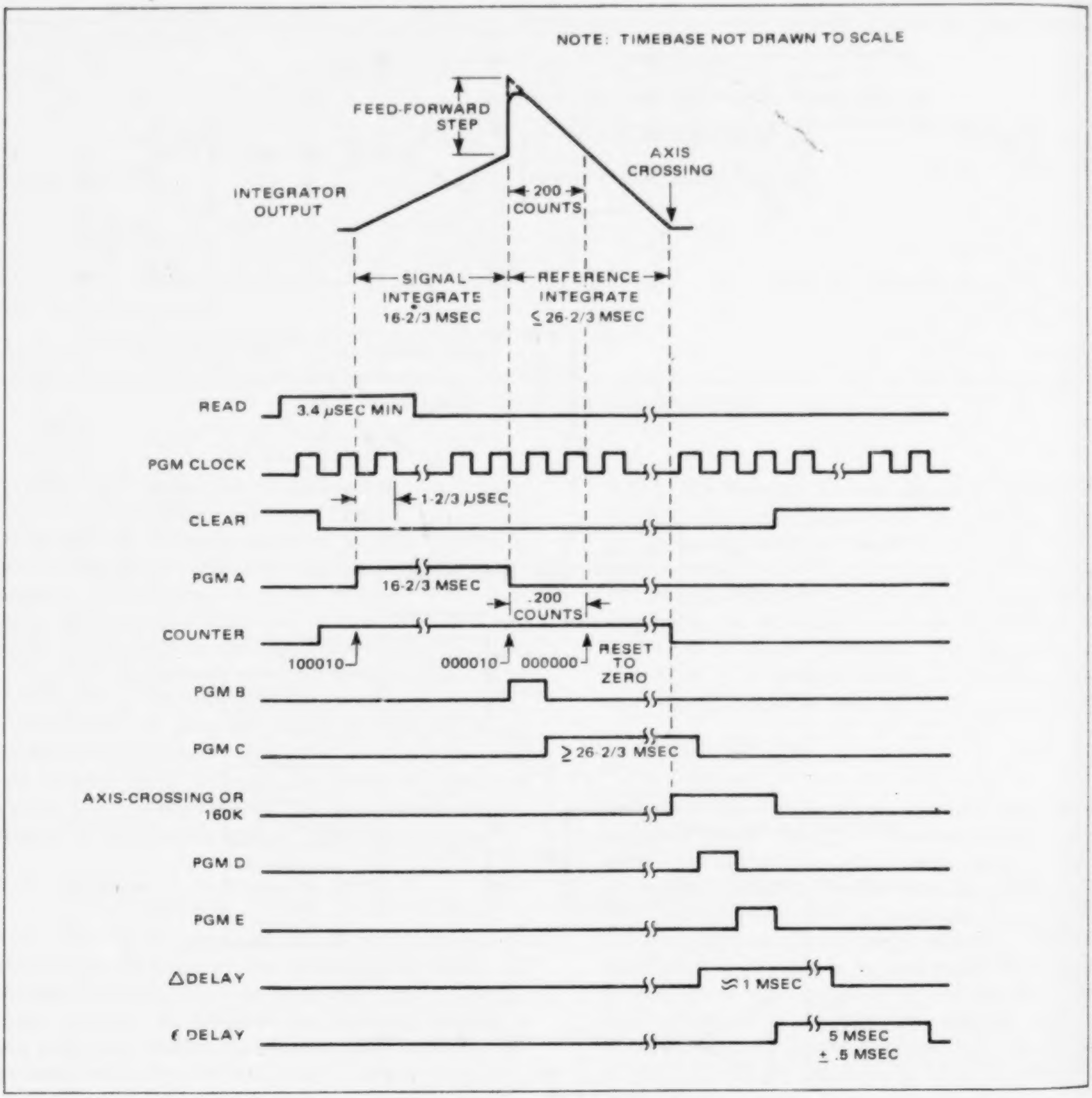

NOTE: TIMEBASE NOT DRAWN TO SCALE
FEED-FORWARD STEP
AXIS CROSSING
INTEGRATOR OUTPUT
200 COUNTS
SIGNAL INTEGRATE 16-2/3 MSEC
REFERENCE INTEGRATE ≤ 26-2/3 MSEC
READ
3.4 µSEC MIN
PGM CLOCK
1-2/3 µSEC
CLEAR
PGM A
16-2/3 MSEC
200 COUNTS
COUNTER
100010
000010
000000
RESET TO ZERO
PGM B
PGM C
≥ 26-2/3 MSEC
AXIS-CROSSING OR 160K
PGM D
PGM E
ΔDELAY
≈ 1 MSEC
ε DELAY
5 MSEC ± .5 MSEC

Figure 4.13 - Integration Timing Diagram

4.46 TRANSFORMER DRIVER.

4.47 This circuit consists of a complementary emitter-follower, capacitor coupled to the isolation transformer T1.

4.48 SIGNAL AND RESET LOGIC.

4.49 This circuitry operates from pulse data obtained through isolation transformers T2, T3 and originating from control circuitry of the Display/Logic section. The data is converted into control signals for operating the integration switches.

4.50 DIGITIZER LOGIC.

4.51 The Digitizer Logic receives its timing information through isolating data transformers T2 and T3. The logic converts this information into control signals that operate the FET switches. The timing of the input data and switch operation is provided in figure 4.13.

4.52 Ratio, Standard.

4.53 The standard ratio consists of relay K1 on the interconnect board. When nonenergized, this relay routes the

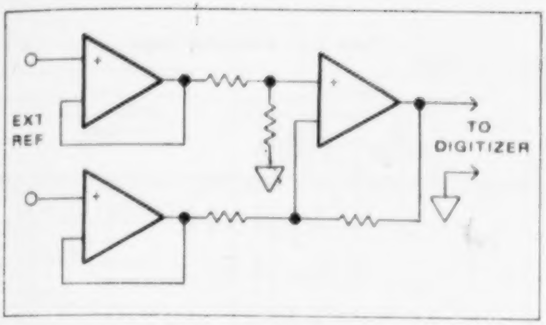

EXT
REF
TO
DIGITIZER

Figure 4.14 - 4-Wire Ratio Option

outputs of the internal zener reference bridge to the reference amplifier (both located on the reference amplifier assembly) and produces a fixed + and − 10 volts; when energized (Ratio selected) the relay reroutes the signal flow, replacing the zener input with the EXT REF input and converting the +10 output amplifier to a noninverting gain of one amplifier.

4.54 Ratio Option.

4.55 The four wire ratio circuitry is mounted on a single printed circuit board and occupies connector J14 on the Main Logic board. The 4-wire ratio option eliminates ground loop errors in ratio measurements by permitting the reference input common lead to float in reference to the signal input common. The circuitry, shown in figure 4.14, consists of two gain-of-one isolation amplifiers driving a differential potentiometric amplifier. Field installation of the ratio board requires removal of jumpers W1 and W2 on the Main Logic board.

4.56 Display Board.

4.57 The Display board is a single printed circuit board and occupies connectors J1 and J2 on the Main Logic board. The display board is located directly behind and parallel to the front panel of the instrument; the function and range control knobs on the front panel are connected by shafts to the rotary switches on the display board. A mechanical interlock attached between the two shafts prevents the manual selection of incorrect function/range combinations. The display board contains the function, the manual and autorange, and the annunciator circuitry. The circuit is shown in simplified block form in figure 4.15 and described below.

4.58 FUNCTION AND RANGE SWITCHES.

4.59 These generate a negative true output and operate only in local control (programming not selected). The function switch has three positions generating outputs (from left to right) of $\overline{AC}$, $\overline{DC}$, and $\overline{\Omega}$. The range switch has nine positions and generates the outputs shown in table 4.2.

4.60 DATA ENCODE.

4.61 The output of the range switch is converted to positive true 1248 BCD logic by this circuitry. The BCD

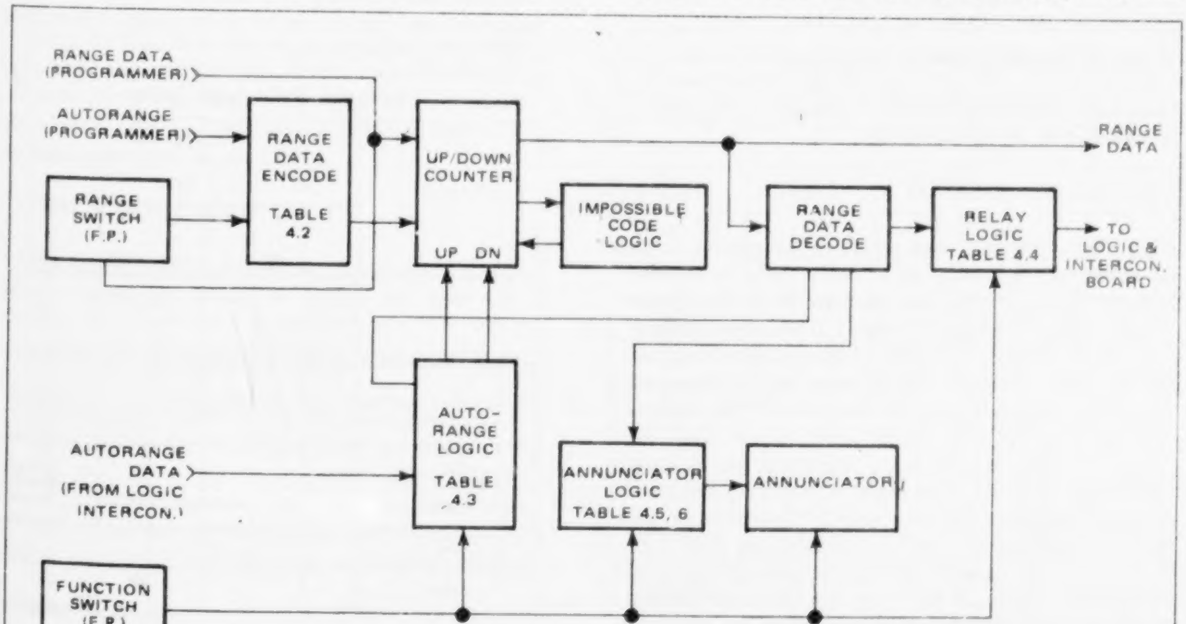

RANGE DATA
(PROGRAMMER)
AUTORANGE
(PROGRAMMER)
RANGE SWITCH (F.P.)
RANGE DATA ENCODE
TABLE 4.2
UP/DOWN COUNTER
UP
DN
IMPOSSIBLE CODE LOGIC
RANGE DATA DECODE
RELAY LOGIC
TABLE 4.4
RANGE DATA
TO LOGIC & INTERCON. BOARD
AUTORANGE DATA
(FROM LOGIC INTERCON.)
AUTO-RANGE LOGIC
TABLE 4.3
ANNUNCIATOR LOGIC
TABLE 4.5, 6
ANNUNCIATOR
FUNCTION SWITCH (F.P.)

Figure 4.15 - Display Board

Table 4.2 - Range Switch Code

RANGE SWITCH	SWITCH OUTPUT	DATA ENCODE OUTPUT				BINARY WEIGHT
		A	B	C	D	
10 Ω	$\overline{R2}$	F	T	F	F	2
.1	$\overline{R3}$	T	T	F	F	3
1	$\overline{R4}$	F	F	T	F	4
10	$\overline{R5}$	T	F	T	F	5
100	$\overline{R6}$	F	T	T	F	6
1000	$\overline{R7}$	T	T	T	F	7
AUTO	–	F	F	F	F	0
10M	$\overline{R8}$	F	F	F	T	8
100M	$\overline{R9}$	T	F	F	T	9

output is the same as produced by the programmer for remote range selection and is shown in table 4.2. The four BCD output lines from the data encode are applied to the up/down counter.

4.62 UP/DOWN COUNTER.

4.63 The up/down counter is a decade counter capable of counting up from 0 to 9 and overflowing to 0, counting down from 9 to 0 and overflowing to 9, being preset to the BCD input, and being cleared to zero. In manual range the up/down counter is held in the preset mode and the output of the counter always equals the BCD code from the data encode circuitry. In autorange the BCD input from the range data encode is inhibited and the output of the counter is controlled by the autorange logic.

4.64 AUTORANGE LOGIC.

4.65 The autorange logic generates the control pulses for advancing the counter up or down. Data are received from the function switch and from the range data decode circuit, a BCD to 10-line converter. If no range change is required the circuitry generates no control signals and the counter is not advanced. An up range signal is generated during the Program D period if an up range 160% signal is true and the $\overline{\text{up range}}$ inhibit signal is not false (see table 4.3). One up range signal is generated per measurement cycle. A down range signal is generated during the Program D period if the down range 15% signal is true and the $\overline{\text{down range}}$ inhibit signal is not false (see table 4.3). One down range signal of this type is generated for each measurement cycle. In the event that the counter is in a forbidden range (not available for the function selected), during the clear period, the counter is made to down range at the clock rate until the counter is in an available range. The autorange circuitry then operates normally to select the most appropriate range. The forbidden ranges are indicated in table 4.3. If the counter output produces an 'impossible code' (as could happen when the instrument is turned on and autorange is selected), the impossible code logic resets the counter to zero; the autorange circuitry then selects the appropriate range.

Table 4.3 - Autorange Logic

DATA	INHIBIT					FORBIDDEN RANGE					
	$\overline{\text{UP RANGE}}$		$\overline{\text{DOWN RANGE}}$								
$\overline{DC}$			F								
$\overline{AC}$				F					F		
Ω	F					F					
$\overline{\Omega}$		F			F		T	T			
$\overline{R0}$										F	
$\overline{R1}$											F
$\overline{R2}$					F	F					
$\overline{R3}$			F						F		
$\overline{R4}$				F							
$\overline{R7}$	F										
$\overline{R8}$							F				
$\overline{R9}$		F						F			

Table 4.4 - Relay Logic Coding

RANGE	FUNCTION						
	Ω		DC			AC	
	.1 ÷ 1	1 ÷ 100	.1 ÷ 1	1 ÷ 100	100 ÷ 1000	.1 ÷ 1	1 ÷ 100
10 Ω							
.1							
1							
10							
100							
1000							
10M							
100M							

▨ ENERGIZED

Table 4.5 - Annunciator Logic

INPUT	ANNUNCIATOR											
	PGM	FILTER	RATIO	DC	AC	Ω	KΩ	MEG		NO	+	−
$\overline{\text{SYSTEM CONTROL}}$	F									SEE TABLE 4.6		
$\overline{\text{FILTER}}$		F										
$\overline{\text{RATIO}}$			F									
$\overline{\text{DC}}$				F							F	F
$\overline{\text{AC}}$					F							
$\overline{\Omega}$						F	F	F	F			
W1 (JUMPER						IN	IN	IN	IN			
W2 (JUMPER)					IN							
$\overline{\text{R2}}$						F						
$\overline{\text{R8}}$							T	F				
$\overline{\text{R9}}$							T		F			
+ POLARITY											T	
− POLARITY												T

4.66 RELAY LOGIC.

4.67 The relay logic is generated by data from the range data decode and the function switch and is used via optically coupled isolators on the Main Logic board to control the ttenuator range relays. The codes as shown in table 4.4 ndicate the attenuator relay closure.

.68 ANNUNCIATOR LOGIC.

.69 The annunciator logic controls the selection of the nnunciator lamps as shown in tables 4.5 and 4.6.

.70 MAIN LOGIC AND CONTROL CIRCUITRY.

.71 This circuitry, shown in the block diagram of figre 4.16, is located on the Main Logic board and consists of e control and display logic. In general this circuitry ontrols all instrument operation including the integration ycle, 'times' the output of the digitizer, and converts this formation into data compatible with the Display board.

.72 Control Logic.

73 This circuitry controls the operation of the instruent and consists of the program logic, program counter,

Table 4.6 - "NO" Annunciator Logic

INPUT	NO									
UP RANGE 160%	T									
CLEAR		T	T	T	T	T	T			
$\overline{\text{R0}}$		F								
$\overline{\text{R1}}$			F							
$\overline{\text{R2}}$						F				
$\overline{\text{R3}}$							F			
$\overline{\text{R7}}$				F						
$\overline{\text{R8}}$					F					
$\overline{\Omega}$				T	T	T			F	
$\overline{\text{AC}}$							F			F
PROGRAM								T		
$\overline{\text{SYSTEM CONTROL}}$								T		
W1 JUMPER									OUT	
W2 JUMPER										OUT

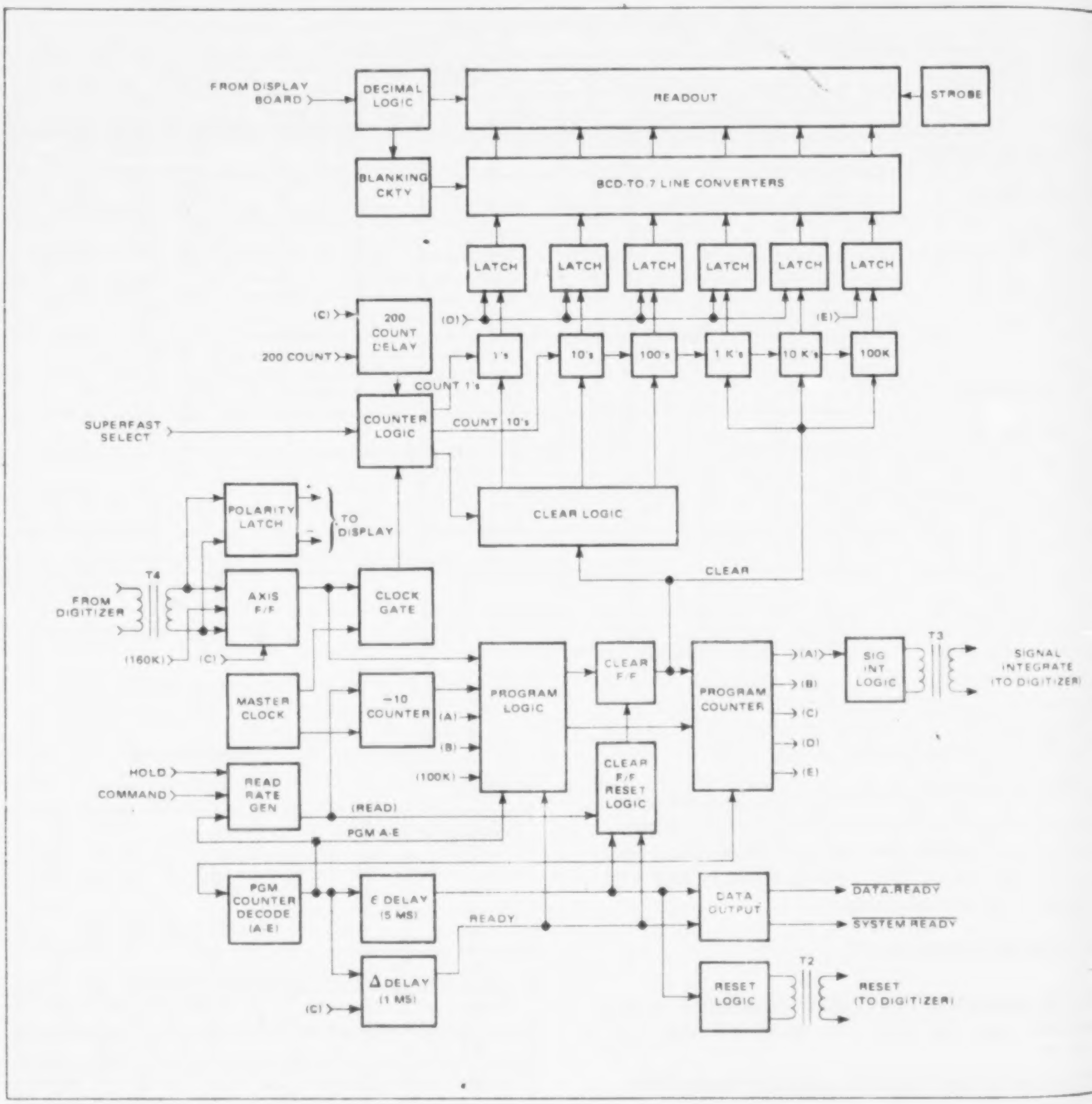

FROM DISPLAY BOARD
DECIMAL LOGIC
READOUT
STROBE
BLANKING CKTY
BCD-TO-7 LINE CONVERTERS
LATCH
LATCH
LATCH
LATCH
LATCH
LATCH
(C)
200 COUNT DELAY
200 COUNT
(D)
(E)
1's
10's
100's
1 K's
10 K's
100K
COUNT 1's
SUPERFAST SELECT
COUNTER LOGIC
COUNT 10's
POLARITY LATCH
+
−
TO DISPLAY
CLEAR LOGIC
T4
FROM DIGITIZER
AXIS F/F
CLOCK GATE
CLEAR
(160K)
(C)
MASTER CLOCK
÷10 COUNTER
(A)
(B)
(100K)
PROGRAM LOGIC
CLEAR F/F
PROGRAM COUNTER
(A)
(B)
(C)
(D)
(E)
SIG INT LOGIC
T3
SIGNAL INTEGRATE (TO DIGITIZER)
HOLD
COMMAND
READ RATE GEN
(READ)
CLEAR F/F RESET LOGIC
PGM A-E
PGM COUNTER DECODE (A-E)
E DELAY (5 MS)
READY
DATA OUTPUT
DATA READY
SYSTEM READY
Δ DELAY (1 MS)
(C)
T2
RESET LOGIC
RESET (TO DIGITIZER)

Figure 4.16 - Main Logic & Control Block Diagram

program clock, read rate generator, master clock, clear logic, and counter logic. The instrument operates on a five step program cycle produced by the program circuitry. This consists of a five stage shift register (or program counter), a program clock, and control logic. The five outputs of the program counter correspond to the five program states (PGM A, PGM B, PGM C, PGM D, and PGM E). Only one output of the counter is true at any time during the measurement cycle of the instrument and all of the outputs are false when the instrument is in clear. Th program counter is advanced by a gated square wave fro the program clock. The program clock output is driven b and equal to the master clock frequency divided by 10 o 600 kHz and is synchronized with the counter at the sta of each measurement cycle. The correlation between th master and program clocks causes a 10 count lag betwe the program counters advancing during the signal integratio period and the counter.

4.74 Program Cycle.

4.75 Preceding the start of the measurement cycle, the instrument is in a clear state with the counter set to 100000 and the logic in the reset state. Upon receipt of a read command, whether from the read rate generator or by an external command, the clear signal is removed and the counter begins counting. After ten clock counts (100010) the program counter advances to PGM A. Program A, via the pulse transformer T3, initiates the signal integrate period of the integration cycle on the digitizer board. Program A is terminated after the counter is advanced 100,000 counts. The period is a precise 16-2/3 millisecond and the counter now reads 000010 (the counter overflows to zero at 199,999). At the completion of Program A, Program B is initiated, lasting for 200 clock cycles. This period corresponds to and provides the required digital delay for the analog delay period between the signal and reference integrate periods of the digitizer as described in paragraph 4.36. At the completion of the 200 count delay (PGM B), the 1's, 10's, and 100's decades of the counter are reset, causing the counter to read 000000. At the same time the program counter is advanced to Program C, which in turn enables the axis crossing detector. The program counter remains in this state until (1) an axis crossing signal is received, indicating the reference integration period of the digitizer is completed or (2) the counter reaches 160,000, indicating an overload condition. One of these two inputs causes the axis flip-flop output to go true, inhibiting the clock gate and stopping the counter. The program counter is advanced to Program D and the Δ (delta) delay is initiated. The delta delay lasts for about 1 millisecond and prevents the ready line from going through until after the program counter has completed its cycle. Program D lasts for one program clock cycle and strobes all but the 100K latches. The program counter then advances to Program E which also lasts for one program clock cycle and strobes the 100K latch. At the end of Program E, the Program A-E line goes false, resetting the read rate oscillator, starting the ϵ (epsilon) delay which lasts for 5 milliseconds and generates the reset signal for the digitizer, and generates the clear signal which sets the counter to 100,000 and resets the logic. At the end of the delta delay, the ready line goes true generating the $\overline{\text{DATA READY}}$ output signal and enables the program logic to receive a new read command. At the completion of the epsilon delay, the $\overline{\text{SYSTEM READY}}$ signal is generated and indicates that the instrument is able to accept an external read command. This returns the circuitry to the state at the start of the program cycle.

4.76 Display Logic.

4.77 This circuitry consists of the counter, latches, BCD-to-7 line converters, display strobe, clear logic, decimal logic, and the counter control logic. The counter consists of five series connected decade counters and a J-K flip-flop, and is capable of counting from 000000 to 199999 and then overflowing to 000000. The counter is driven by the 6 MHz master clock by way of the clock gate and the counter logic. The clock gate permits the counter to advance only during the measurement cycle. The counter logic circuitry routes the clock signal for normal or superfast operation. The clear logic operates in conjunction with the clear flip-flop and the counter logic to clear all or portions of the counter depending on normal or superfast operation for different parts of the measurement cycle. The latches store the information from the counters from the last measurement cycle in the form of BCD data. The units through 10K latches are strobed (the information at the input of the latch is transferred to the latch output) at Program D; the 100K latch is strobed at Program E. The data from the latches are converted to be compatible with the seven segment LED displays by the BCD-to-7 line converter. The converters are also used in conjunction with the decimal logic to provide leading zero blanking. The LEDs on the readout board are controlled by the BCD-to-7 line converters and powered by the driver decoder strobing circuit. This consists of a one-shot driven by a 120 Hz signal. The strobe permits the LED's to operate at their greatest efficiency while maintaining low overall power consumption.

4.78 Superfast.

4.79 This is a high speed operating mode in which the instrument is effectively converted to a four decade instrument. The actual selection of this mode and timing considerations are covered under paragraphs 2.68 and 2.105. Basically the superfast mode is achieved by bypassing the least significant decade (1's) with the counter logic. At the start of a superfast measurement cycle, the 1's decade is in the clear mode and the 6 MHz clock is applied directly to the input of the 10's decade. The signal integration time of the digitizer is reduced from 16-2/3 milliseconds to 1.6-2/3 milliseconds. The 200 digit time period must remain the same as in the standard measurement operation so the instrument is converted back to standard operation during the 200 count delay. At its completion the operation reverts back to superfast and the reference integration period is measured at the same rate the signal integration period was generated.

4.80 Power Supplies.

4.81 There are two separate power supplies used in the Model 5900; one provides the voltage levels required for operating the analog portions of the instrument and the other provides the voltage levels for the digital/display, data output, and programming. The two supplies use a common transformer core and primary winding but the supplies themselves are completely isolated to maintain the common

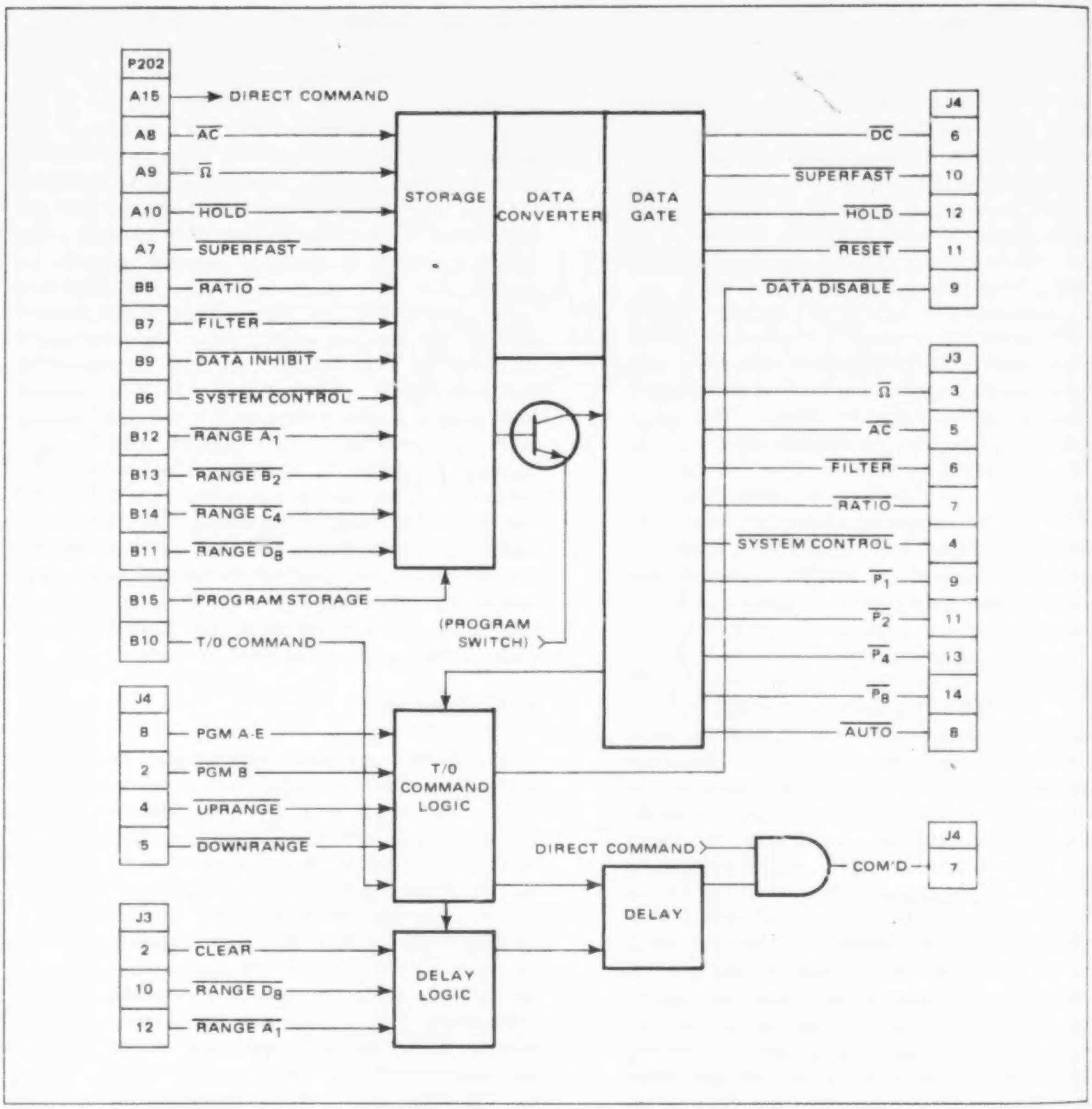

P202
A15
DIRECT COMMAND
A8
AC
A9
Ω
A10
HOLD
A7
SUPERFAST
B8
RATIO
B7
FILTER
B9
DATA INHIBIT
B6
SYSTEM CONTROL
B12
RANGE A1
B13
RANGE B2
B14
RANGE C4
B11
RANGE D8
B15
PROGRAM STORAGE
B10
T/O COMMAND
STORAGE
DATA CONVERTER
DATA GATE
(PROGRAM SWITCH)
J4
8
PGM A-E
2
PGM B
4
UPRANGE
5
DOWNRANGE
T/O COMMAND LOGIC
J3
2
CLEAR
10
RANGE D8
12
RANGE A1
DELAY LOGIC
DIRECT COMMAND
DELAY
COM'D
J4
7
J4
DC
6
SUPERFAST
10
HOLD
12
RESET
11
DATA DISABLE
9
J3
Ω
3
AC
5
FILTER
6
RATIO
7
SYSTEM CONTROL
4
P1
9
P2
11
P4
13
P8
14
AUTO
8

Figure 4.17 - Program Block Diagram

mode rejection characteristics of the instrument. The analog supply provide outputs of ±25 volts, ±20 volts, and −40 volts, all referenced to analog common (mecca). The digital supply provides outputs of +5 volts, +5 volts unregulated, and +150 volts, all referenced to digital common.

4.82 Program.

4.83 The program board is an option provided to allow external remote operation of the instrument. The circuitry is mounted on a single printed circuit board. The board is located above and parallel to the Main Logic board and mounted to the Main Logic board on standoffs. Electrical connection between the Ma[illegible] board and the Program board is by way of tw[illegible] 3-J3A and J4-J4A). A portion of one end of the Program board extends through the rear panel of the instrument, through an opening provided for that purpose. This portion of the board forms an edge connector and is designated P202 PROGRAM INPUT.

4.84 The circuitry, shown simplified in figure 4.17, consists of the program logic, data gate, and the command/timeout circuitry. The program logic converts the control line data generated at P202 to correspond to the instrument control lines. The data gate passes or rejects the converted data; the gate is enabled when the program switch is operated and system control is selected on P202. A series of inputs with their outputs are shown in table 4.7. The timeout circuitry generates the necessary timeouts for delayed remote programming. The timeout periods are covered under paragraph 2.99.

Table 4.7 - Program Logic Conversion

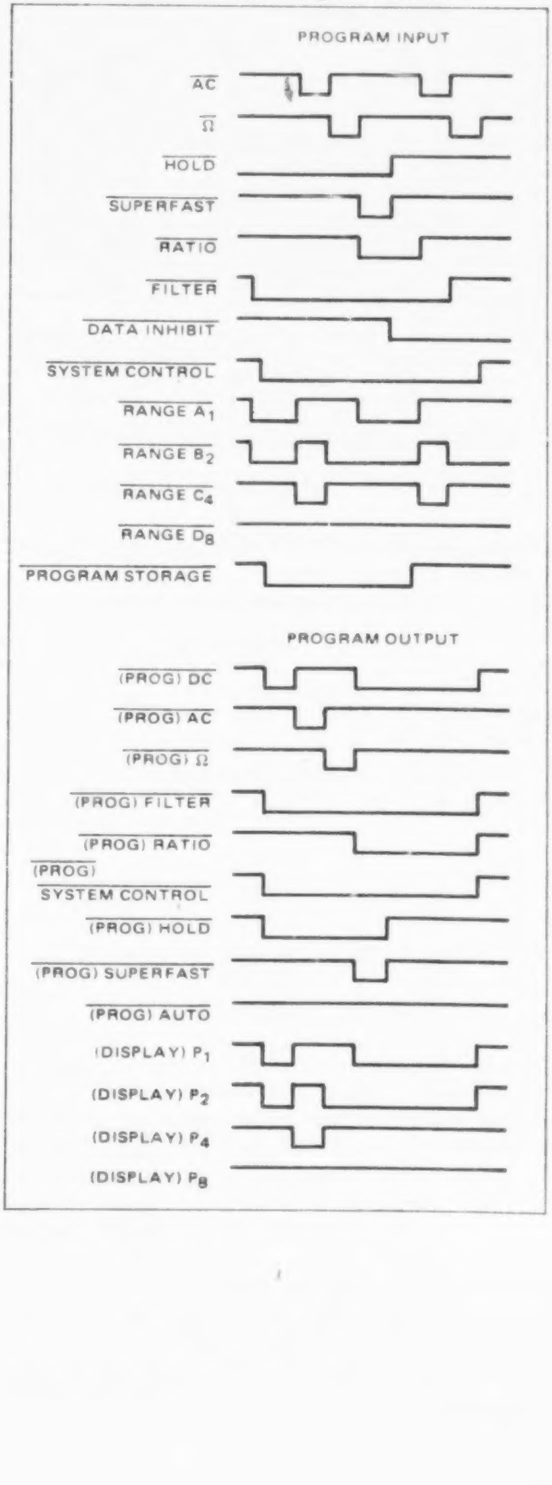

PROGRAM INPUT
$\overline{AC}$
$\overline{\Omega}$
$\overline{HOLD}$
$\overline{SUPERFAST}$
$\overline{RATIO}$
$\overline{FILTER}$
$\overline{DATA\ INHIBIT}$
$\overline{SYSTEM\ CONTROL}$
$\overline{RANGE\ A_1}$
$\overline{RANGE\ B_2}$
$\overline{RANGE\ C_4}$
$\overline{RANGE\ D_8}$
$\overline{PROGRAM\ STORAGE}$
PROGRAM OUTPUT
$\overline{(PROG)\ DC}$
$\overline{(PROG)\ AC}$
$\overline{(PROG)\ \Omega}$
$\overline{(PROG)\ FILTER}$
$\overline{(PROG)\ RATIO}$
$\overline{(PROG)\ SYSTEM\ CONTROL}$
$\overline{(PROG)\ HOLD}$
$\overline{(PROG)\ SUPERFAST}$
$\overline{(PROG)\ AUTO}$
(DISPLAY) P_1
(DISPLAY) P_2
(DISPLAY) P_4
(DISPLAY) P_8

Table 5.1 - Required Calibration Equipment

Function	Qty	Item	Minimum Use Specifications	Suggested Equipment
DC	(1)	Saturated Standard Cell Bank (6 cells)	1 ppm, certified	EPPLEY 106
	(2)	DC Voltage Sources	0.1 ppm resolution	FLUKE 332B
	(1)	Voltage Divider Adjustable	0.1 ppm linearity	FLUKE 720A
	(1)	10:1 Voltage Divider, Fixed	1 ppm, Output Z ≤10 Kohms	Fabricated*
	(1)	100:1 Voltage Divider, Fixed	1 ppm, Output Z ≤10 Kohms	Fabricated*
	(2)	Null Detector/μVoltmeters	1 μV sensitivity	FLUKE 845AR
AC	(1)	Thermal Transfer Standard	35 ppm @ 400 Hz, 50 ppm @ 40 kHz	HOLT 6A
	(1)	Thermal Voltage Converter	50 ppm @ 100 kHz	HOLT 11
	(1)	AC Voltage Source	1 ppm resolution	HP745A/746A
	(1)	Pulse Generator*	10 volts variable, 50Ω output	TEKTRONIX 2101
Ω	(8)	Resistance Standards		
		10Ω	10 ppm	ESI SR1 with corrections
		100Ω	10 ppm	ESI SR1 with corrections
		1 KΩ	10 ppm	ESI SR1 with corrections
		10 KΩ	10 ppm	ESI SR1 with corrections
		100 KΩ	10 ppm	ESI SR1 with corrections
		1 MΩ	10 ppm	Fabricated*
		10 MΩ	50 ppm	ESI SR1 with corrections
		100 MΩ	80 ppm	Fabricated*
OTHER	(1)	Momentary Switch, SPST	–	–
	(1)	Phillips head screwdriver #1	–	–
	(1)	Insulated Adjustment tool	–	JFD5284
	(1)	10 Kohm 1/4 Watt 5% Carbon Resistor	5%	–
	(1)	1 Megohm 1/4 Watt 5% Carbon Resistor	5%	–

*See Text

5.1 SCOPE.

5.2 This section describes the calibration of the Dana Model 5900 DMM and the following options:

Model 52 Ohms/DC Converter

Model 32 AC/DC Converter, RMS

Model 33 AC/DC Converter, Averaging

Model 62 4-Wire Ratio

5.3 GENERAL.

5.4 The Dana Model 5900 undergoes rigorous testing and is precisely calibrated under closely controlled conditions, prior to leaving the plant. The procedure provided in this section is designed to recalibrate the Model 5900 and to keep the instrument operating within specifications for indefinite periods of time. If the instrument is equipped with the rear panel selectable input option, set the FRONT/REAR switch to the appropriate position for each of the checks in this section calling for the application of input signals to the instrument.

5.5 Required Equipment.

5.6 A list of the equipment required for calibration is provided in table 5.1. The specific types of equipment in the Suggested Equipment column are acceptable for calibration and provided as a guide in selecting suitable equipment; instruments having operating characteristics equal to or better than those indicated may be substituted.

5.7 Fabricated Calibration Equipment.

5.8 Two fixed voltage dividers, 10:1 and 100:1, are required for dc range calibration. The dividers provide the required accuracy and low output impedance not normally available on commercial equipment. The specifications of the dividers are indicated in table 5.2.

5.9 Two standard resistors are called out for ohms calibration, for which no commercially available standard resistors having the required accuracy are presently available. These can be fabricated by mounting precision resistors in a standard minibox on 5-way binding posts.

Table 5.2 - Fixed Voltage Dividers

Ratio	Range Used on	Output Impedance	Accuracy
10:1	1V 100V	≤10 KΩ	The absolute accuracy of each divider must be known to 1 ppm with the operating voltage applied (i.e., the voltage coefficient must also be known)
100:1	0.1V 1000V		

Suitable types of resistors for the two ranges are listed below.

1 Megohm	2 ppm TC wirewound of known value
100 Megohm	25 ppm TC metal film of known value

5.10 DC Voltage Sources.

5.11 To produce voltage levels of necessary accuracy, special techniques are required. Suitable methods of generating these voltages are shown in figures 5.1 and 5.2.

5.12 10 VOLT SOURCE

5.13 A precise and traceable source of 10 volts is required, not only for calibrating the 10 volt range, but also as a reference for generating highly accurate .1, 1, 100, and 1000 volt levels. The 10 volt source used must satisfy the following requirements.

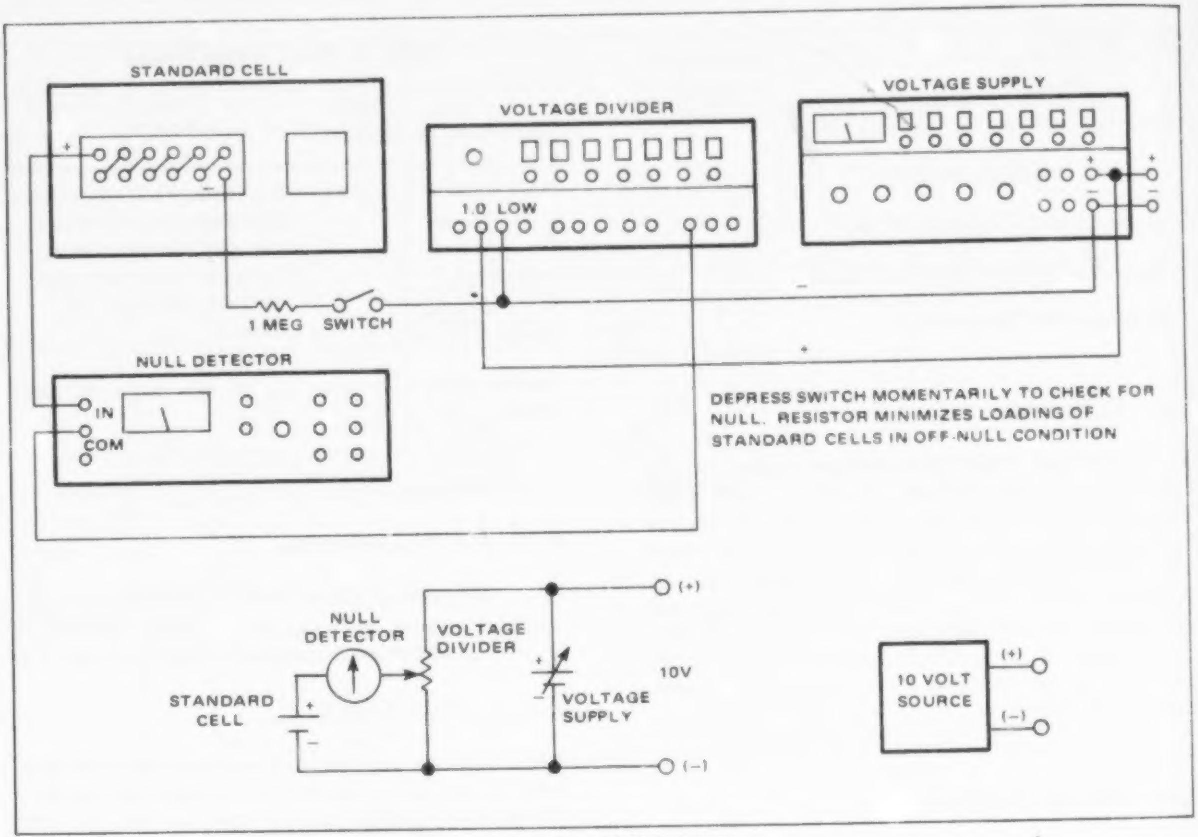
a. It must be traceable to the National Bureau of Standards;

b. It must have a total accuracy of 1.1 ppm;

c. It must have a low output impedance.

A source filling these requirements is shown in figure 5.1. This circuit consists of a null detector, 7-decade voltage divider, a dc voltage supply, and a bank of saturated standard cells. Two advantages of this particular hookup are that; (a) there is minimal loading of the standard cells and (b) stability, not accuracy, is the primary requirement of the dc voltage supply.

5.14 The output of this circuit is set to a precise 10 volts by setting the voltage divider to the value of the standard cells. The dc voltage source is then adjusted to produce a null on the null detector. The accuracy of the 10 volt source is within 1.1 ppm.


STANDARD CELL
VOLTAGE DIVIDER
VOLTAGE SUPPLY
+
−
1.0 LOW
1 MEG
SWITCH
NULL DETECTOR
IN
COM
NULL DETECTOR
VOLTAGE DIVIDER
STANDARD CELL
VOLTAGE SUPPLY
10V
(+)
(−)
10 VOLT SOURCE
(+)
(−)

DEPRESS SWITCH MOMENTARILY TO CHECK FOR NULL. RESISTOR MINIMIZES LOADING OF STANDARD CELLS IN OFF-NULL CONDITION

Figure 5.1 - 10 Volt Source

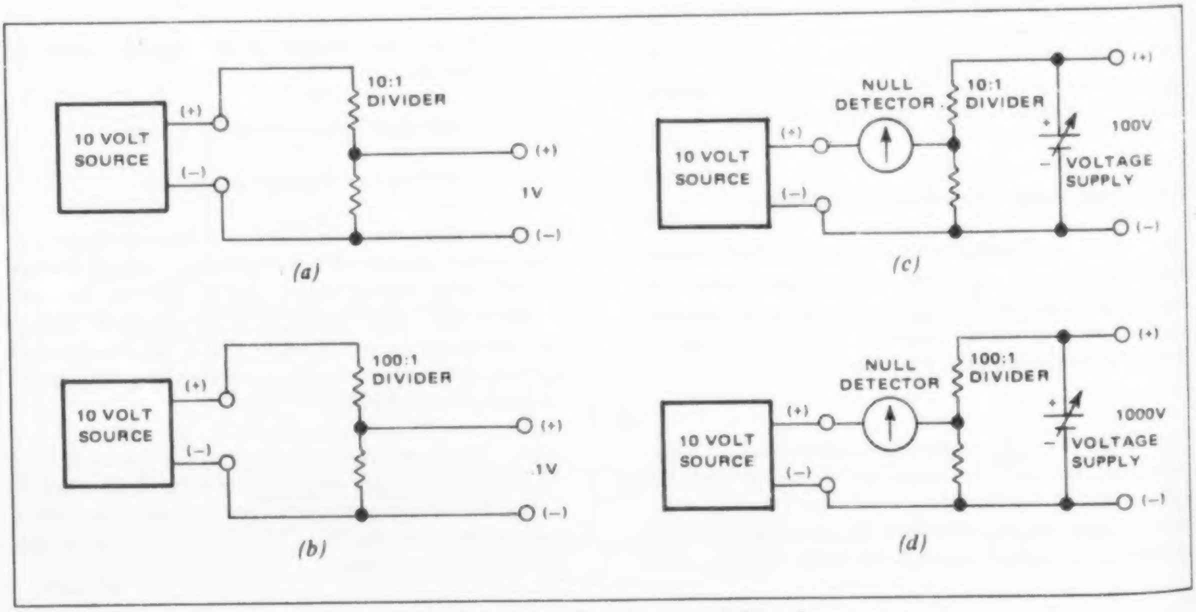

10 VOLT SOURCE
(+)
(−)
10:1 DIVIDER
(+)
1V
(−)
(a)
10 VOLT SOURCE
(+)
(−)
NULL DETECTOR
10:1 DIVIDER
(+)
100V
VOLTAGE SUPPLY
(−)
(c)
10 VOLT SOURCE
(+)
(−)
100:1 DIVIDER
(+)
.1V
(−)
(b)
10 VOLT SOURCE
(+)
(−)
NULL DETECTOR
100:1 DIVIDER
(+)
1000V
VOLTAGE SUPPLY
(−)
(d)

Figure 5.2 - Generating Accurate DC Levels

Table 5.3 - DC Source Accuracies

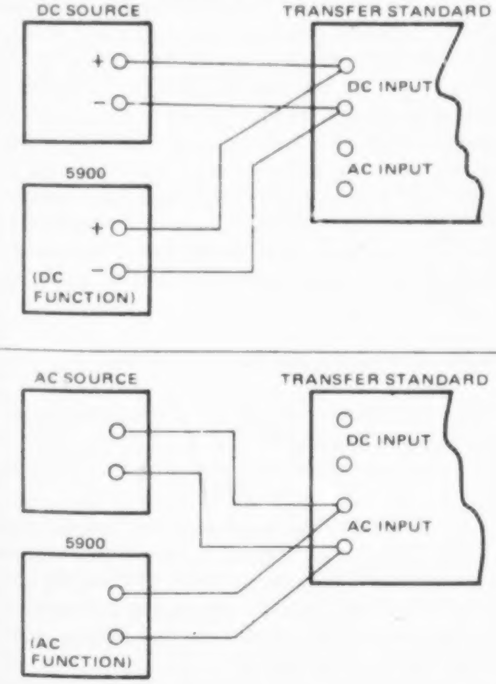
Range	10 Volt Source	Fixed Divider	Total Accuracy	24 hr. DMM Accuracy	Times Better
10	1.1 ppm	–	1.1 ppm	10 ppm	9
1	1.1 ppm	1 ppm	2.1 ppm	10 ppm	4.8
.1	1.1 ppm	1 ppm	2.1 ppm	20 ppm	4.8
100	1.1 ppm	1 ppm	2.12 ppm	10 ppm	4.7
1000	1.1 ppm	1 ppm	2.12 ppm	10 ppm	4.7

5.15 OTHER SOURCES.

5.16 The remainder of the dc sources can be generated by the circuits shown in figure 5.2. Each of these hookups use a calibrated 10 volt source having the characteristics of the one previously described.

5.17 ACCURACY, DC.

5.18 The accuracy of the dc voltage sources is obtained by adding the various sources of error in each hookup; errors in this discussion are defined in parts per million (ppm). For the 10 volt source, the error is the sum of the standard cell bank (certified at 1 ppm) and the voltage divider (0.1 ppm), giving a constant 1.1 ppm. In table 5.3 is shown the errors of each voltage source, the total accuracy of each hookup, the accuracy of the Model 5900 DMM, and the degree to which the sources exceed the required accuracy of the DMM (4 to 10 times better is the suggested accuracy ratio per MIL-M-38793).



DC. With DMM used to monitor DC output, dc source is adjusted to desired voltage. Transfer standard is then used per manufacturer instructions.

AC. The ac source is adjusted to produce a null on transfer standard. The ac source output is now calibrated and can be used as a precision source for the recalibration of the 5900 AC Converter.

NOTE: Because of the low input impedance of the transfer standard, the DMM input leads must be connected as close to the transfer standard input terminals as possible.

Figure 5.3 - AC Source

Table 5.4 - AC Source Accuracies

INPUT		ACCURACY				
AC Source						
Volts	Freq	Thermal Transfer Standard	DC Source	Total AC Source	Model 33 (Spec)	Model 32 (Spec)
1, 10, and 100 Volts	400 Hz	35 ppm	10 ppm	45 ppm	200 ppm	
	500 Hz					600 ppm
	50 kHz	50 ppm		60 ppm		1800 ppm
	100 kHz				400 ppm	
500 Volts	40 kHz	50 ppm		60 ppm	400 ppm	
						900 ppm
1000 Volts	400 Hz	52 ppm		62 ppm	200 ppm	
						600 ppm

5.19 AC Voltage Sources.

5.20 The generation of accurate ac signals for calibrating the ac converter ranges, requires the use of a thermal transfer standard and a precise dc standard as well as a stable ac source. Sufficient accuracy can be obtained by using a dc source and the Model 5900 being calibrated (immediately after the dc calibration has been completed). The circuitry connections are shown in figure 5.3. Information on the use of the transfer standard can be obtained from the operators manual accompanying the standard. The 5900 is used to set the dc source to the desired voltage; the thermal transfer standard is then used to calibrate the output of the ac source. The calibrated ac source is used to calibrate the 5900 ac converter. This procedure is repeated for each range.

5.21 ACCURACY, AC.

5.22 The accuracy of the ac source is equal to the sum of the transfer standard accuracy and the accuracy of the dc source. The accuracy of the setup for each range and frequency used in the calibration procedures as well as the specified accuracy of the converters is provided in table 5.4.

5.23 PRELIMINARY PROCEDURE.

5.24 Warmup.

5.25 Apply power to the instrument and allow two (2) hours of warmup time before calibrating with instrument covers in place.

5.26 Familiarization.

5.27 Prior to starting the procedure, read all of the steps and verify that all of the necessary equipment, tools, and miscellaneous hookup cables required for the calibration procedure are readily available. Verify that all of the equipment used in the calibration is warmed up for the period prescribed by the manufacturer to reach full accuracy.

5.28 Calibration Points.

5.29 The location of the calibration points (except ac) used in the calibration procedure are shown in figure 5.4. The ac calibration points are shown in figure 5.8. Access to the calibration points is gained by removal of the top cover; the cover is secured to the DMM by captive screws located in each of the four corners of the cover. Directly beneath the cover is located a metal shield with access holes to calibration points (except ac). The shield is part of the instrument internal guard system and helps to reduce noise. For reference, an abbreviated form of the basic calibration procedure is silkscreened to the top of the shield. The shield is to be left in place unless specific instructions are given for removal.

5.30 Environmental Considerations.

5.31 The ambient temperature of the calibration environment shall be held to 23°C ± 1°C.

NOTE

In instruments equipped with the optional 4-wire ratio option it is necessary to remove the 4-wire ratio circuit board and connect jumpers W2 and W3 on the interconnection board. The shield is removed for access to the circuit board and then replaced prior to calibration of the instrument.

5.32 RECALIBRATION PROCEDURE.

5.33 The procedure is designed to produce the highest accuracy in the least number of steps while minimizing interaction between adjustments. To ensure accuracy, do

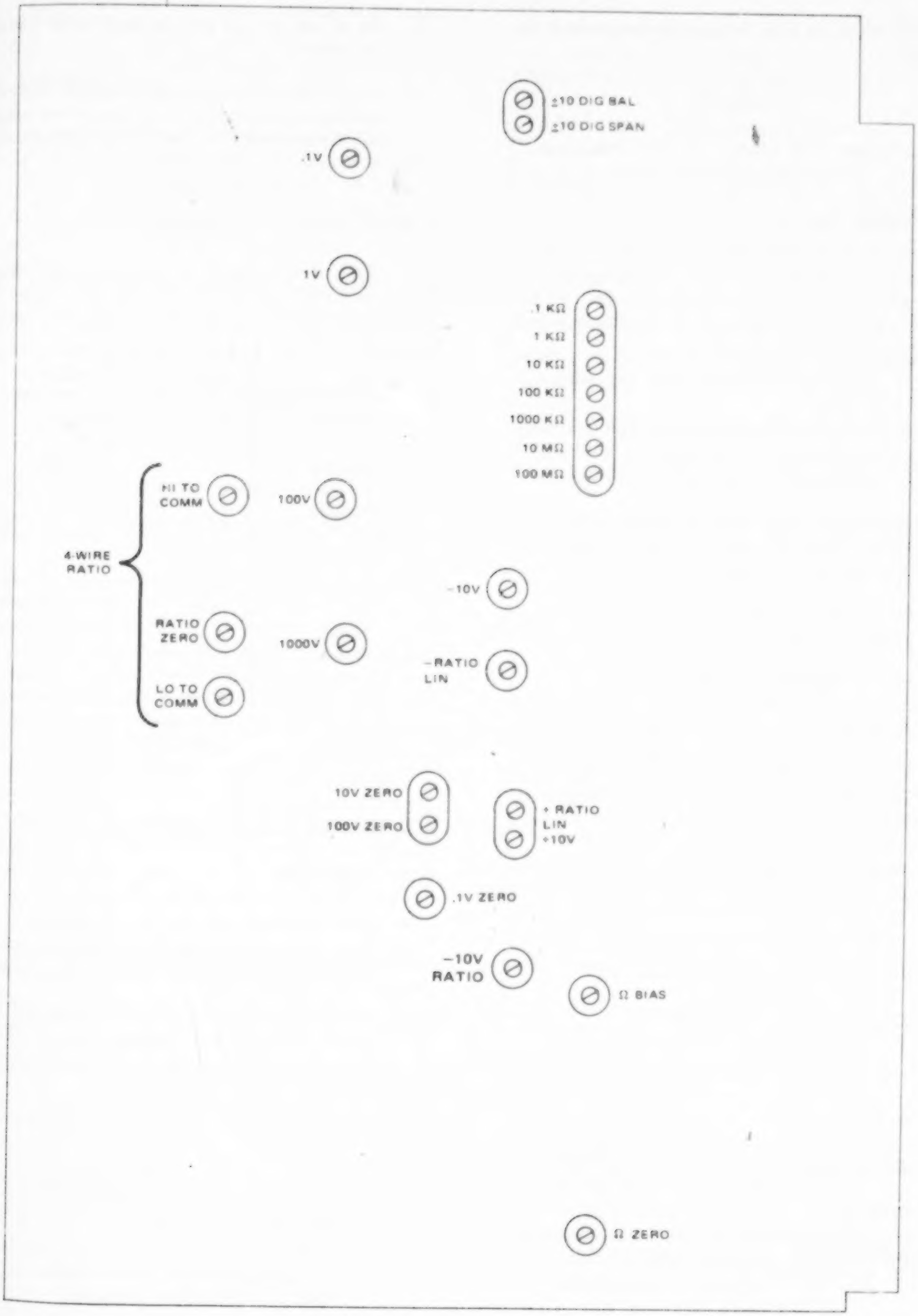

±10 DIG BAL
±10 DIG SPAN
.1V
1V
.1 KΩ
1 KΩ
10 KΩ
100 KΩ
1000 KΩ
10 MΩ
100 MΩ
HI TO COMM
100V
4-WIRE RATIO
-10V
RATIO ZERO
1000V
-RATIO LIN
LO TO COMM
10V ZERO
100V ZERO
+RATIO LIN
+10V
.1V ZERO
-10V RATIO
Ω BIAS
Ω ZERO

Figure 5.4 - Adjustment Locations

not deviate from the order of adjustments as given in the procedure.

> **WARNING**
>
> Removal of covers exposes potentially lethal voltages. Avoid contact with internal electrical connections while unit is connected to AC Power source.

5.34 Isolator Zero.

a. Select DC function and .1 volt range. Short the ± input terminals; connect microvoltmeter to ANALOG OUTPUT terminals. Set DC OFFSET (front panel) to the center of its mechanical span.

b. Adjust R14 (.1V Zero) on Isolator for a microvoltmeter reading of less than 100 microvolts.

c. Replace the jumper across the ± INPUT terminals with a 1 Megohm resistor. Adjust R3 (100V ZERO) for a reading of less than 1 millivolt.

d. Replace the resistor with a jumper. Select 10V range. Adjust R7 (10V Zero) for a reading on the microvoltmeter of less than 20 microvolts.

e. Repeat the above steps until all ranges are zeroed within the indicated tolerance.

5.35 DC Voltage Zero and Gain.

a. Select DC function and 10V range. Apply an input of +00.0010V. Observe readout and reverse polarity of input (−00.0010V). Adjust R27 (±10 DIGIT BAL) for same readout for "+" or "−" inputs. Adjust R40 (±10 DIGIT SPAN) for ± 10 digits.

b. Apply inputs listed in following tables to the front panel input terminals and the rear panel ratio input terminals. Select mode as listed for each step and adjust indicated control for a readout of ±10.0000.

F/P Input	Ratio Input	Select Mode	Adjust (on Digitizer)
−1.00000V	+1.00000V	Ratio	R5 (−RATIO LIN)
+1.00000V	+1.00000V	Ratio	R64 (+RATIO LIN)
−10.0000V	+10.0000V	Ratio	R2 (−10V RATIO)
−10.0000V		DC	R4 (−10V)
+10.0000V		DC	R57 (+10V)

c. Repeat the above steps as required until all steps are calibrated.

d. Verify that the voltage at the ANALOG OUTPUT terminals (on rear panel) is the same as the voltage on the input terminals at various input levels (±10V, ±1V, etc.).

5.36 DC RANGE CALIBRATION.

5.37 Apply inputs listed in the following table. Select DC function and range as listed for each step. Adjust the indicated control for a readout equal to the input voltage.

Input	Range	Adjust
+.100000V	.1	R18 (.1V)
−.100000V	.1	R18 (.1V)
+1.00000V	1	R5 (1V)
−1.00000V	1	R5 (1V)
+100.000	100	R11 (100V)
−100.000	100	Verify
+1000.00	1000	R13 (1000V)
−1000.00	1000	Verify

5.38 OHMS CALIBRATION (Figure 5.4).

5.39 Ohms Zero.

a. Select Ω-MΩ function and the 10 Megohm range. Short the ± INPUT and ± CURRENT terminals together. Adjust R56 (Ω ZERO) for zero readout.

b. Remove short from INPUT and CURRENT terminals. Connect a 10 Megohm standard resistor across the ± INPUT terminals. Do not connect the + CURRENT lead (figure 5.5).

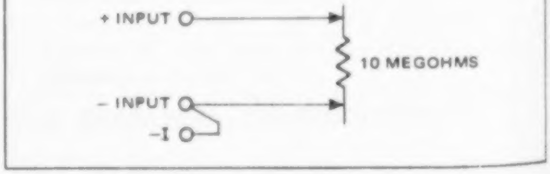


Figure 5.5 - Ohms Input Connection

c. Adjust R9 (Ω BIAS) for a readout of zero.

5.40 OHMS RANGE.

a. Connect standard resistors to the input in the order listed in the following table. Select range as listed for each step and adjust the indicated control for a readout equal to the standard resistor value. Connect as shown in figure 5.6.

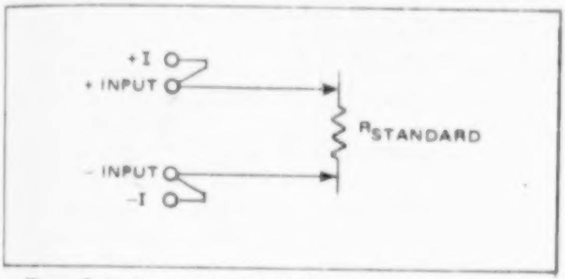


Figure 5.6 - Connections for Ohms Range Adjustment

Standard Resistor	Range	Adjust
100M	100M	R51 (100M)
10M	10M	R47 (10M)
1M	1000K	R45 (1000K)
100K	100K	R43 (100K)
10K	10K	R5 (10K)

b. Connect input with 4-wire configuration (figure 5.7) and complete the adjustments of the low ranges.

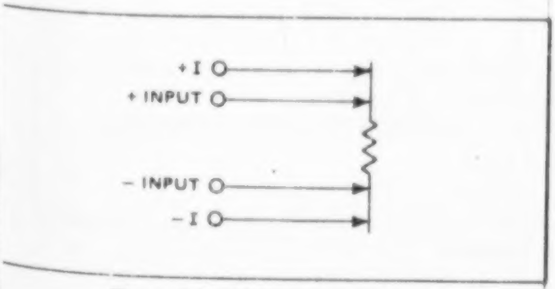


Figure 5.7 - 4-Wire Connections

Standard Resistor	Range	Adjust
1K	1K	R22 (1K)
100	.1K	R36 (.1K)
10	10	No adjustment; verify accuracy

5.41 AC CALIBRATION (Model 33).

5.42 AC Converter Zero (Figure 5.8).

a. Remove Scaling Amp, insert Scaling Amp Bypass card (Dana P/N 410617) in its place, and install AC Converter on extender. Select AC function, any range, and leave input open.

b. Connect the microvoltmeter with a 10 kilohm resistor in series with the positive lead to TP3, with negative side to TP6 (common). Adjust R35 for a microvoltmeter reading of less than 10 μvolts.

c. Connect the + microvoltmeter lead alternately between TP5 and TP1. Adjust R5 so that the voltages at TP5 and TP1 are equal (balanced) within approximately ±10 μvolts (opposite polarities). The voltage at each of the two test points must be less than ±20 μvolts.

5.43 Frequency Response.

a. Depress FILTER switch. Apply 1 volt at 400 Hz and record readout. Apply 1 volt at 100 kHz and adjust C13 for same readout (±10 digits) as obtained with 400 Hz.

b. Remove extender from AC Converter; remove Scaling Amp Bypass card. Install AC Converter and Scaling Amp into instrument.

c. Apply the inputs listed in the following table and adjust the indicated control on Scaling Amp for a readout equal to the input.

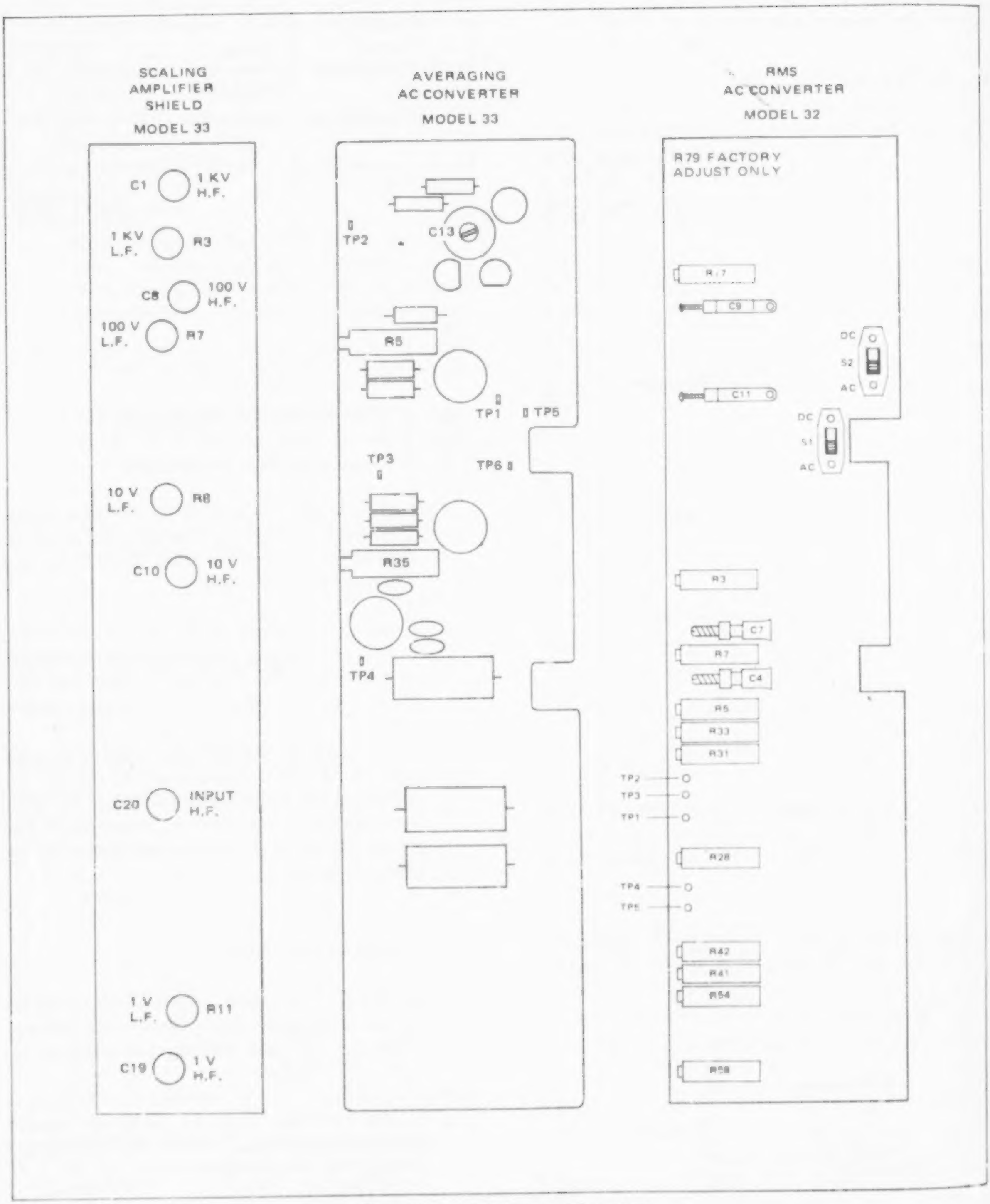

SCALING AMPLIFIER SHIELD
MODEL 33
C1
1 KV H.F.
1 KV L.F.
R3
C8
100 V H.F.
100 V L.F.
R7
10 V L.F.
R8
C10
10 V H.F.
C20
INPUT H.F.
1 V L.F.
R11
C19
1 V H.F.
AVERAGING AC CONVERTER
MODEL 33
TP2
C13
R5
TP1
TP5
TP3
TP6
R35
TP4
RMS AC CONVERTER
MODEL 32
R79 FACTORY ADJUST ONLY
R17
C9
DC
S2
AC
C11
DC
S1
AC
R3
C7
R7
C4
R5
R33
R31
TP2
TP3
TP1
R28
TP4
TP5
R42
R41
R54
R58

Figure 5.8 - AC Adjustment Locations

E_{in}	Freq.	Adjust
.01000	400 Hz	AC OFFSET (front panel)
1.00000	400 Hz	R11 (1V L.F.)
10.0000	400 Hz	R8 (10V L.F.)
100.000	400 Hz	R7 (100V L.F.)
1000.00	400 Hz	R3 (1000V L.F.)
–	–	C1 (1000V H.F.) set to center of span
500.00	40 kHz	C20 (INPUT H.F.)
1.00000	100 kHz	C19 (1V H.F.)
10.0000	100 kHz	C10 (10V H.F.)
100.000	100 kHz	C8 (100V H.F.)

5.44 RMS AC CALIBRATION (Model 32).

NOTE

Use 10 KΩ resistor in series with + lead of microvoltmeter.

a. Set DVM power switch to off. Extract converter. Set S1 and S2 to DC (away from center of board), replace converter and set power switch to on. Select AC and 1 volt range on DVM front panel. Allow 10 minutes for temperature to stabilize.

b. Connect jumper across DVM input terminals. Connect the microvoltmeter to TP1 (+) and TP5 (–). Adjust R17 for a microvoltmeter reading of 0 ± 30 μV. Remove + microvoltmeter lead from TP1. Turn R41 fully clockwise.

c. Connect microvoltmeter + lead to TP4. Adjust R42 for a microvoltmeter reading of +20 μV ± 10 μV.

d. Connect + microvoltmeter lead to TP2. Adjust R33 for a microvoltmeter reading of 0 ± 30 μV. Remove microvoltmeter + lead from TP2.

e. Connect + microvoltmeter lead to TP3. Adjust R31 for 0 ± 30 mV. Remove + microvoltmeter lead from TP3.

f. Connect + microvoltmeter lead to TP4. Adjust R41 counterclockwise until the voltage at TP4 reads 0 ± 5 μV. Remove microvoltmeter leads and remove jumper across DVM input.

g. Apply –1.00000V DC and note DVM display.

h. Reverse polarity of input to +1.00000V DC and adjust R28 to obtain approximately the same DVM display as obtained in step g. Repeat steps g and h until the two readings are within .01% of each other. Remove DC supply from DVM input.

i. Apply +0.10000 to DVM input and note DVM display.

j. Reverse polarity of input to –0.10000 and verify DVM display is within ±5 digits of the reading obtained in step i. If not, use R31 to balance the readings.

NOTE

R54 is an FSV adjustment and is reset only if major repairs have been performed on the converter. In this event, perform the following adjustment:

Apply a calibrated 1.00000V RMS pulse train with a crest factor of 7 and a period of 1 millisecond (1 kHz repetition rate). Adjust R54 for a readout of 1.00000. Remove the pulse generator from the DVM input and continue with the calibration procedure starting at step k.

k. Set power switch on DVM to off, extract converter, set S1 and S2 to AC (toward center of board). Replace converter in DVM. Reapply power and allow 10 minutes for temperature to stabilize. Select FILTER.

l. Connect the AC source to the DVM input terminals. Apply the inputs listed in the following table and adjust the indicated control for the proper readout.

Range	INPUT		Adjust	Readout
	AC Voltage	Freq		
1V	1.0000	500 Hz	R58	1.00000
1V	.1000	500 Hz	a.c. offset	0.10000
Repeat steps for R58 and a.c. offset until no adjustment is required.				
1000V	1000.0	500 Hz	R3	1000.00
1000V	500.00	40 kHz	C11*	500.000
1V	1.0000	50 kHz	C9*	1.00000
10V	10.000	500 Hz	R7	10.0000
10V	10.000	50 kHz	C7*	10.0000
100V	100.00	500 Hz	R5	100.000
100V	100.00	50 kHz	C4*	100.000
*Use an insulated screwdriver when adjusting the capacitors. High voltage present on C11.				

5.45 4-WIRE RATIO CALIBRATION.

NOTE

DC calibration of the instrument must be completed before calibrating 4-wire ratio. The DC calibration must be performed with the 4-Wire Ratio board removed and jumpers installed across W2 and W3 on the interconnection board.

a. Install 4-Wire Ratio board; remove jumpers across W2 and W3. Select RATIO, 10V RANGE, and DC function.

b. Connect −1.0V to front panel INPUT terminals; connect +1.0V to REF INPUT terminals on rear panel. Connect a jumper from high side of reference input to ANALOG COMMON (on rear panel).

c. Adjust R12 on 4-Wire Ratio board for a readout of −10.0000 (figure 5.9).

d. Increase voltage at INPUT terminals to −10.0 volts; increase REF INPUT to +10.0 volts. Adjust R10 for a readout of −10.0000.

e. Repeat steps b through c until both are achieved without further adjustment.

f. Remove jumper from high side of reference inpu to common. Connect the jumper between low reference input and common. Adjust R9 for readout of −10.0000. Remove jumper and verify that the readout does not change by more than one digit.

g. Apply +1.0V to both signal and reference inputs. Verify a readout of +10.0000.

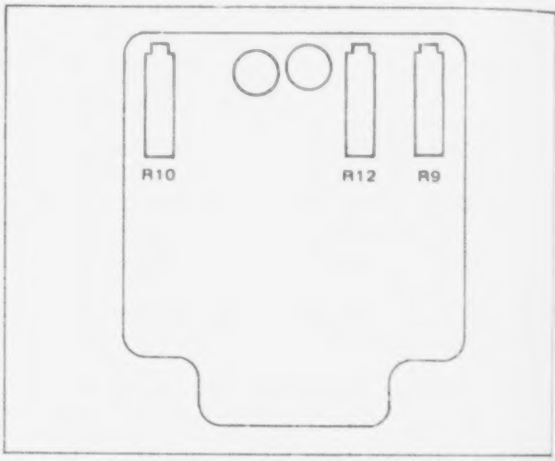


Figure 5.9 - 4-Wire Ratio Adjustment Locations

5.46 TROUBLESHOOTING

5.47 This procedure is provided to aid in locating the cause of an instrument malfunction. The procedure consist of a systematic basic instrument operation check, a table of symptoms and probable causes, and a brief description of major areas of the instrument circuitry from a trouble-shooting standpoint. To eliminate as much as possible redundant material, frequent reference is made to information located in other sections of the manual.

5.48 Troubleshooting Equipment

5.49 The equipment listed in table 5.1 can be used for the procedure with the addition of an oscilloscope such as the Tektronix type 453. Access to the main circuitry is gained by removal of the top cover and shield.

5.50 Power Supply Check

WARNING

Removal of covers exposes potentially lethal voltages. Avoid contact with internal electrical connections while unit is connected to AC Power source.

5.51 Before proceeding with the analog and digital cir-cuitry, check that all the supply voltages are present. If a discrepancy is found, proceed to table 5.5.

Table 5.5 - Power Supply Check

Low or Zero output of Supply:

1. Set power switch to OFF.
2. Remove all plug in boards to eliminate possible source of shorts.
3. Apply power and recheck supply output.
   a. If normal, check plug in boards
   b. If low, check components on main board for excessive heat dissipation.

No output of any Supply:

1. Disconnect power cable from line voltage and back of instrument.
2. With power switch on, check for continuity (Low DC resistance) between HI (pin 2) and LO (pin 1) of J203 (typically, 110 volt range is 12Ω, 220 volt range is 35Ω). An open indicates a failure of F201, S201, S202, or T1.

5.52 Operational Check

5.53 The check consists of narrowing down the problem to a specific area or circuit. This is done by checking out the basic instrument functions in a prescribed sequence and referring to the troubleshooting guide when the instrument fails to perform as indicated.

5.54 Preliminary Instrument Setup

a. Select DC function, 1 Range, Filter OUT, Ratio Out, Data Output Out, Remote Control Out, and Rate Control fully clockwise.

b. Verify the line voltage selector on the DMM rear panel is positioned for the available line voltage and connect power cable.

c. Perform the checks indicated in Table 5.6 if the instrument fails to provide the indicated output, refer to table 5.7 for assistance.

5.55 Circuit Descriptions

5.56 The remaining paragraphs describe the operation of the instrument circuitry from the standpoint of specific operating characteristics. These are to be used in troubleshooting to verify proper operation. The majority of the circuitry operates in a linear flow; that is, a signal is generated at one point and processed, encoded, decoded, or converted through various other circuits. If the output of one of the circuits is incorrect, the circuit is bad.

Table 5.6 - Operational Check

Step	Input	Annunciator	Display *
1.	a. Connect jumper across + and input b. Set power switch on	+ or –	0.00000
2.	a. Remove jumper from input b. Apply +1 volt to input (DC source)	+	1.00000
3.	Reverse Polarity of input source	–	1.00000
4.	Select 100 mV range on DMM	NO	.160000
5.	Reduce input to –0.1 volt (DC source)	–	100.000
6.	Select 10 volt range on DMM	–	00.1000
7.	Select 100 volt range on DMM	–	000.100
8.	Select 1000 volt range on DMM	–	0000.10
9.	Increase input voltage to –200 volts	–	0200.00
10.	Reduce DC Source and remove source from input		

*May require adjustment of front panel offset

5.57 MEASUREMENT LOGIC. These circuits are covered in paragraphs 5.58 through 5.63 and include the control and display logic. Symptoms of a failure in this area is the Display does not change with a change of input signal (see block diagram in figure 4.16 and schematic - figure 6.3)

5.58 CLOCK OSCILLATOR. The clock oscillator runs continuously at 6 MHz. If there is no signal, repair the clock. (5 MHz for 50 Hz operation). Q1, U38C, Y1, U13A

5.59 DECADE COUNTERS. These IC's (U14-U18, U33A) are the decade counters and are driven by the 6 MHz clock and run only during measurement cycle. If they do not run, replace them.

5.60 LATCHES. These IC's (U7-U11, U38B) store the information appearing at the input line (2, 6, 7, 3) upon receipt of a transfer pulse (PGMD) the stored date

Table 5.7 - Troubleshooting Guide

Symptom	Diagnosis	Probable Cause	Reference
Display Blank		Power Supply	Para. 5.50
Left 3 digits blank		Leading Zero Blanking	Para. 5.61
Decimal point wrong position	Range Logic	Annunciator Logic	Para. 5.66
Display does not change with a change in input signal	Instrument locked up	Clock	Para. 5.58
		Decade Counters	Para. 5.59
		Measurement Logic	Para. 5.57
		Control Logic	Para. 5.63
		Digitizer Logic	Para. 5.64
		Isolator	Para. 5.65
Display numbers incorrectly formed	BCD to 7-Line Converters		Para. 5.61
Manual range selection incorrect	Range Logic	Manual Range Encoding	Para 4.56 – 4.59
Readout does not Zero properly	Offset Error	DC Offset Adjust	R 60
		Isolator	Para. 5.65
		Digitizer Logic	Para. 5.64
Reading Unstable	Isolator	Oscillation	Para. 5.65
		Attenuator Relays	Table 5.9 Table 5.10
Improper Reading	Out of Calibration		Para. 5.1-5.45
	Non Linear	Isolator	Para. 5.65
		Integrating Capacitor	C8
	Will not read full scale	Digitizer Logic	Table 5.8
		Isolator	Table 5.9

appears and remains at output lines (16, 10, 9, 15) until the next transfer pulse. (PGMD).

5.61 BCD TO 7-LINE CONVERTERS. These units U1 through U6 convert the data from the counters to 7-line for the display and provide the leading zero blanking function. These units may be checked by swapping if a malfunction is suspected.

5.62 READ-OUT. The LED's on the readout board (LED 1 through LED 6) are controlled by the BCD to 7-line converters and are powered by the driver decades strobing circuit (U44, Q19). Failure of strobe circuit is indicated by blank display or excessive current drain from +5V regulated supply.

5.63 CONTROL LOGIC. This circuit controls the operation of the instrument and consists of the program logic (U38B, U38D, U13D, U32A, U38A, U36A-E), program counter (U37), read rate generator (Q43, Q42, Q9, Q10, U31B, U30A), program clock (U39), clear logic (U28A, B, U29B), counter clear logic (U19A, U13F, U27B, U27A, U34C, U20A, C, D, U13E, U34D), decimal logic (U41), auto range logic (U40A, D, U26A, U19B, D), are axis crossing/polarity logic (Q2, Q3, U32C, U30B, U26B, U29A). Failure of any of above circuits would be indicated by a locked-up display. See figure 4.13 for timing and waveform diagram.

5.64 DIGITIZER LOGIC AND 10V REFERENCE AMPLIFIER. (See block diagram, figure 4.12 and schematic figure 6.13). This circuitry performs the A/D conversion. It is mounted on a single PC Board and mates with connector J9 on main logic board. The circuitry includes: integrator and switching network, gain stage, null detector, transformer driver, signal and reset logic, switch controls and ±10 volt reference supplies (see paragraph 4.38 through 4.51 for theory of operation). For troubleshooting chart on digitizer and 100 reference amplifier see table 5.8.

Table 5.8 - Troubleshooting Chart – Digitizer and 10V Reference Amplifier

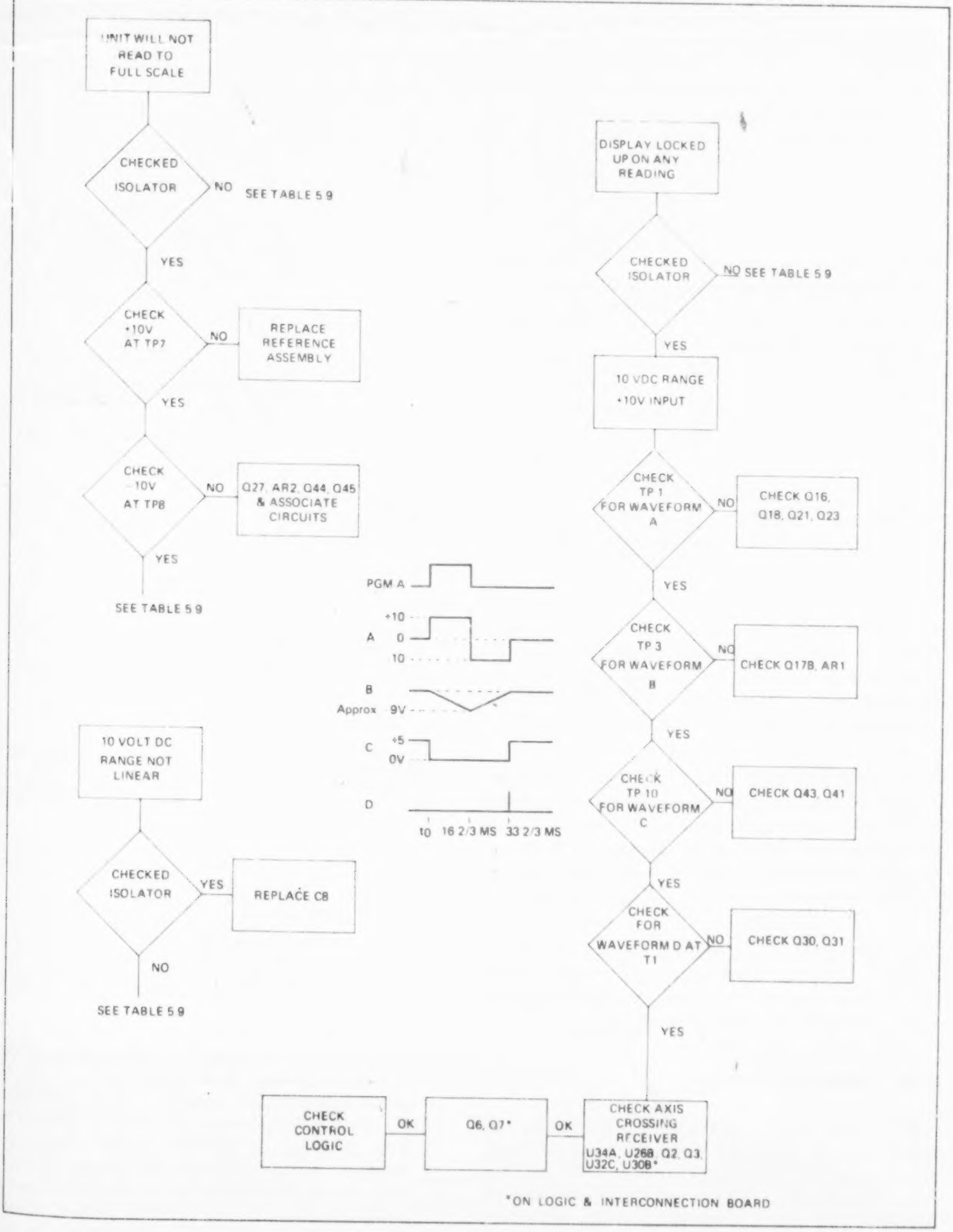

UNIT WILL NOT READ TO FULL SCALE
CHECKED ISOLATOR
NO
SEE TABLE 5.9
YES
CHECK +10V AT TP7
NO
REPLACE REFERENCE ASSEMBLY
YES
CHECK −10V AT TP8
NO
Q27, AR2, Q44, Q45 & ASSOCIATE CIRCUITS
YES
SEE TABLE 5.9
10 VOLT DC RANGE NOT LINEAR
CHECKED ISOLATOR
YES
REPLACE C8
NO
SEE TABLE 5.9
PGM A
+10
A
0
−10
B
Approx −9V
+5
C
0V
D
t0
16 2/3 MS
33 2/3 MS
DISPLAY LOCKED UP ON ANY READING
CHECKED ISOLATOR
NO
SEE TABLE 5.9
YES
10 VDC RANGE +10V INPUT
CHECK TP 1 FOR WAVEFORM A
NO
CHECK Q16, Q18, Q21, Q23
YES
CHECK TP 3 FOR WAVEFORM B
NO
CHECK Q17B, AR1
YES
CHECK TP 10 FOR WAVEFORM C
NO
CHECK Q43, Q41
YES
CHECK FOR WAVEFORM D AT T1
NO
CHECK Q30, Q31
YES
CHECK AXIS CROSSING RECEIVER U34A, U26B, Q2, Q3, U32C, U30B*
OK
Q6, Q7*
OK
CHECK CONTROL LOGIC
*ON LOGIC & INTERCONNECTION BOARD

Table 5.9 - Troubleshooting Chart — Isolator and Attenuator Boards

Sympton	Probable Cause
1. Unstable, Noisy Reading on all functions	a. Oscillation of AR2, or AR4 b. Bad Q6 c. Filter circuit AR5 K1 (Isolator) d. Attenuator relays K1, K2, K3, K4
2. Won't Read Full Scale	a. Q8 or Q9 b. See table 5.8
3. Display locked on overload	a. Q7 b. AR3 (Check AR4 pin 6 if 20 volts replace AR3) c. Q6 d. Q2, Q3, Q4, or Q5 e. See table 5.8
4. Excessive Input Bias Current	a. Adjust R3, R7 b. Q1 failure if cannot adjust

5.65 ISOLATOR. The isolator is a single printed circuit board and mates with connector J8 on the main logic board. The isolator operates in conjunction with the attenuator board as shown in figure 4.10. See table 5.9 for troubleshooting information.

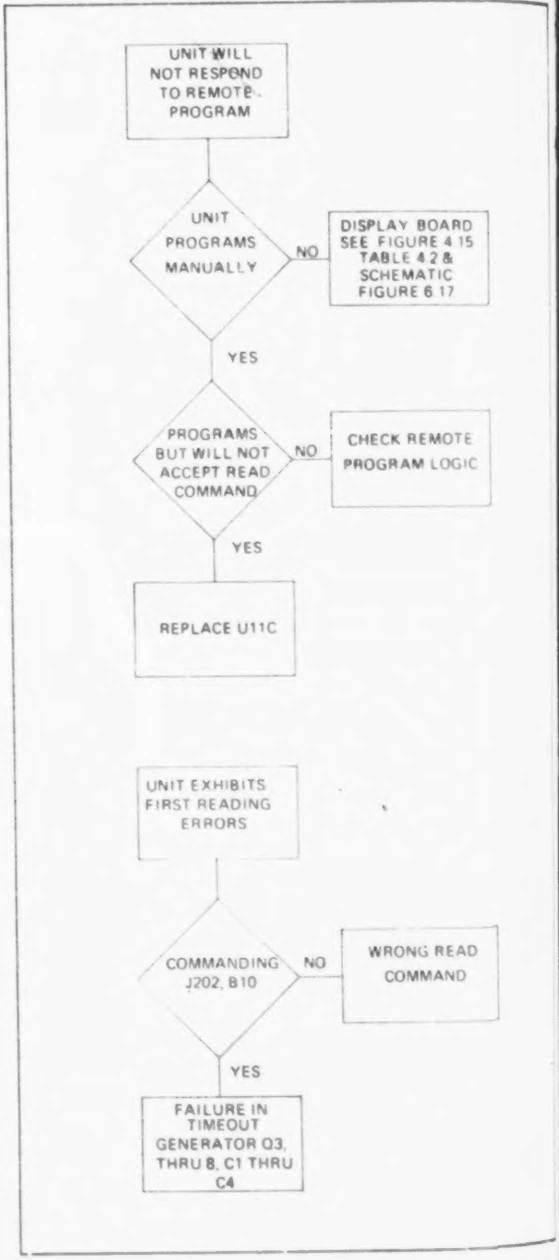
5.66 ANNUNCIATOR LOGIC. This logic controls the selection of annunciator, autoranging and polarity as shown in tables 4.5 and 4.6, and schematic figure 6.17. Failure of these circuits would cause improper range coding, improper annunciation, improper decimal placement, or improper autoranging.

5.67 OPTIONAL ACCESSORIES. Options include AC converter, RMS AC converter, ohms converter, remote programming, and four-wire ratio. See troubleshooting guide table 5.10 for more information.

5.68 BOARD REVISION.

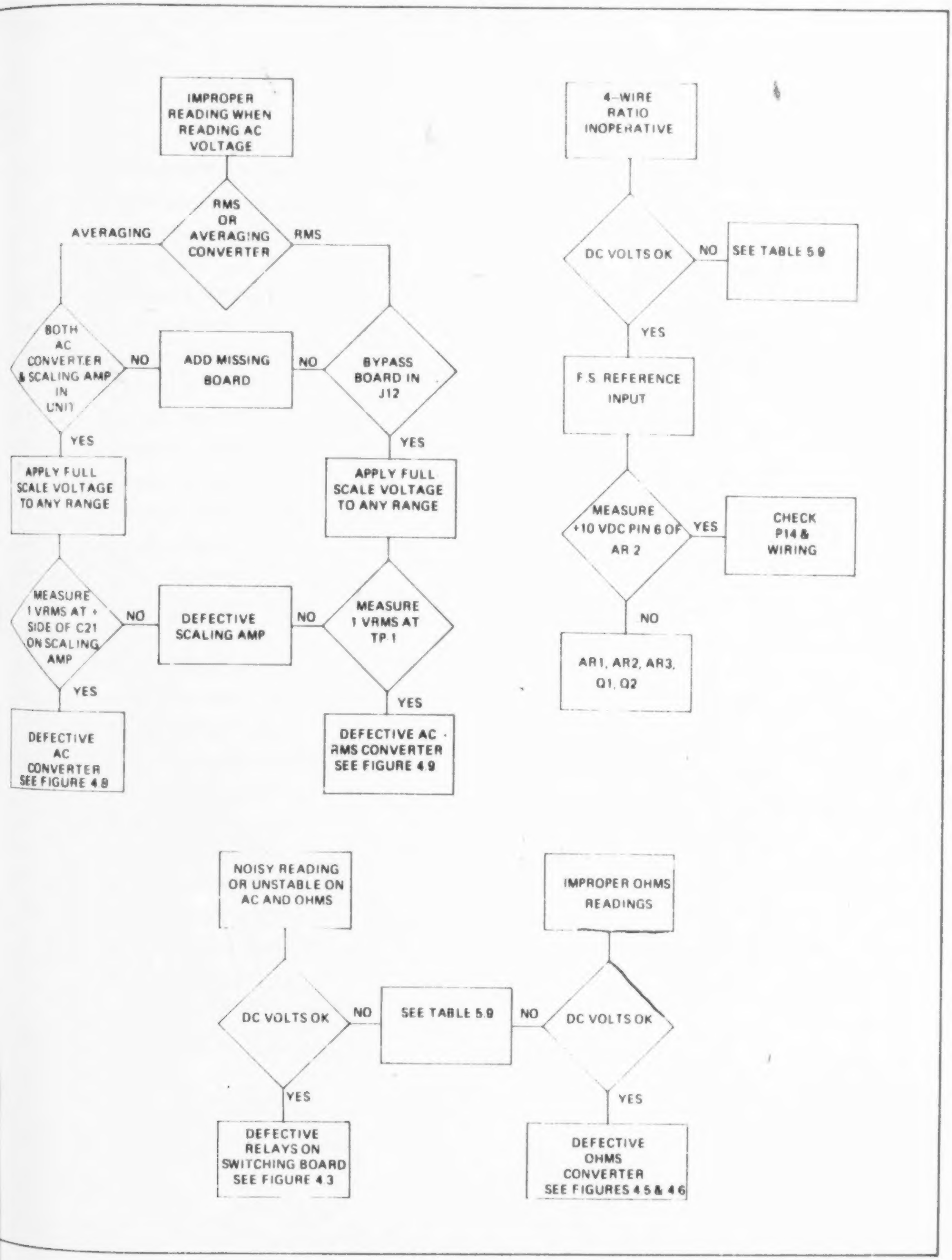
5.69 Every effort is made to keep the manual concurrent with the instrument despite changes to the design, which are an inevitable adjunct of the manufacturing process. The manual is updated and periodically reprinted throughout the year. In between printings, Addendums and Errata Sheets are added to the manual if required to implement the reprinted copy.

Table 5.10 - Troubleshooting Chart – Optional Accessories


UNIT WILL NOT RESPOND TO REMOTE PROGRAM
UNIT PROGRAMS MANUALLY
NO
DISPLAY BOARD SEE FIGURE 4.15 TABLE 4.2 & SCHEMATIC FIGURE 6.17
YES
PROGRAMS BUT WILL NOT ACCEPT READ COMMAND
NO
CHECK REMOTE PROGRAM LOGIC
YES
REPLACE U11C
UNIT EXHIBITS FIRST READING ERRORS
COMMANDING J202, B10
NO
WRONG READ COMMAND
YES
FAILURE IN TIMEOUT GENERATOR Q3, THRU 8, C1 THRU C4

5.70 Any design change is accompanied by an updating of a board revision. Such change could be as simple as a revised hole size or as complex as major modifications of the circuitry. The revision of a board is indicated by the letter preceding the assembly number stamped on the board; the revision of the assembly drawing in Section 6 or on an Errata Sheet is indicated by the letter following the assembly number, located below the drawing. Comparing the revision letters can indicate how closely the drawing corresponds to the board.

Table 5.10 - Troubleshooting Chart – Optional Accessories (continued)


IMPROPER READING WHEN READING AC VOLTAGE
RMS OR AVERAGING CONVERTER
AVERAGING
RMS
BOTH AC CONVERTER & SCALING AMP IN UNIT
NO
ADD MISSING BOARD
NO
BYPASS BOARD IN J12
YES
YES
APPLY FULL SCALE VOLTAGE TO ANY RANGE
APPLY FULL SCALE VOLTAGE TO ANY RANGE
MEASURE 1 VRMS AT + SIDE OF C21 ON SCALING AMP
NO
DEFECTIVE SCALING AMP
NO
MEASURE 1 VRMS AT TP 1
YES
YES
DEFECTIVE AC CONVERTER SEE FIGURE 4.8
DEFECTIVE AC RMS CONVERTER SEE FIGURE 4.9
4-WIRE RATIO INOPERATIVE
DC VOLTS OK
NO
SEE TABLE 5.9
YES
F.S. REFERENCE INPUT
MEASURE +10 VDC PIN 6 OF AR 2
YES
CHECK P14 & WIRING
NO
AR1, AR2, AR3, Q1, Q2
NOISY READING OR UNSTABLE ON AC AND OHMS
IMPROPER OHMS READINGS
DC VOLTS OK
NO
SEE TABLE 5.9
NO
DC VOLTS OK
YES
YES
DEFECTIVE RELAYS ON SWITCHING BOARD SEE FIGURE 4.3
DEFECTIVE OHMS CONVERTER SEE FIGURES 4.5 & 4.6

SECTION 6

DRAWINGS

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Charts Transposed to

End of Volume.

SECTION 7 PARTS LIST

7.0 If parts are required during repair or overhaul, refer first to Section 8 of this manual. The part number-national stock number cross reference index in Section 8 has been specially prepared for you so that parts already in the stock system can readily be obtained. Refer additional parts requirements to your local supply channel.

7.1 This section contains lists of replaceable parts arranged in the order of the following subassemblies:

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7.2 Manufacturers are identified by FSC numbers listed in table 7.2, "List of Suppliers". The code numbers are from the Federal Supply Code for Manufacturers Cataloging Handbooks H4-1, H4-2, and their supplements.

7.3 Certain parts having 21793 (Dana) listed in the "FSC" column are specially-selected semiconductors. For some of these, standard commercial parts will serve as satisfactory replacements. These Dana parts are identified in table 7.1 along with the commerical equivalent.

Table 7.1

Semiconductor Type:	Equivalent:
018 Diode	Fairchild 1N916B
021 Transistor	MPS A42

Table 7.2 - List of Suppliers

FSC	NAME
00779	AMP, INC. HARRISBURG, PENNSYLVANIA
01121	ALLEN BRADLEY CO. MILWAUKEE, WISCONSIN
01295	TEXAS INSTRUMENTS, INC. DALLAS, TEXAS
02111	SPECTROL ELECTRONICS CORP. CITY OF INDUSTRY, CALIFORNIA
02114	FERROXCUBE CORP. SAUGERTIES, NEW YORK
02660	AMPHENOL CORPORATION BROADVIEW, ILLINOIS
03888	PYROFILM CORP. WHIPPANY, NEW JERSEY
04222	AEROVOX CORP. (Hi-Q Division) MYRTLE BEACH, SOUTH CAROLINA
04713	MOTOROLA, INC. (Semiconductor Products Division) PHOENIX, ARIZONA
05397	UNION CARBIDE CORP. (Materials Systems Division) CLEVELAND, OHIO
05574	VIKING INDUSTRIES, INC. CHATSWORTH, CALIFORNIA
06665	PRECISION MONOLITHICS SANTA CLARA, CALIFORNIA
07263	FAIRCHILD SEMICONDUCTOR MOUNTAIN VIEW, CALIFORNIA

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Table 7.2 - List of Suppliers continued

FSC	NAME	FSC	NAME
07716	TRW ELECTRONIC COMPONENTS (IRC) BURLINGTON, IOWA	71471	AEROVOX CORP. (Cinema Plant) MONCKS CORNER, SOUTH CAROLINA
08257	NPC ELECTRONICS CANOGA PARK, CALIFORNIA	71590	CENTRALAB ELECTRONICS MILWAUKEE, WISCONSIN
08806	GENERAL ELECTRIC (Miniature Light Division) CLEVELAND, OHIO	71785	TRW ELECTRONIC COMPONENTS (Cinch Division) ELK GROVE VILLAGE, ILLINOIS
09023	CORNELL-DUBILIER ELECTRONICS SANFORD, NORTH CAROLINA	72136	ELECTRO MOTIVE MFG. CO., INC. WILLIMANTIC, CONNECTICUT
10389	CHICAGO SWITCH, INC. CHICAGO, ILLINOIS	73138	BECKMAN INSTRUMENTS, INC. FULLERTON, CALIFORNIA
11237	CTS KEENE, INC. PASO ROBLES, CALIFORNIA	73445	AMPEREX ELECTRONIC CORP. HICKSVILLE, LONG ISLAND, NEW YORK
12406	ELPAC, INC. IRVINE, CALIFORNIA	74970	E. F. JOHNSON CO. WASECA, MINNESOTA
13571	ELECTRONIC RESEARCH CO. OVERLAND PK., KANSAS	75915	LITTELFUSE, INC. DES PLAINES, ILLINOIS
15636	ELEC-TROL, INC. SAUGUS, CALIFORNIA	76493	J. W. MILLER CO. COMPTON, CALIFORNIA
17856	SILICONIX, INC. SANTA CLARA, CALIFORNIA	77342	AMERICAN MACHINE & FOUNDRY CO. (Potter/Brumfield Co.) PRINCETON, INDIANA
18612	VISHAY RESISTOR PRODUCTS MALVERN, PENNSYLVANIA	79727	C-W INDUSTRIES WARMINSTER, PENNSYLVANIA
21793	DANA LABORATORIES, INC. IRVINE, CALIFORNIA	80131	ELECTRONICS INDUSTRIES ASSOC. WASHINGTON, D.C.
22045	JORDAN ELECTRIC CO. VAN NUYS, CALIFORNIA	81349	MILITARY SPECIFICATION
24796	PARELCO, INC. SAN JUAN CAPISTRANO, CALIFORNIA	82389	SWITCHCRAFT, INC. CHICAGO, ILLINOIS
27014	NATIONAL SEMI-CONDUCTOR CORP. SANTA CLARA, CALIFORNIA	87730	UNITED MINERAL & CHEMICAL CORP. NEW YORK CITY, NEW YORK
27556	IMB ELECTRONIC PRODUCTS, INC. SANTA FE SPRINGS, CALIFORNIA	91293	JOHANSON MFG. CO. BOONTON, NEW JERSEY
50434	HEWLETT-PACKARD CO. PALO ALTO, CALIFORNIA	91637	DALE ELECTRONICS, INC. COLUMBUS, NEBRASKA
52763	STETTNER-TRUSH CAZENOVIA, NEW YORK	95275	VITRAMON, INC. BRIDGEPORT, CONNECTICUT
56289	SPRAGUE ELECTRIC CO. (Pacific Division) LOS ANGELES, CALIFORNIA	99800	AMERICAN PRECISION INDUSTRIES, INC. (Delevan Division) EAST AURORA, NEW YORK

403614 – Assy., PCB, LOGIC & INTERCONNECTION (Figure 6.3)

REF DES	DANA P/N	DESCRIPTION					FSC	MANU P/N
C1	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C2*	130142	CAP	MICA	150 PFD		5%	72136	DM5-181J
C3	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C4	110129	CAP	TANTA	.1 MFD	35 V	20%	05397	T368A104M035AS
C5	110129	CAP	TANTA	.1 MFD	35 V	20%	05397	T368A104M035AS
C6	120310	CAP	POLY	.01 MFD	250 V	10%	73445	C280MAE/A10K
C7	110156	CAP	TANTA	4.7 MFD	6 V	10%	05397	T368A475K006AS
C8	120308	CAP	POLY	.047 MFD	250 V	10%	73445	C280MAE/A47K
C10	110128	CAP	ELECT	6000 MFD	10 V	–10% +75%	56289	36D602G010AA2A
C11	110067	CAP	ELECT	2000 MFD	35 V		87730	2000DXW35
C12	110067	CAP	ELECT	2000 MFD	35 V		87730	2000DXW35
C13	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C14	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C15	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C16	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C17	110127	CAP	TANTA	22 MFD	6 V	20%	05397	T368B226M006AS
C18	110066	CAP	ELECT	16 MFD	200 V		73445	C436AR/L16
C19	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C20	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C21	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C22	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C23	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C24	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C25	120290	CAP	MYLAR	.22 MFD	100 V	20%	73445	C281AH/A220K
C29	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C30	101174	CAP	CERAM	.001 MFD	500 V	10%	04222	SCD-DI-2X5F-1000
C31	101145	CAP	CERAM	100 PFD	500V	10%	04222	TCD-DI-1N5600-100
C32	101174	CAP	CERAM	.001 MFD	500 V	10%	04222	SCD-DI-2X5F-1000
C33	110178	CAP	ELECT	47 MFD	100 V			100VBSL47
C34	110178	CAP	ELECT	47 MFD	100 V			100VBSL47
C35	110178	CAP	ELECT	47 MFD	100 V			100VBSL47
C37	110143	CAP	TANTA	1 MFD	35 V	20%	05397	T368A105M035AS
C38	110148	CAP	TANTA	.22 MFD	50 V	10%	05397	T368A224K050AS
C39	110140	CAP	TANTA	47 MFD	6 V	20%	05397	T368B476M006AS
C40	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C41	120090	CAP	MYLAR	.22 MFD	600 V	5%	27556	ZA1101J
C42	120308	CAP	POLY	.047 MFD	250 V	10%	73445	C280MAE/A47K
CR1	211083	DIODE	SILICO		018		21793	211083
CR3	211083	DIODE	SILICO		018		21793	211083
CR8	210004	DIODE	SILICO		1N4004		81349	1N4004
CR9	210004	DIODE	SILICO		1N4004		81349	1N4004

*In 50 Hz units only:

C2	130094	CAP	MICA	220 PFD	500V	5%	72136	SCDM10-221J

403614 – Assy., PCB, LOGIC & INTERCONNECTION *continued*

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
CR10	210004	DIODE SILICO 1N4004	81349	1N4004
CR11	210004	DIODE SILICO 1N4004	81349	1N4004
CR12	210004	DIODE SILICO 1N4004	81349	1N4004
CR13	210004	DIODE SILICO 1N4004	81349	1N4004
CR14	210004	DIODE SILICO 1N4004	81349	1N4004
CR15	210004	DIODE SILICO 1N4004	81349	1N4004
CR16	210004	DIODE SILICO 1N4004	81349	1N4004
CR17	210004	DIODE SILICO 1N4004	81349	1N4004
CR18	210004	DIODE SILICO 1N4004	81349	1N4004
CR19	220011	DIODE SILICO ZENER 1N968B	81349	1N968B
CR20	220007	DIODE SILICO ZENER 1N751A	81349	1N751A
CR21	211083	DIODE SILICO 018	21793	211083
CR22	211083	DIODE SILICO 018	21793	211083
CR23	211083	DIODE SILICO 018	21793	211083
CR24	210004	DIODE SILICO 1N4004	81349	1N4004
CR25	220011	DIODE SILICO ZENER 1N968B	81349	1N968B
CR26	220040	DIODE ZENER 1N753A	81349	1N753A
CR27	210004	DIODE SILICO 1N4004	81349	1N4004
CR28	210004	DIODE SILICO 1N4004	81349	1N4004
CR29	211083	DIODE SILICO 018	21793	211083
CR30	211083	DIODE SILICO 018	21793	211083
CR31	220049	DIODE ZENER 1N957B 5%	81349	1N957B
CR32	210004	DIODE SILICO 1N4004	81349	1N4004
CR33	211083	DIODE SILICO 018	21793	211083
CR34	211083	DIODE SILICO 018	21793	211083
CR35	211083	DIODE SILICO 018	21793	211083
CR36	211083	DIODE SILICO 018	21793	211083
CR37	211083	DIODE SILICO 018	21793	211083
CR38	211083	DIODE SILICO 018	21793	211083
CR39	211083	DIODE SILICO 018	21793	211083
J1	453505	CONN 12 PIN MODIFIED	21793	453505
J2	453505	CONN 12 PIN MODIFIED	21793	453505
J5	600228	CONN 18 PIN 252-18-30-160	71785	252-18-30-160
J6	600671	CONN 6 PIN 252-06-30-160	71785	252-06-30-160
J7	600280	CONN 12 PIN 252-12-30-160	71785	252-12-30-160
J8	600280	CONN 12 PIN 252-12-30-160	71785	252-12-30-160
J9	600670	CONN 10 PIN 252-10-30-160	71785	252-10-30-160
J10	600280	CONN 12 PIN 252-12-30-160	71785	252-12-30-160
J11	600280	CONN 12 PIN 252-12-30-160	71785	252-12-30-160
J12	600280	CONN 12 PIN 252-12-30-160	71785	252-12-30-160
J13	600575	CONN 25 PIN 252-25-30-160	71785	252-25-30-160
J14	600671	CONN 6 PIN 252-06-30-160	71785	252-06-30-160
K1	310109	RELAY 5 V	24796	R10-E2515-1

403614 – Assy., PCB, LOGIC & INTERCONNECTION *continued*

REF DES	DANA P/N	DESCRIPTION				FSC	MANU P/N
L1	310055	CHOKE	RF	4.7 µH	9310-28	76493	9310-28
OCI-1	250000	ISOLATOR		OPTICALLY COUPLED		01295	TIL108
OCI-2	250000	ISOLATOR		OPTICALLY COUPLED		01295	TIL108
OCI-3	250000	ISOLATOR		OPTICALLY COUPLED		01295	TIL108
OCI-4	250000	ISOLATOR		OPTICALLY COUPLED		01295	TIL108
Q1	200099	TRANS			2N4258	81349	2N4258
Q2	200037	TRANS	SILICO	NPN	2N3646	80131	2N3646
Q3	200037	TRANS	SILICO	NPN	2N3646	80131	2N3646
Q4	200099	TRANS			2N4258	81349	2N4258
Q5	200037	TRANS	SILICO	NPN	2N3646	80131	2N3646
Q6	200099	TRANS			2N4258	81349	2N4258
Q7	200037	TRANS	SILICO	NPN	2N3646	80131	2N3646
Q8	200200	TRANS		NPN	200200	21793	200200
Q9	200099	TRANS			2N4258	81349	2N4258
Q10	200037	TRANS	SILICO	NPN	2N3646	80131	2N3646
Q11	200136	TRANS	SILICO	NPN	2N5963	81349	2N5963
Q12	200037	TRANS	SILICO	NPN	2N3646	80131	2N3646
Q13	200037	TRANS	SILICO	NPN	2N3646	80131	2N3646
Q14	200200	TRANS		NPN	200200	21793	200200
Q15	200200	TRANS		NPN	200200	21793	200200
Q16	200200	TRANS		NPN	200200	21793	200200
Q17	200200	TRANS		NPN	200200	21793	200200
Q18	200200	TRANS		NPN	200200	21793	200200
Q19	200167	TRANS		PNP	MPS-U55	04713	MPS-U55
Q21	200200	TRANS		NPN	200200	21793	200200
Q22	200200	TRANS		NPN	200200	21793	200200
Q23	200088	TRANS	SILICO	PNP	2N4248	80131	2N4248
Q24	200200	TRANS		NPN	200200	21793	200200
Q25	200088	TRANS	SILICO	PNP	2N4248	80131	2N4248
Q26	200088	TRANS	SILICO	PNP	2N4248	80131	2N4248
Q27	200200	TRANS		NPN	200200	21793	200200
Q28	200088	TRANS	SILICO	PNP	2N4248	80131	2N4248
Q29	200200	TRANS		NPN	200200	21793	200200
Q30	200068	TRANS		PNP	2N4250	80131	2N4250
Q31	200155	TRANS	SILICO	NPN	MJE520	04713	MJE520
Q32	200130	TRANS	SILICO	NPN	MJE3055	04713	MJE3055
Q33	200154	TRANS	SILICO	PNP	MJE370	04713	MJE370
Q35	200200	TRANS		NPN	200200	21793	200200
Q37	200200	TRANS		NPN	200200	21793	200200
Q39	200200	TRANS		NPN	200200	21793	200200
Q41	200200	TRANS		NPN	200200	21793	200200
Q42	200200	TRANS		NPN	200200	21793	200200
Q43	200200	TRANS		NPN	200200	21793	200200

403614 – Assy., PCB, LOGIC & INTERCONNECTION *continued*

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
R1	000391	RES CARBON 390 OHM 5% 1/4W	81349	RC07GF391J
R2	000102	RES CARBON 1 K 5% 1/4W	81349	RC07GF102J
R3	000121	RES CARBON 120 OHM 5% 1/4W	81349	RC07GF121J
R4	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R5	000102	RES CARBON 1 K 5% 1/4W	81349	RC07GF102J
R6	000102	RES CARBON 1 K 5% 1/4W	81349	RC07GF102J
R7	000102	RES CARBON 1 K 5% 1/4W	81349	RC07GF102J
R8	000201	RES CARBON 200 OHM 5% 1/4W	81349	RC07GF201J
R9	000201	RES CARBON 200 OHM 5% 1/4W	81349	RC07GF201J
R10	000200	RES CARBON 20 OHM 5% 1/4W	81349	RC07GF200J
R11	000200	RES CARBON 20 OHM 5% 1/4W	81349	RC07GF200J
R12	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R13	000102	RES CARBON 1 K 5% 1/4W	81349	RC07GF102J
R14	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R15	000204	RES CARBON 200 K 5% 1/4W	81349	RC07GF204J
R16	000303	RES CARBON 30 K 5% 1/4W	81349	RC07GF303J
R17	000202	RES CARBON 2 K 5% 1/4W	81349	RC07GF202J
R18	000302	RES CARBON 3 K 5% 1/4W	81349	RC07GF302J
R19	000432	RES CARBON 4.3 K 5% 1/4W	81349	RC07GF432J
R20	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R21	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R22	000154	RES CARBON 150 K 5% 1/4W	81349	RC07GF154J
R23	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R24	000102	RES CARBON 1 K 5% 1/4W	81349	RC07GF102J
R26	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R27	000102	RES CARBON 1 K 5% 1/4W	81349	RC07GF102J
R28	000511	RES CARBON 510 OHM 5% 1/4W	81349	RC07GF511J
R29	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R30	000302	RES CARBON 3 K 5% 1/4W	81349	RC07GF302J
R31	000182	RES CARBON 1.8 K 5% 1/4W	81349	RC07GF182J
R32	000361	RES CARBON 360 OHM 5% 1/4W	81349	RC07GF361J
R33	000182	RES CARBON 1.8 K 5% 1/4W	81349	RC07GF182J
R34	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R35	000361	RES CARBON 360 OHM 5% 1/4W	81349	RC07GF361J
R36	000182	RES CARBON 1.8 K 5% 1/4W	81349	RC07GF182J
R37	000361	RES CARBON 360 OHM 5% 1/4W	81349	RC07GF361J
R38	000182	RES CARBON 1.8 K 5% 1/4W	81349	RC07GF182J
R39	000102	RES CARBON 1 K 5% 1/4W	81349	RC07GF102J
R40	000361	RES CARBON 360 OHM 5% 1/4W	81349	RC07GF361J
R41	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R42	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R43	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J

403614 – Assy., PCB, LOGIC & INTERCONNECTION *continued*

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
R44	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R45	000272	RES CARBON 2.7 K 5% 1/4W	81349	RC07GF272J
R46	000272	RES CARBON 2.7 K 5% 1/4W	81349	RC07GF272J
R47	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R48	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R49	000332	RES CARBON 3.3 K 5% 1/4W	81349	RC07GF332J
R50	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R51	000511	RES CARBON 510 OHM 5% 1/4W	81349	RC07GF511J
R52	000122	RES CARBON 1.2 K 5% 1/4W	81349	RC07GF122J
R53	000122	RES CARBON 1.2 K 5% 1/4W	81349	RC07GF122J
R54	000183	RES CARBON 18 K 5% 1/4W	81349	RC07GF183J
R55	000202	RES CARBON 2 K 5% 1/4W	81349	RC07GF202J
R56	001738	RES CARBON 1.5 OHM 5% 1/2W	81349	RC20GF1R5J
R57	020576	RES WW .24 OHM 5% 2 W	07716	BWH Series
R58	000183	RES CARBON 18 K 5% 1/4W	81349	RC07GF183J
R59	001738	RES CARBON 1.5 OHM 5% 1/2W	81349	RC20GF1R5J
R60	040235	POT CERMET 100 K 10%	73138	89PR100K
R61	010778	RES METAL 2.74 K 1% 1/10W	81349	RN55C2741F
R62	010778	RES METAL 2.74 K 1% 1/10W	81349	RN55C2741F
R63	000203	RES CARBON 20 K 5% 1/4W	81349	RC07GF203J
R64	000203	RES CARBON 20 K 5% 1/4W	81349	RC07GF203J
R69	000203	RES CARBON 20 K 5% 1/4W	81349	RC07GF203J
R72	040235	POT CERMET 100 K 10%	73138	89PR100K
R75	000203	RES CARBON 20 K 5% 1/4W	81349	RC07GF203J
R78	000202	RES CARBON 2 K 5% 1/4W	81349	RC07GF202J
R79	000510	RES CARBON 51 OHM 5% 1/4W	81349	RC07GF510J
R80	000102	RES CARBON 1 K 5% 1/4W	81349	RC07GF102J
R82	000154	RES CARBON 150 K 5% 1/4W	81349	RC07GF154J
R83	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R84	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R85	000202	RES CARBON 2 K 5% 1/4W	81349	RC07GF202J
R86	000511	RES CARBON 510 OHM 5% 1/4W	81349	RC07GF511J
R87	040272	POT 50 K 10%	11237	200
R89	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R90	000332	RES CARBON 3.3 K 5% 1/4W	81349	RC07GF332J
R91	000224	RES CARBON 220 K 5% 1/4W	81349	RC07GF224J
R92	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R93	000822	RES CARBON 8.2 K 5% 1/4W	81349	RC07GF822J
R94	000432	RES CARBON 4.3 K 5% 1/4W	81349	RC07GF432J
R95	000822	RES CARBON 8.2 K 5% 1/4W	81349	RC07GF822J
R96	000432	RES CARBON 4.3 K 5% 1/4W	81349	RC07GF432J
R97	000822	RES CARBON 8.2 K 5% 1/4W	81349	RC07GF822J
R103	000102	RES CARBON 1 K 5% 1/4W	81349	RC07GF102J

403614 – Assy., PCB, LOGIC & INTERCONNECTION *continued*

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
R104	000102	RES CARBON 1 K 5% 1/4W	81349	RC07GF102J
R105	000102	RES CARBON 1 K 5% 1/4W	81349	RC07GF102J
R106	000102	RES CARBON 1 K 5% 1/4W	81349	RC07GF102J
R107	000202	RES CARBON 2 K 5% 1/4W	81349	RC07GF202J
R108	000102	RES CARBON 1 K 5% 1/4W	81349	RC07GF102J
R109	000562	RES CARBON 5.6 K 5% 1/4W	81349	RC07GF562J
R110	000562	RES CARBON 5.6 K 5% 1/4W	81349	RC07GF562J
R111	000562	RES CARBON 5.6 K 5% 1/4W	81349	RC07GF562J
R112	000562	RES CARBON 5.6 K 5% 1/4W	81349	RC07GF562J
S2	600683	SWITCH PUSHBUTTON 600683	21793	600683
S3	600683	SWITCH PUSHBUTTON 600683	21793	600683
S4	600683	SWITCH PUSHBUTTON 600683	21793	600683
S5	600683	SWITCH PUSHBUTTON 600683	21793	600683
T2	300087	TRANSFORMER PULSE 300087	21793	300087
T3	300087	TRANSFORMER PULSE 300087	21793	300087
T4	300087	TRANSFORMER PULSE 300087	21793	300087
TP1	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP2	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP3	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP4	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP5	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP6	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP7	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP8	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP9	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP10	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP11	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP12	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP13	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP14	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
U1	230132	INTEGRATED CIRCUIT 7447	07263	7447
U2	230132	INTEGRATED CIRCUIT 7447	07263	7447
U3	230132	INTEGRATED CIRCUIT 7447	07263	7447
U4	230132	INTEGRATED CIRCUIT 7447	07263	7447
U5	230132	INTEGRATED CIRCUIT 7447	07263	7447
U6	230132	INTEGRATED CIRCUIT 7447	07263	7447
U7	230065	INTEGRATED CIRCUIT 7475	01295	7475
U8	230065	INTEGRATED CIRCUIT 7475	01295	7475

403614 – **Assy., PCB, LOGIC & INTERCONNECTION** *continued*

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
U9	230065	INTEGRATED CIRCUIT 7475	01295	7475
U10	230065	INTEGRATED CIRCUIT 7475	01295	7475
U11	230065	INTEGRATED CIRCUIT 7475	01295	7475
U12	230028	INTEGRATED CIRCUIT 7400	01295	7400
U13	230064	INTEGRATED CIRCUIT 7404	01295	7404
U14	230037	INTEGRATED CIRCUIT 7490	07263	7490
U15	230037	INTEGRATED CIRCUIT 7490	07263	7490
U16	230037	INTEGRATED CIRCUIT 7490	07263	7490
U17	230037	INTEGRATED CIRCUIT 7490	07263	7490
U18	230037	INTEGRATED CIRCUIT 7490	07263	7490
U19	230114	INTEGRATED CIRCUIT 7408	07263	7408
U20	230114	INTEGRATED CIRCUIT 7408	07263	7408
U21	230114	INTEGRATED CIRCUIT 7408	07263	7408
U22	230064	INTEGRATED CIRCUIT 7404	01295	7404
U23	230064	INTEGRATED CIRCUIT 7404	01295	7404
U24	230064	INTEGRATED CIRCUIT 7404	01295	7404
U25	230064	INTEGRATED CIRCUIT 7404	01295	7404
U26	230072	INTEGRATED CIRCUIT 7474	01295	7474
U27	230072	INTEGRATED CIRCUIT 7474	01295	7474
U28	230029	INTEGRATED CIRCUIT 7401	01295	7401
U29	230072	INTEGRATED CIRCUIT 7474	01295	7474
U30	230030	INTEGRATED CIRCUIT 7402	01295	7402
U31	230028	INTEGRATED CIRCUIT 7400	01295	7400
U32	230031	INTEGRATED CIRCUIT 7410	01295	7410
U33	230035	INTEGRATED CIRCUIT 7473	01295	7473
U34	230028	INTEGRATED CIRCUIT 7400	01295	7400
U35	230064	INTEGRATED CIRCUIT 7404	01295	7404
U36	230116	INTEGRATED CIRCUIT 7432	07263	7432
U37	230080	INTEGRATED CIRCUIT 7496	07263	7496
U38	230028	INTEGRATED CIRCUIT 7400	01295	7400
U39	230037	INTEGRATED CIRCUIT 7490	07263	7490
U40	230028	INTEGRATED CIRCUIT 7400	01295	7400
U41	230071	INTEGRATED CIRCUIT 7445	07263	7445
U42	230073	INTEGRATED CIRCUIT 7405	01295	7405
U43	230029	INTEGRATED CIRCUIT 7401	01295	7401
U44	230133	INTEGRATED CIRCUIT 74121	07263	74121
W2	600245	JUMPER L-2007-1LP		L-2007-1LP
W3	600245	JUMPER L-2007-1LP		L-2007-1LP
Y1*	920627	CRYSTAL 6 MHz	13571	6 MHz

*In 50 Hz units only:

Y1 920677 CRYSTAL 5.0 MHz 21793 920677

403857 – Assy., PCB, READOUT (Figure 6.3)

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
LED1	210065	L.E.D. DISPLAY HP5082-7750	50434	HP5082-7750
LED2	210065	L.E.D. DISPLAY HP5082-7750	50434	HP5082-7750
LED3	210065	L.E.D. DISPLAY HP5082-7750	50434	HP5082-7750
LED4	210065	L.E.D. DISPLAY HP5082-7750	50434	HP5082-7750
LED5	210065	L.E.D. DISPLAY HP5082-7750	50434	HP5082-7750
LED6	210065	L.E.D. DISPLAY HP5082-7750	50434	HP5082-7750
R1	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R2	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R3	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R4	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R5	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R6	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R7	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R8	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R9	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R10	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R11	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R12	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R13	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R14	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R15	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R16	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R17	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R18	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R19	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R20	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R21	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R22	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R23	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R24	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R25	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R26	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R27	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R28	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R29	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R30	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R31	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R32	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R33	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R34	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R35	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J

403857 – Assy., PCB, READOUT *continued*

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
R36	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R37	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R38	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R39	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R40	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R41	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R42	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R43	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R44	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R45	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R46	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J

403623 – Assy., PCB, ATTENUATOR (Figure 6.5)

REF DES	DANA P/N	DESCRIPTION						FSC	MANU P/N
C1	100046	CAP	CERAM	.01 MFD	1000 V	20%		56289	C023B102K103M
C2	100012	CAP	CERAM	33 PFD	500 V	10%		71471	TCD-DI-1(N750)
C3	120309	CAP	POLY	.015 MFD	200 V	5%		27556	PA2C153J
C4	120313	CAP	POLY	.15 MFD	200 V	5%		27556	PA2C154J
CR1	211083	DIODE	SILICO		018			21793	211083
CR2	211083	DIODE	SILICO		018			21793	211083
CR3	211083	DIODE	SILICO		018			21793	211083
CR4	211083	DIODE	SILICO		018			21793	211083
CR5	211083	DIODE	SILICO		018			21793	211083
CR6	211083	DIODE	SILICO		018			21793	211083
CR7	210004	DIODE	SILICO		1N4004			81349	1N4004
CR8	211083	DIODE	SILICO		018			21793	211083
K1	310111	RELAY			28 V			77342	R10-E2314-2
K2	310112	RELAY			28 V			77342	R10-E2315-2
K3	310111	RELAY			28 V			77342	R10-E2314-2
K4	310113	RELAY	REED		R4092-3			15636	R4092-3
R1	000107	RES	CARBON	100 M		5%	1/4W	81349	RC07GF107J
R2	001796	RES	CARBON	2.7 OHM		5%	1/4W	01121	See Descrpt.
R3	010027	RES	METAL	499 OHM	T-0	1%	1/8W	81349	RN60D4990F
R4	010769	RES	METAL	4.5 K		.01%		18612	HP202
R5	040229	POT	CERMET	1 K		10%		73138	89PR1K
R6	010770	RES	METAL	45.0097 K		.01%		18612	HP202
R7	000753	RES	CARBON	75 K		5%	1/4W	81349	RC07GF753J
R8	001806	RES	CARBON	200 K		5%	2 W	01121	See Descrpt.
R9	001806	RES	CARBON	200 K		5%	2 W	01121	See Descrpt.
R11	040229	POT	CERMET	1 K		10%		73138	89PR1K
R12	020640	RES	WW, SET					21793	020640
R13	040229	POT	CERMET	1 K		10%		73138	89PR1K
R14	010161	RES	METAL	110 OHM	T-0	1%	1/8W	81349	RN60D1100F
R15	020640	RES	WW, SET					21793	020640
R16	010768	RES	METAL	500 OHM		.01%		18612	HP202-M
R17	000107	RES	CARBON	100 M		5%	1/4W	81349	RC07GF107J
R18	040235	POT	CERMET	100 K		10%		73138	89PR100K
R19	010033	RES	METAL	49.9 K	T-0	1%	1/8W	81349	RN60D4992F
R20	000107	RES	CARBON	100 M		5%	1/4W	81349	RC07GF107J
R21	010616	RES	METAL	50 K		.1%	1/10W	81349	RN55C5002B
R22	010616	RES	METAL	50 K		.1%	1/10W	81349	RN55C5002B
R23	010616	RES	METAL	50 K		.1%	1/10W	81349	RN55C5002B
R24	020640	RES	WW, SET					21793	020640

403623 – Assy., PCB, ATTENUATOR *continued*

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
R25	020640	RES WW, SET	21793	020640
R26	020640	RES WW, SET	21793	020640
R27	020640	RES WW, SET	21793	020640
AR1	230103	INTEGRATED CIRCUIT AMP LM308	27014	LM308

403625 -- Assy., PCB, SWITCHING (Figure 6.7)

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
CR1	211083	DIODE SILICO 018	21793	211083
CR2	211083	DIODE SILICO 018	21793	211083
CR3	220019	DIODE SILICO ZENER 1N752A	81349	1N752A
CR4	220022	DIODE SILICO ZENER 1N965B	81349	1N965B
CR5	211083	DIODE SILICO 018	21793	211083
CR6	211083	DIODE SILICO 018	21793	211083
CR7	211083	DIODE SILICO 018	21793	211083
CR8	211083	DIODE SILICO 018	21793	211083
K1	310110	RELAY 5 V	24796	R10-E2445-1
K2	310110	RELAY 5 V	24796	R10-E2445-1
OCI-1	250000	ISOLATOR OPTICALLY COUPLED	01295	TIL108
OCI-2	250000	ISOLATOR OPTICALLY COUPLED	01295	TIL108
OCI-3	250000	ISOLATOR OPTICALLY COUPLED	01295	TIL108
Q1	200200	TRANS NPN 200200	21793	200200
Q2	200200	TRANS NPN 200200	21793	200200
Q3	200200	TRANS NPN 200200	21793	200200
Q4	200200	TRANS NPN 200200	21793	200200
Q5	200200	TRANS NPN 200200	21793	200200
Q6	200200	TRANS NPN 200200	21793	200200
Q7	200200	TRANS NPN 200200	21793	200200
Q8	200200	TRANS NPN 200200	21793	200200
Q9	200200	TRANS NPN 200200	21793	200200
R1	000102	RES CARBON 1 K 5% 1/4W	81349	RC07GF102J
R2	000102	RES CARBON 1 K 5% 1/4W	81349	RC07GF102J
R3	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R4	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R5	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R6	000361	RES CARBON 360 OHM 5% 1/4W	81349	RC07GF361J
R7	000361	RES CARBON 360 OHM 5% 1/4W	81349	RC07GF361J
R8	000361	RES CARBON 360 OHM 5% 1/4W	81349	RC07GF361J
R9	000203	RES CARBON 20 K 5% 1/4W	81349	RC07GF203J
R10	000203	RES CARBON 20 K 5% 1/4W	81349	RC07GF203J
R11	000203	RES CARBON 20 K 5% 1/4W	81349	RC07GF203J
R12	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R13	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R14	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R15	000682	RES CARBON 6.8 K 5% 1/4W	81349	RC07GF682J
R16	000472	RES CARBON 4.7 K 5% 1/4W	81349	RC07GF472J

403625 – Assy., PCB, SWITCHING *continued*

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
R17	000472	RES CARBON 4.7 K 5% 1/4W	81349	RC07GF472J
R18	000472	RES CARBON 4.7 K 5% 1/4W	81349	RC07GF472J
U1	230073	INTEGRATED CIRCUIT 7405	01295	7405
U2	230034	INTEGRATED CIRCUIT 74141	07263	74141

403624 – Assy., PCB, ISOLATOR (Figure 6.9)

REF DES	DANA P/N	DESCRIPTION					FSC	MANU P/N
C1	120004	CAP	POLY	.001 MFD	500 V		08257	KSO Series
C2	101175	CAP	CERAM	220 PFD	100 V	10%	71471	SCD1X5F
C3	101175	CAP	CERAM	220 PFD	100 V	10%	71471	SCD1X5F
C4	101145	CAP	CERAM	100 PFD	500 V	10%	04222	TCD-DI-1N5600-100
C5	101099	CAP	CERAM	680 PFD	1000 V	10%	71471	SCD2X5F
C6	121092	CAP	MYLAR	.0022 MFD	100 V	10%	09023	WMF1D22
C7	101145	CAP	CERAM	100 PFD	500 V	10%	04222	TCD-DI-1N5600-100
C8	100012	CAP	CERAM	33 PFD	500 V	10%	71471	TCD-DI-1(N750)
C9	120271	CAP	POLY	.5 MFD	50 V	5%	27556	PV2A504J
C10	101182	CAP	CERAM	47 PFD	500 V	10%	71471	TCD-DI-2(N750)
C11	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C12	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C13	101182	CAP	CERAM	47 PFD	500 V	10%	71471	TCD-DI-2(N750)
C14	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C16	120313	CAP	POLY	.15 MFD	200 V	5%	27556	PA2C154J
C17	100032	CAP	MICA	.0013 MFD	100 V		72136	DM19F132J
C18	121394	CAP	MYLAR	.15 MFD	100 V	10%	09023	WMF1P15
C19	101145	CAP	CERAM	100 PFD	500 V	10%	04222	TCD-DI-1N5600-100
C20	120313	CAP	POLY	.15 MFD	200 V	5%	27556	PA2C154J
C21	110163	CAP	TANTA	4.7 MFD	35 V	20%	05397	T368B475M035AS
C22	110163	CAP	TANTA	4.7 MFD	35 V	20%	05397	T368B475M035AS
C23	101145	CAP	CERAM	100 PFD	500 V	10%	04222	TCD-DI-1N5600-100
C24	101145	CAP	CERAM	100 PFD	500 V	10%	04222	TCD-DI-1N5600-100
C25	100012	CAP	CERAM	33 PFD	500 V	10%	71471	TCD-DI-1(N750)
C26	100012	CAP	CERAM	33 PFD	500 V	10%	71471	TCD-DI-1(N750)
C27	100012	CAP	CERAM	33 PFD	500 V	10%	71471	TCD-DI-1(N750)
CR1	210004	DIODE	SILICO		1N4004		81349	1N4004
CR2	220015	DIODE	SILICO	ZENER	1N967B		81349	1N967B
CR3	220035	DIODE		ZENER	16 V	5%	81349	1N966B
CR4	220007	DIODE	SILICO	ZENER	1N751A		81349	1N751A
CR5	220007	DIODE	SILICO	ZENER	1N751A		81349	1N751A
CR6	210004	DIODE	SILICO		1N4004		81349	1N4004
CR7	210004	DIODE	SILICO		1N4004		81349	1N4004
CR8	211083	DIODE	SILICO		018		21793	211083
CR9	211083	DIODE	SILICO		018		21793	211083
CR10	211083	DIODE	SILICO		018		21793	211083
CR11	211083	DIODE	SILICO		018		21793	211083
CR12	211083	DIODE	SILICO		018		21793	211083
K1A	310113	RELAY	REED		R4092-3		15636	R4092-3
K1B	310113	RELAY	REED		R4092-3		15636	R4092-3
Q1	200191	TRANS		PNP	2N1305		81349	2N1305

403624 – Assy., PCB, ISOLATOR *continued*

REF DES	DANA P/N	DESCRIPTION							FSC	MANU P/N
Q2	200200	TRANS		NPN	200200				21793	200200
Q3	200088	TRANS	SILICO	PNP	2N4248				80131	2N4248
Q4	200200	TRANS		NPN	200200				21793	200200
Q5	200200	TRANS		NPN	200200				21793	200200
Q6	200201	TRANS	DUAL	NPN	200201				21793	200201
Q7	200199	TRANS		FET					27014	FM1302
Q8	200088	TRANS	SILICO	PNP	2N4248				80131	2N4248
Q9	200200	TRANS		NPN	200200				21793	200200
R1	010529	RES	METAL	10 K	T-0		1%	1/10W	81349	RN55C1002F
R2	010277	RES	METAL	442 OHM			1%	1/8W	81349	RN60D4420F
R3	040235	POT	CERMET	100 K			10%		73138	89PR100K
R4	001810	RES	CARBON	47 M			5%	1/2W	01121	See Descrpt.
R5	000103	RES	CARBON	10 K			5%	1/4W	81349	RC07GF103J
R6	000103	RES	CARBON	10 K			5%	1/4W	81349	RC07GF103J
R7	040235	POT	CERMET	100 K			10%		73138	89PR100K
R8	001810	RES	CARBON	47 M			5%	1/2W	01121	See Descrpt.
R9	000103	RES	CARBON	10 K			5%	1/4W	81349	RC07GF103J
R10	001737	RES	CARBON	FSV			5%	1/4W	21793	001737
R11	010779	RES	METAL	806 K			1%	1/4W	81349	RN65E8063F
R12	001737	RES	CARBON	FSV			5%	1/4W	21793	001737
R13	000220	RES	CARBON	22 OHM			5%	1/4W	81349	RC07GF220J
R14	040232	POT	CERMET	10 K			10%		73138	89PR10K
R15	010429	RES	METAL	475 K			1%	1/8W	81349	RN60D4753F
R16	001737	RES	CARBON	FSV			5%	1/4W	21793	001737
R17	010779	RES	METAL	806 K			1%	1/4W	81349	RN65E8063F
R18	001737	RES	CARBON	FSV			5%	1/4W	21793	001737
R19	000432	RES	CARBON	4.3 K			5%	1/4W	81349	RC07GF432J
R20	000432	RES	CARBON	4.3 K			5%	1/4W	81349	RC07GF432J
R21	000513	RES	CARBON	51 K			5%	1/4W	81349	RC07GF513J
R22	000513	RES	CARBON	51 K			5%	1/4W	81349	RC07GF513J
R24	010106	RES	METAL	324 K	T-0		1%	1/8W	81349	RN60D3243F
R25	000103	RES	CARBON	10 K			5%	1/4W	81349	RC07GF103J
R26	000103	RES	CARBON	10 K			5%	1/4W	81349	RC07GF103J
R28	010106	RES	METAL	324 K	T-0		1%	1/8W	81349	RN60D3243F
R29	010536	RES	METAL	100 K			1%	1/10W	81349	RN55C1003F
R30	010529	RES	METAL	10 K	T-0		1%	1/10W	81349	RN55C1002F
R31	010536	RES	METAL	100 K			1%	1/10W	81349	RN55C1003F
R32	010621	RES	METAL	49.9 K			1%	1/10W	81349	RN55C4992F
R33	000102	RES	CARBON	1 K			5%	1/4W	81349	RC07GF102J
R34	000241	RES	CARBON	240 OHM			5%	1/4W	81349	RC07GF241J

403624 – Assy., PCB, ISOLATOR *continued*

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
R36	000104	RES CARBON 100 K 5% 1/4W	81349	RC07GF104J
R37	000241	RES CARBON 240 OHM 5% 1/4W	81349	RC07GF241J
R38	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
AR1	230103	INTEGRATED CIRCUIT LM308	27014	LM308
AR2	230054	INTEGRATED CIRCUIT LM301A	27014	LM301A
AR3	230054	INTEGRATED CIRCUIT LM301A	27014	LM301A
AR4	230054	INTEGRATED CIRCUIT LM301A	27014	LM301A
AR5	230054	INTEGRATED CIRCUIT LM301A	27014	LM301A
TP1	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP2	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP3	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0

403916 – Assy., PCB, 10V REFERENCE (Figure 6.12)

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
CR1*	220032	DIODE ZENER BRIDGE ELEMENT	21793	220032
Q1	200196	TRANS NPN 2N3568	81349	2N3568
Q2**	200200	TRANS NPN 200200	21793	200200
R1*	020654	RES WW FSV 1%	21793	020654
R2	020641	RES WW 10 K 1% .05 W	22045	J-90
R3	040232	POT CERMET 10 K 10%	73138	89PR10K
R4	040235	POT CERMET 100 K 10%	73138	89PR100K
R5	040236	POT CERMET 200 K 10%	73138	89PR200K
R6	000565	RES CARBON 5.6 M 5% 1/4W	81349	RC07GF565J
R7	010774	RES METAL 3.7 K .1%	18612	V53-1
R8	010773	RES METAL 6.3 K .1%	18612	V53-1
R9*	020655	RES WW FSV 1%	21793	020655
R10*	010941	RES METAL FSV ZENER BRIDGE	21793	010941
R11**	010879	RES METAL 1 M 1% 1/10W	81349	RN55D1004F
R12**	012007	RES METAL 23.2 K NOM FSV 1%	21793	012007
AR1	230127	INTEGRATED CIRCUIT	06665	SSS725C

*Zener Ref FSV Kit 403686

**Part of temperature compensation circuit and may not be installed in all assemblies

All other components are Ref Amp Assy 403917

403626 – Assy., PCB, DIGITIZER (Figure 6.12)

REF DES	DANA P/N	DESCRIPTION					FSC	MANU P/N
C1	120343	CAP	MYLAR	1 MFD	50 V	10%	12406	Z5R105K
C3	101098	CAP	CERAM	330 PFD	1000 V	10%	56289	C016B102E331K
C4	101642	CAP	CERAM	150 PFD	500 V	10%	71471	SCD1X5F
C5	101174	CAP	CERAM	.001 MFD	500 V	10%	04222	SCD-DI-2X5F-1000
C6	100006	CAP	CERAM	10 PFD	500 V		71471	TCDI-1(N750)
C7	110127	CAP	TANTA	22 MFD	6 V	20%	05397	T368B226M006AS
C8	120253	CAP	POLY	.33 MFD	100 V	5%	71785	863UW
C9	101642	CAP	CERAM	150 PFD	500 V	10%	71471	SCD1X5F
C10	110125	CAP	TANTA	2.2 MFD	35 V	20%	05397	T368B225M035AS
C11	101642	CAP	CERAM	150 PFD	500 V	10%	71471	SCD1X5F
C12	100012	CAP	CERAM	33 PFD	500 V	10%	71471	TCD-DI-1(N750)
C13	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C15	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C16	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C17	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C18	100068	CAP	CERAM	.02 MFD	100 V	20%	56289	C023B101H203M
C19	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C20	100068	CAP	CERAM	.02 MFD	100 V	20%	56289	C023B101H203M
C21	120308	CAP	POLY	.047 MFD	250 V	10%	73445	C280MAE/A47K
C26	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C27	130142	CAP	MICA	150 PFD		5%	72136	DM5-181J
C28	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C29	120308	CAP	POLY	.047 MFD	250 V	10%	73445	C280MAE/A47K
C30	100068	CAP	CERAM	.02 MFD	100 V	20%	56289	C023B101H203M
C31	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C32	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C36	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C37	110127	CAP	TANTA	22 MFD	6 V	20%	05397	T368B226M006AS
C38	110129	CAP	TANTA	.1 MFD	35 V	20%	05397	T368A104M035AS
C39	101098	CAP	CERAM	330 PFD	1000 V	10%	56289	C016B102E331K
C41	100006	CAP	CERAM	10 PFD	500 V		71471	TCDI-1(N750)
C42	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C43	110165	CAP	TANTA	.15 MFD	35 V	10%	05397	T368A154K035AS
C44	110158	CAP	TANTA	10 MFD	50 V	10%	05397	T362C106K050A
C45	101644	CAP	CERAM	200 PFD	1000V	20%	71471	GPDX5F201K
C46	101642	CAP	CERAM	150 PFD	500 V	10%	71471	SCD1X5F
C47	101644	CAP	CERAM	200 PFD	1000V	20%	71471	GPDX5F201K
CR1	220054	DIODE		ZENER	1N5260B		81349	1N5260B
CR2	220059	DIODE		ZENER	33 V	5%	81349	1N973B
CR3	211083	DIODE	SILICO		018		21793	211083
CR4	211083	DIODE	SILICO		018		21793	211083
CR5	211083	DIODE	SILICO		018		21793	211083
CR6	211083	DIODE	SILICO		018		21793	211083
CR7	211083	DIODE	SILICO		018		21793	211083
CR8	211083	DIODE	SILICO		018		21793	211083

403626 – Assy., PCB, DIGITIZER *continued*

REF DES	DANA P/N	DESCRIPTION				FSC	MANU P/N
CR9	211083	DIODE	SILICO		018	21793	211083
CR10	211083	DIODE	SILICO		018	21793	211083
CR11	211083	DIODE	SILICO		018	21793	211083
CR12	211083	DIODE	SILICO		018	21793	211083
CR13	211083	DIODE	SILICO		018	21793	211083
CR14	220022	DIODE	SILICO	ZENER	1N965B	81349	1N965B
CR15	220007	DIODE	SILICO	ZENER	1N751A	81349	1N751A
CR16	211083	DIODE	SILICO		018	21793	211083
CR17	211083	DIODE	SILICO		018	21793	211083
CR18	220007	DIODE	SILICO	ZENER	1N751A	81349	1N751A
CR19	220007	DIODE	SILICO	ZENER	1N751A	81349	1N751A
CR20	211083	DIODE	SILICO		018	21793	211083
CR21	211083	DIODE	SILICO		018	21793	211083
CR22	220022	DIODE	SILICO	ZENER	1N965B	81349	1N965B
CR25	220022	DIODE	SILICO	ZENER	1N965B	81349	1N965B
CR26	220031	DIODE	SILICO	ZENER	3.3 V	04713	1/4M3.3AZ5
CR27	211083	DIODE	SILICO		018	21793	211083
CR28	211083	DIODE	SILICO		018	21793	211083
CR29	220031	DIODE	SILICO	ZENER	3.3 V	04713	1/4M3.3AZ5
CR31	221177	DIODE	SILICO	ZENER		04713	1/4M.2.4AZ5
Q1	200088	TRANS	SILICO	PNP	2N4248	80131	2N4248
Q2	200088	TRANS	SILICO	PNP	2N4248	80131	2N4248
Q3	200088	TRANS	SILICO	PNP	2N4248	80131	2N4248
Q4	200088	TRANS	SILICO	PNP	2N4248	80131	2N4248
Q5	200088	TRANS	SILICO	PNP	2N4248	80131	2N4248
Q6	200088	TRANS	SILICO	PNP	2N4248	80131	2N4248
Q7	200088	TRANS	SILICO	PNP	2N4248	80131	2N4248
Q8	200162	TRANS	FET	MATCHED SET		21793	200162
Q9	200224	TRANS	SELECTED	NPN	200224	21793	200224
Q10	200162	TRANS	FET	MATCHED SET		21793	200162
Q11	200224	TRANS	SELECTED	NPN	200224	21793	200224
Q12	200162	TRANS	FET	MATCHED SET		21793	200162
Q13	200224	TRANS	SELECTED	NPN	200224	21793	200224
Q16	200037	TRANS	SILICO	NPN	2N3646	80131	2N3646
Q17	200161	TRANS	SELECTED	FET	E415	17856	E415
Q18	200037	TRANS	SILICO	NPN	2N3646	80131	2N3646
Q19	200160	TRANS	FET		E-304	17856	E-304
Q20	200160	TRANS	FET		E-304	17856	E-304
Q21	200037	TRANS	SILICO	NPN	2N3646	80131	2N3646
Q22	200200	TRANS		NPN	200200	21793	200200
Q23	200037	TRANS	SILICO	NPN	2N3646	80131	2N3646

403626 – Assy., PCB, DIGITIZER *continued*

REF DES	DANA P/N	DESCRIPTION						FSC	MANU P/N
Q24	200203	TRANS		FET	E-201			17856	E-201
Q25	200203	TRANS		FET	E-201			17856	E-201
Q26	200088	TRANS	SILICO	PNP	2N4248			80131	2N4248
Q27	200088	TRANS	SILICO	PNP	2N4248			80131	2N4248
Q28	200200	TRANS		NPN	200200			21793	200200
Q29	200200	TRANS		NPN	200200			21793	200200
Q30	200037	TRANS	SILICO	NPN	2N3646			80131	2N3646
Q31	200099	TRANS			2N4258			81349	2N4258
Q32	200200	TRANS		NPN	200200			21793	200200
Q33	200200	TRANS		NPN	200200			21793	200200
Q34	200200	TRANS		NPN	200200			21793	200200
Q35	200088	TRANS	SILICO	PNP	2N4248			80131	2N4248
Q36	200088	TRANS	SILICO	PNP	2N4248			80131	2N4248
Q37	200200	TRANS		NPN	200200			21793	200200
Q38	200068	TRANS		PNP	2N4250			80131	2N4250
Q41	200201	TRANS	DUAL	NPN	200201			21793	200201
Q43	200037	TRANS	SILICO	NPN	2N3646			80131	2N3646
Q44	200200	TRANS		NPN	200200			21793	200200
Q45	200200	TRANS		NPN	200200			21793	200200
Q46	200200	TRANS		NPN	200200			21793	200200
R1	000273	RES	CARBON	27 K		5%	1/4W	81349	RC07GF273J
R2	040240	POT	CERMET	10 OHM		20%		73138	89PR10
R3	000101	RES	CARBON	100 OHM		5%	1/4W	81349	RC07GF101J
R4	000101	RES	CARBON	100 OHM		5%	1/4W	81349	RC07GF101J
R5	000103	RES	CARBON	10 K		5%	1/4W	81349	RC07GF103J
R6	000104	RES	CARBON	100 K		5%	1/4W	81349	RC07GF104J
R7	000202	RES	CARBON	2 K		5%	1/4W	81349	RC07GF202J
R8	000104	RES	CARBON	100 K		5%	1/4W	81349	RC07GF104J
R9	000202	RES	CARBON	2 K		5%	1/4W	81349	RC07GF202J
R10	000202	RES	CARBON	2 K		5%	1/4W	81349	RC07GF202J
R11	000302	RES	CARBON	3 K		5%	1/4W	81349	RC07GF302J
R12	000152	RES	CARBON	1.5 K		5%	1/4W	81349	RC07GF152J
R13	000302	RES	CARBON	3 K		5%	1/4W	81349	RC07GF302J
R14	000152	RES	CARBON	1.5 K		5%	1/4W	81349	RC07GF152J
R15	000103	RES	CARBON	10 K		5%	1/4W	81349	RC07GF103J
R16	000104	RES	CARBON	100 K		5%	1/4W	81349	RC07GF104J
R17	000104	RES	CARBON	100 K		5%	1/4W	81349	RC07GF104J
R18*	010621	RES	METAL	49.9 K		1%	1/10W	81349	RN55C4992F
R19	000152	RES	CARBON	1.5 K		5%	1/4W	81349	RC07GF152J
R20	000153	RES	CARBON	15 K		5%	1/4W	81349	RC07GF153J
R21	000511	RES	CARBON	510 OHM		5%	1/4W	81349	RC07GF511J

*In 50 Hz units, R18 is:

R18 010812 RES METAL 59 K 1% 1/10W 81349 RN55C5902F

403626 – Assy., PCB, DIGITIZER *continued*

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
R22	000511	RES CARBON 510 OHM 5% 1/4W	81349	RC07GF511J
R23	000511	RES CARBON 510 OHM 5% 1/4W	81349	RC07GF511J
R24	000153	RES CARBON 15 K 5% 1/4W	81349	RC07GF153J
R25	000272	RES CARBON 2.7 K 5% 1/4W	81349	RC07GF272J
R26	010013	RES METAL 60.4 K T-2 1% 1/8W	81349	RN60C6042F
R27	040228	POT CERMET 500 OHM 10%	73138	89PR500
R28	010621	RES METAL 49.9 K 1% 1/10W	81349	RN55C4992F
R29	000473	RES CARBON 47 K 5% 1/4W	81349	RC07GF473J
R30	010013	RES METAL 60.4 K T-2 1% 1/8W	81349	RN60C6042F
R31	010502	RES METAL 249 K 1% 1/8W	81349	RN60D2493F
R32	000302	RES CARBON 3 K 5% 1/4W	81349	RC07GF302J
R33	000153	RES CARBON 15 K 5% 1/4W	81349	RC07GF153J
R34	000104	RES CARBON 100 K 5% 1/4W	81349	RC07GF104J
R35	010027	RES METAL 499 OHM T-0 1% 1/8W	81349	RN60D4990F
R36	010621	RES METAL 49.9 K 1% 1/10W	81349	RN55C4992F
R37	600245	JUMPER L-2007-1LP		L-2007-1LP
R38	000107	RES CARBON 100 M 5% 1/4W	81349	RC07GF107J
R39	001737	RES CARBON FSV 5% 1/4W	21793	001737
R40	040234	POT CERMET 50 K 10%	73138	89PR50K
R41	000272	RES CARBON 2.7 K 5% 1/4W	81349	RC07GF272J
R42	000153	RES CARBON 15 K 5% 1/4W	81349	RC07GF153J
R43	000203	RES CARBON 20 K 5% 1/4W	81349	RC07GF203J
R44	000104	RES CARBON 100 K 5% 1/4W	81349	RC07GF104J
R45	001737	RES CARBON FSV 5% 1/4W	21793	001737
R46	000272	RES CARBON 2.7 K 5% 1/4W	81349	RC07GF272J
R47	000153	RES CARBON 15 K 5% 1/4W	81349	RC07GF153J
R50	000392	RES CARBON 3.9 K 5% 1/4W	81349	RC07GF392J
R51	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R52	000204	RES CARBON 200 K 5% 1/4W	81349	RC07GF204J
R53	000511	RES CARBON 510 OHM 5% 1/4W	81349	RC07GF511J
R54	000511	RES CARBON 510 OHM 5% 1/4W	81349	RC07GF511J
R55	000392	RES CARBON 3.9 K 5% 1/4W	81349	RC07GF392J
R56	000152	RES CARBON 1.5 K 5% 1/4W	81349	RC07GF152J
R57	040235	POT CERMET 100 K 10%	73138	89PR100K
R58	000390	RES CARBON 39 OHM 5% 1/4W	81349	RC07GF390J
R59	000102	RES CARBON 1 K 5% 1/4W	81349	RC07GF102J
R60	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R61	000622	RES CARBON 6.2 K 5% 1/4W	81349	RC07GF622J
R62	000206	RES CARBON 20 M 5% 1/4W	81349	RC07GF206J
R63	010871	RES MATCHED SET 5 K	21793	010871
R64	040235	POT CERMET 100 K 10%	73138	89PR100K

403626 – Assy., PCB, DIGITIZER *continued*

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
R65	000100	RES CARBON 10 OHM 5% 1/4W	81349	RC07GF100J
R66	010520	RES METAL 21.5 K 1% 1/10W	81349	RN60D2152F
R67	010871	RES MATCHED SET 5 K	21793	010871
R68	010646	RES METAL 2.49 K 1% 1/10W	81349	RN55C2491F
R70	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R71	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R72	010124	RES METAL 19.6 K T-0 1% 1/8W	81349	RN60D1962F
R73	000750	RES CARBON 75 OHM 5% 1/4W	81349	RC07GF750J
R74	010533	RES METAL 28.7 K 1% 1/10W	81349	RN55C2872F
R75	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R76	000390	RES CARBON 39 OHM 5% 1/4W	81349	RC07GF390J
R77	000513	RES CARBON 51 K 5% 1/4W	81349	RC07GF513J
R78	000101	RES CARBON 100 OHM 5% 1/4W	81349	RC07GF100J
R79	000510	RES CARBON 51 OHM 5% 1/4W	81349	RC07GF510J
R80	000200	RES CARBON 20 OHM 5% 1/4W	81349	RC07GF200J
R81	000183	RES CARBON 18 K 5% 1/4W	81349	RC07GF183J
R82	000202	RES CARBON 2 K 5% 1/4W	81349	RC07GF202J
R83	000104	RES CARBON 100 K 5% 1/4W	81349	RC07GF104J
R84	000565	RES CARBON 5.6 M 5% 1/4W	81349	RC07GF565J
R85	000100	RES CARBON 10 OHM 5% 1/4W	81349	RC07GF100J
R86	000105	RES CARBON 1 M 5% 1/4W	81349	RC07GF105J
R87	000204	RES CARBON 200 K 5% 1/4W	81349	RC07GF204J
R88	000684	RES CARBON 680 K 5% 1/4W	81349	RC07GF684J
T1	300087	TRANSFORMER PULSE 300087	21793	300087
T2	300087	TRANSFORMER PULSE 300087	21793	300087
T3	300087	TRANSFORMER PULSE 300087	21793	300087
TP1	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP2	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP3	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP4	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP5	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP6	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP7	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP8	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP9	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP10	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP11	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0
TP12	600786	POST MACHINE APPLIED STRIP 1-87022-0	00779	1-87022-0

403626 – Assy., PCB, DIGITIZER *continued*

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
AR1	230054	INTEGRATED CIRCUIT LM301A	27014	LM301A
AR2	230090	INTEGRATED CIRCUIT µA725	07263	SL22348
U1	230076	INTEGRATED CIRCUIT 74L00	07263	74L00
U2	230076	INTEGRATED CIRCUIT 74L00	07263	74L00

403616 – Assy., PCB, PROGRAM (Figure 6.14)

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
A1	403682	CABLE Assy. (2)	21793	403682
C1	110152	CAP TANTA .47 MFD 50 V 10%	05397	T368A474K050AS
C2	110165	CAP TANTA .15 MFD 35 V 10%	05397	T368A154K035AS
C3	110153	CAP TANTA .27 MFD 50 V 10%	05397	KR27P50K
C4	121091	CAP MYLAR .033 MFD 100 V 10%	09023	WMF1S33
C5	120308	CAP POLY .047 MFD 250 V 10%	73445	C280MAE/A47K
C6	120308	CAP POLY .047 MFD 250 V 10%	73445	C280MAE/A47K
CR1	211083	DIODE SILICO 018	21793	211083
CR2	211083	DIODE SILICO 018	21793	211083
CR3	211083	DIODE SILICO 018	21793	211083
Q1	200200	TRANS NPN 200200	21793	200200
Q2	200200	TRANS NPN 200200	21793	200200
Q3	200200	TRANS NPN 200200	21793	200200
Q4	200200	TRANS NPN 200200	21793	200200
Q5	200200	TRANS NPN 200200	21793	200200
Q6	200136	TRANS SILICO NPN 2N5963	81349	2N5963
Q7	200088	TRANS SILICO PNP 2N4248	80131	2N4248
Q8	200037	TRANS SILICO NPN 2N3646	80131	2N3646
Q9	200037	TRANS SILICO NPN 2N3646	80131	2N3646
Q10	200037	TRANS SILICO NPN 2N3646	80131	2N3646
R1	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R2	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R3	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R4	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R5	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R6	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R7	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R8	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R9	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R10	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R11	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R12	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R13	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R14	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R15	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R16	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R17	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R18	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R19	000432	RES CARBON 4.3 K 5% 1/4W	81349	RC07GF432J
R20	000432	RES CARBON 4.3 K 5% 1/4W	81349	RC07GF432J
R21	000432	RES CARBON 4.3 K 5% 1/4W	81349	RC07GF432J
R22	000432	RES CARBON 4.3 K 5% 1/4W	81349	RC07GF432J

403616 – Assy., PCB, PROGRAM *continued*

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
R23	010650	RES METAL 1 M 1% 1/10W	81349	RN55E1004F
R24	000105	RES CARBON 1 M 5% 1/4W	81349	RC07GF105J
R25	010529	RES METAL 10 K 1% 1/10W	81349	RN55C1002F
R26	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R27	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R28	010676	RES METAL 15 K 1% 1/10W	81349	RN55C1502F
R29	000242	RES CARBON 2.4 K 5% 1/4W	81349	RC07GF242J
R30	010697	RES METAL 26.7 K 1% 1/10W	81349	RN55D2672F
R31	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R32	000222	RES CARBON 2.2 K 5% 1/4W	81349	RC07GF222J
R33	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R34	000242	RES CARBON 2.4 K 5% 1/4W	81349	RC07GF242J
R35	000511	RES CARBON 510 OHM 5% 1/4W	81349	RC07GF511J
R36	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R37	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R38	000222	RES CARBON 2.2 K 5% 1/4W	81349	RC07GF222J
R39	000222	RES CARBON 2.2 K 5% 1/4W	81349	RC07GF222J
U1	230029	INTEGRATED CIRCUIT 7401	01295	7401
U2	230114	INTEGRATED CIRCUIT 7408	07263	7408
U3	230029	INTEGRATED CIRCUIT 7401	01295	7401
U4	230029	INTEGRATED CIRCUIT 7401	01295	7401
U5	230028	INTEGRATED CIRCUIT 7400	01295	7400
U6	230065	INTEGRATED CIRCUIT 7475	01295	7475
U7	230065	INTEGRATED CIRCUIT 7475	01295	7475
U8	230065	INTEGRATED CIRCUIT 7475	01295	7475
U9	230029	INTEGRATED CIRCUIT 7401	01295	7401
U10	230029	INTEGRATED CIRCUIT 7401	01295	7401
U11	230115	INTEGRATED CIRCUIT 7409	07263	7409
U12	230029	INTEGRATED CIRCUIT 7401	01295	7401
U13	230028	INTEGRATED CIRCUIT 7400	01295	7400

403615 – Assy., PCB, DISPLAY (Figure 6.16)

REF DES	DANA P/N	DESCRIPTION					FSC	MANU P/N
CR1	220031	DIODE	SILICO	ZENER	3.3 V		04713	1/4M3.3AZ5
DS1	920116	LAMP	NEON	NE-2H			08806	C2A
DS2	920116	LAMP	NEON	NE-2H			08806	C2A
DS3	920116	LAMP	NEON	NE-2H			08806	C2A
DS4	920116	LAMP	NEON	NE-2H			08806	C2A
DS5	920116	LAMP	NEON	NE-2H			08806	C2A
DS6	920116	LAMP	NEON	NE-2H			08806	C2A
DS7	920116	LAMP	NEON	NE-2H			08806	C2A
DS8	920116	LAMP	NEON	NE-2H			08806	C2A
DS9	920116	LAMP	NEON	NE-2H			08806	C2A
DS10	920116	LAMP	NEON	NE-2H			08806	C2A
DS11	920116	LAMP	NEON	NE-2H			08806	C2A
Q1	200087	TRANS	SILICO		021		21793	200087
Q2	200087	TRANS	SILICO		021		21793	200087
Q3	200087	TRANS	SILICO		021		21793	200087
Q4	200087	TRANS	SILICO		021		21793	200087
Q5	200087	TRANS	SILICO		021		21793	200087
Q6	200087	TRANS	SILICO		021		21793	200087
Q7	200087	TRANS	SILICO		021		21793	200087
Q8	200087	TRANS	SILICO		021		21793	200087
Q9	200087	TRANS	SILICO		021		21793	200087
Q10	200087	TRANS	SILICO		021		21793	200087
Q11	200087	TRANS	SILICO		021		21793	200087
R1	000512	RES	CARBON	5.1 K		5% ¼W	81349	RC07GF512J
R2	000512	RES	CARBON	5.1 K		5% ¼W	81349	RC07GF512J
R3	000512	RES	CARBON	5.1 K		5% ¼W	81349	RC07GF512J
R4	000512	RES	CARBON	5.1 K		5% ¼W	81349	RC07GF512J
R5	000512	RES	CARBON	5.1 K		5% ¼W	81349	RC07GF512J
R6	000512	RES	CARBON	5.1 K		5% ¼W	81349	RC07GF512J
R7	000512	RES	CARBON	5.1 K		5% ¼W	81349	RC07GF512J
R8	000512	RES	CARBON	5.1 K		5% ¼W	81349	RC07GF512J
R9	000272	RES	CARBON	2.7 K		5% ¼W	81349	RC07GF272J
R10	000272	RES	CARBON	2.7 K		5% ¼W	81349	RC07GF272J
R11	000272	RES	CARBON	2.7 K		5% ¼W	81349	RC07GF272J
R12	000272	RES	CARBON	2.7 K		5% ¼W	81349	RC07GF272J
R13	000272	RES	CARBON	2.7 K		5% ¼W	81349	RC07GF272J
R14	000272	RES	CARBON	2.7 K		5% ¼W	81349	RC07GF272J
R15	000272	RES	CARBON	2.7 K		5% ¼W	81349	RC07GF272J
R16	000272	RES	CARBON	2.7 K		5% ¼W	81349	RC07GF272J
R17	000132	RES	CARBON	1.3 K		5% ¼W	81349	RC07GF132J
R18	000102	RES	CARBON	1 K		5% ¼W	81349	RC07GF102J
R19	000512	RES	CARBON	5.1 K		5% ¼W	81349	RC07GF512J
R20	000512	RES	CARBON	5.1 K		5% ¼W	81349	RC07GF512J
R21	000132	RES	CARBON	1.3 K		5% ¼W	81349	RC07GF132J

403615 – Assy., PCB, DISPLAY (Figure 6.16) *continued*

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
R22	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R23	000132	RES CARBON 1.3 K 5% 1/4W	81349	RC07GF132J
R24	000102	RES CARBON 1 K 5% 1/4W	81349	RC07GF102J
R25	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R26	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R27	000102	RES CARBON 1 K 5% 1/4W	81349	RC07GF102J
R28	000272	RES CARBON 2.7 K 5% 1/4W	81349	RC07GF272J
R29	000102	RES CARBON 1 K 5% 1/4W	81349	RC07GF102J
R30	000202	RES CARBON 2 K 5% 1/4W	81349	RC07GF202J
R31	000512	RES CARBON 5.1 K 5% 1/4W	81349	RC07GF512J
R32	000102	RES CARBON 1 K 5% 1/4W	81349	RC07GF102J
R33	000331	RES CARBON 330 OHM 5% 1/4W	81349	RC07GF331J
R34	000153	RES CARBON 15 K 5% 1/4W	81349	RC07GF153J
R35	000153	RES CARBON 15 K 5% 1/4W	81349	RC07GF153J
S1	600675	SWITCH ROTARY	11237	MOD 215
S2	600674	SWITCH ROTARY	11237	MOD 215
U1	230115	INTEGRATED CIRCUIT 7409	07263	7409
U2	230115	INTEGRATED CIRCUIT 7409	07263	7409
U3	230064	INTEGRATED CIRCUIT 7404	01295	7404
U4	230067	INTEGRATED CIRCUIT 74192	07263	74192
U5	230074	INTEGRATED CIRCUIT 7442	07263	7442
U6	230114	INTEGRATED CIRCUIT 7408	07263	7408
U7	230073	INTEGRATED CIRCUIT 7405	01295	7405
U8	230028	INTEGRATED CIRCUIT 7400	01295	7400
U9	230030	INTEGRATED CIRCUIT 7402	01295	7402
U10	230030	INTEGRATED CIRCUIT 7402	01295	7402
U11	230030	INTEGRATED CIRCUIT 7402	01295	7402
U12	230116	INTEGRATED CIRCUIT 7432	07263	7432
U13	230115	INTEGRATED CIRCUIT 7409	07263	7409
U14	230031	INTEGRATED CIRCUIT 7410	01295	7410
U15	230029	INTEGRATED CIRCUIT 7401	01295	7401
U16	230073	INTEGRATED CIRCUIT 7405	01295	7405
Z1	080014	RES NETWORK 15 K	11237	750-61-R15K

403667 – Assy., PCB, AC CONVERTER (Figure 6.19)

REF DES	DANA P/N	DESCRIPTION					FSC	MANU P/N
B1	920563	BEAD	SHIELDING		56-59065-4B		02114	56-59065-4B
B2	920563	BEAD	SHIELDING		56-59065-4B		02114	56-59065-4B
C2	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C3	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C4	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C5	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C6	100038	CAP	CERAM	560 PFD	500 V	10%	71590	DD561
C7	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C8	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C9	100012	CAP	CERAM	33 PFD	500 V	10%	71471	TCD-DI-1(N750)
C10	100038	CAP	CERAM	560 PFD	500 V	10%	71590	DD561
C11	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C12	100095	CAP	CERAM	2.7±.5 PFD	1000 V		56289	C030B102S2R7D
C13	130131	CAP	TRIMMER	2-20 PFD	100 V		73445	C010KA/20E
C14	100095	CAP	CERAM	2.7±.5 PFD	1000 V		56289	C030B102S2R7D
C15	121394	CAP	MYLAR	.15 MFD	100 V	10%	09023	WMF1P15
C16	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C17	100012	CAP	CERAM	33 PFD	500 V	10%	71471	TCD-DI-1(N750)
C18	120130	CAP	MYLAR	.15 MFD	100 V	5%	27556	XT2B154J
C19	121394	CAP	MYLAR	.15 MFD	100 V	10%	09023	WMF1P15
C20	100081	CAP	CERAM	4.7 PFD	1000 V	5%	56289	C030B102E4R7D
C21	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C22	110151	CAP	TANTA	10 MFD	35 V	20%	05397	T362C106M035A
C23	110125	CAP	TANTA	2.2 MFD	35 V	20%	05397	T368B225M035AS
C24	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C25	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C26	120025	CAP	MYLAR	1.5 MFD	100 V	10%	27556	XA2B155K
C27	100012	CAP	CERAM	33 PFD	500 V	10%	71471	TCD-DI-1(N750)
C28	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C29	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C30	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
CR1	220004	DIODE	SILICO	ZENER	1N961B		81349	1N961B
CR2	220004	DIODE	SILICO	ZENER	1N961B		81349	1N961B
CR3	210035	DIODE	HOT CARRIER		HP5082-2810		50434	HP5082-2810
CR4	210035	DIODE	HOT CARRIER		HP5082-2810		50434	HP5082-2810
Q1	200200	TRANS		NPN	200200		21793	200200
Q2	200200	TRANS		NPN	200200		21793	200200
Q3	200201	TRANS	DUAL	NPN	200201		21793	200201
Q4	200197	TRANS	SILICO	NPN	MPS-H10		04713	MPS-H10

403667 – Assy., PCB, AC CONVERTER *continued*

REF DES	DANA P/N	DESCRIPTION						FSC	MANU P/N
Q5	200197	TRANS	SILICO	NPN	MPS-H10			04713	MPS-H10
Q6	200178	TRANS		PNP	2N5910			81349	2N5910
Q8	200197	TRANS	SILICO	NPN	MPS-H10			04713	MPS-H10
Q9	200197	TRANS	SILICO	NPN	MPS-H10			04713	MPS-H10
Q10	200178	TRANS		PNP	2N5910			81349	2N5910
Q11	200197	TRANS	SILICO	NPN	MPS-H10			04713	MPS-H10
Q12	200201	TRANS	DUAL	NPN	200201			21793	200201
Q13	200200	TRANS		NPN	200200			21793	200200
Q14	200200	TRANS		NPN	200200			21793	200200
Q15	200068	TRANS		PNP	2N4250			80131	2N4250
Q16	200179	TRANS	FET		KE4391			27014	KE4391
R1	000100	RES	CARBON	10 OHM		5%	1/4W	81349	RC07GF100J
R2	010727	RES	METAL	2.5 K		.01%		18612	V53-1M
R3	000102	RES	CARBON	1 K		5%	1/4W	81349	RC07GF102J
R4	010650	RES	METAL	1 M		1%	1/10W	81349	RN55E1004F
R5	040234	POT	CERMET	50 K		10%		73138	89PR50K
R6	000333	RES	CARBON	33 K		5%	1/4W	81349	RC07GF333J
R7	010650	RES	METAL	1 M		1%	1/10W	81349	RN55E1004F
R8	000105	RES	CARBON	1 M		5%	1/4W	81349	RC07GF105J
R9	000163	RES	CARBON	16 K		5%	1/4W	81349	RC07GF163J
R10	000183	RES	CARBON	18 K		5%	1/4W	81349	RC07GF183J
R11	000332	RES	CARBON	3.3 K		5%	1/4W	81349	RC07GF332J
R13	000103	RES	CARBON	10 K		5%	1/4W	81349	RC07GF103J
R14	000151	RES	CARBON	150 OHM		5%	1/4W	81349	RC07GF151J
R15	010529	RES	METAL	10 K		1%	1/10W	81349	RN55C1002F
R17	000512	RES	CARBON	5.1 K		5%	1/4W	81349	RC07GF512J
R18	010660	RES	METAL	5 K		.01%		18612	V53-1
R19	000333	RES	CARBON	33 K		5%	1/4W	81349	RC07GF333J
R20	000101	RES	CARBON	100 OHM		5%	1/4W	81349	RC07GF101J
R21	000510	RES	CARBON	51 OHM		5%	1/4W	81349	RC07GF510J
R22	000101	RES	CARBON	100 OHM		5%	1/4W	81349	RC07GF101J
R23	010829	RES	METAL	4.99 K		1%	1/10W	81349	RN55C4991F
R24	000161	RES	CARBON	160 OHM		5%	1/4W	81349	RC07GF161J
R25	000911	RES	CARBON	910 OHM		5%	1/4W	81349	RC07GF911J
R26	010529	RES	METAL	10 K		1%	1/10W	81349	RN55C1002F
R27	000101	RES	CARBON	100 OHM		5%	1/4W	81349	RC07GF101J
R28	000101	RES	CARBON	100 OHM		5%	1/4W	81349	RC07GF101J
R29	000153	RES	CARBON	15 K		5%	1/4W	81349	RC07GF153J
R30	000751	RES	CARBON	750 OHM		5%	1/4W	81349	RC07GF751J
R31	000821	RES	CARBON	820 OHM		5%	1/4W	81349	RC07GF821J
R32	010661	RES	METAL	10 K		.01%		18612	V53-1M
R33	010808	RES	METAL	10 K		.1%	1/10W	81349	RN55C1002B
R34	010650	RES	METAL	1 M		1%	1/10W	81349	RN55E1004F

403667 – Assy., PCB, AC CONVERTER *continued*

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
R35	040234	POT CERMET 50 K 10%	73138	89PR50K
R36	000105	RES CARBON 1 M 5% 1/4W	81349	RC07GF105J
R37	010650	RES METAL 1 M 1% 1/10W	81349	RN55E1004F
R38	000562	RES CARBON 5.6 K 5% 1/4W	81349	RC07GF562J
R39	020657	RES WW 11.109 K .01% .15W	22045	J-110
R40	000113	RES CARBON 11 K 5% 1/4W	81349	RC07GF113J
R41	000201	RES CARBON 200 OHM 5% 1/4W	81349	RC07GF201J
R42	000107	RES CARBON 100 M 5% 1/4W	81349	RC07GF107J
R43	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R44	000103	RES CARBON 10 K 5% 1/4W	81349	RC07GF103J
R45	010536	RES METAL 100 K 1% 1/10W	81349	RN55C1003F
R46	010813	RES METAL 7.87 K 1% 1/10W	81349	RN55C7871F
R47	000124	RES CARBON 120 K 5% 1/4W	81349	RC07GF124J
R48	000106	RES CARBON 10 M 5% 1/4W	81349	RC07GF106J
R49	000201	RES CARBON 200 OHM 5% 1/4W	81349	RC07GF201J
TP1	600591	TEST POINT 85931-6	00779	85931-6
TP2	600591	TEST POINT 85931-6	00779	85931-6
TP3	600591	TEST POINT 85931-6	00779	85931-6
TP4	600591	TEST POINT 85931-6	00779	85931-6
TP5	600591	TEST POINT 85931-6	00779	85931-6
TP6	600591	TEST POINT 85931-6	00779	85931-6
AR1	230054	INTEGRATED CIRCUIT LM301A	27014	LM301A
AR2	230103	INTEGRATED CIRCUIT LM308	27014	LM308
AR3	230103	INTEGRATED CIRCUIT LM308	27014	LM308
W1	600245	JUMPER L-2007-1LP		L-2007-1LP

403774 – Assy., PCB, RMS AC CONVERTER

REF DES	DANA P/N	DESCRIPTION					FSC	MANU P/N
B1	920563	BEADS			56-59065-4B		02114	56-59065-4B
C1	130116	CAP	MICA	3900 PFD	500 V	2%	72136	DM19F392G0
C2	130160	CAP	MICA	330 PFD			72136	DM15 Series
C3	100100	CAP	CERAM	FSV			21793	100100
C4	130124	CAP	TRIMMER	1.7-10 PFD	250 V		52763	R-TRIKO-122-09SD
C5	100100	CAP	CERAM	FSV			21793	100100
C6	100054	CAP	CERAM	27 PFD	1000 V	5%	56289	C030B102G270J
C7	130123	CAP	TRIMMER	1-3 PFD	250 V		52763	R-TRIKO-122-09SD
C8	100095	CAP	CERAM	2.7±.5 PFD	1000 V		56289	C030B102S2R7D
C9	130146	CAP	TRIMMER	.25-1.5 PFD			74970	273-0001-002
C10	100100	CAP	CERAM	FSV			21793	100100
C11	130146	CAP	TRIMMER	.25-1.5 PFD			74970	273-0001-002
C12	120280	CAP	MYLAR	.22 MFD	1000 V	10%	27556	ZA2J224K
C14	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C15	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C16	101098	CAP	CERAM	330 PFD	1000 V	10%	56289	C016B102E331K
C17	101641	CAP	CERAM	470 PFD	500 V	10%	71471	SCD1X5F
C18	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C19	130076	CAP	MICA	200 PFD	500 V	5%	72136	DM15-201J
C20	101642	CAP	CERAM	150 PFD	500 V	10%	71471	SCD1X5F
C21	120026	CAP	MYLAR	.47 MFD	100 V	10%	27556	SA2B474K
C22	101174	CAP	CERAM	.001 MFD	500 V	10%	04222	SCD-DI-2X5F-1000
C23	120026	CAP	MYLAR	.47 MFD	100 V	10%	27556	SA2B474K
C24	120026	CAP	MYLAR	.47 MFD	100 V	10%	27556	SA2B474K
C25	101641	CAP	CERAM	470 PFD	500 V	10%	71471	SCD1X5F
C26	101098	CAP	CERAM	330 PFD	1000 V	10%	56289	C016B102E331K
C27	100100	CAP	CERAM	FSV			21793	100100
C29	110140	CAP	TANTA	47 MFD	6 V	20%	05397	T368B476M006AS
C30	120286	CAP	MYLAR	.1 MFD	100 V	20%	73445	C281AH/A100K
C31	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C32	101641	CAP	CERAM	470 PFD	500 V	10%	71471	SCD1X5F
C33	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
CR4	211083	DIODE	SILICO		018		21793	211083
CR5	211083	DIODE	SILICO		018		21793	211083
CR6	211083	DIODE	SILICO		018		21793	211083
CR8	210035	DIODE	HOT CARRIER		HP5082-2810		50434	HP5082-2810
CR9	210035	DIODE	HOT CARRIER		HP5082-2810		50434	HP5082-2810
CR11	220022	DIODE	SILICO	ZENER	1N965B		81349	1N965B
CR12	220022	DIODE	SILICO	ZENER	1N965B		81349	1N965B
CR13	220022	DIODE	SILICO	ZENER	1N965B		81349	1N965B

403774 – Assy., PCB, RMS AC CONVERTER *continued*

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
K1	310078	RELAY REED 28 V	15636	R2690-3
K2	310078	RELAY REED 28 V	15636	R2690-3
K3	310078	RELAY REED 28 V	15636	R2690-3
Q1	200200	TRANS NPN 200200	21793	200200
Q2	200200	TRANS NPN 200200	21793	200200
Q4	200199	TRANS FET	27014	FM1302
Q5	200200	TRANS NPN 200200	21793	200200
Q6	200201	TRANS DUAL NPN 200201	21793	200201
Q7	200112	TRANS MATCHED PAIR 200112	21793	200112
Q8	200112	TRANS MATCHED PAIR 200112	21793	200112
Q9	200068	TRANS PNP 2N4250	80131	2N4250
Q10	200200	TRANS NPN 200200	21793	200200
Q11	200220	TRANS DUAL	21793	200220
Q12	200136	TRANS SILICO NPN 2N5963	81349	2N5963
Q13	200068	TRANS PNP 2N4250	80131	2N4250
Q14	403865	TRANS MATCHED LOG TRANS KIT	21793	403865
Q15	403865	TRANS MATCHED LOG TRANS KIT	21793	403865
Q19	200068	TRANS PNP 2N4250	80131	2N4250
Q20	200197	TRANS SILICO NPN MPS-H10	04713	MPS-H10
Q21	200197	TRANS SILICO NPN MPS-H10	04713	MPS-H10
AR1	230180	INTEGRATED CIRCUIT LM318H	27014	LM318H
AR2	230054	INTEGRATED CIRCUIT LM301A	27014	LM301A
R1	010721	RES METAL MATCHED SET	21793	010721
R2	010720	RES METAL 9.09 K .25% 1/10W	81349	RN55C9091C
R3	040228	POT CERMET 500 OHM 10%	73138	89PR500
R4	010721	RES METAL MATCHED SET	21793	010721
R5	040225	POT CERMET 50 OHM 20%	73138	89PR50
R6	010721	RES METAL MATCHED SET	21793	010721
R7	040228	POT CERMET 500 OHM 10%	73138	89PR500
R8	010721	RES METAL MATCHED SET	21793	010721
R9	000242	RES CARBON 2.4 K 5% 1/4W	81349	RC07GF242J
R10	010721	RES METAL MATCHED SET	21793	010721
R11	001737	RES CARBON FSV 5% 1/4W	21793	001737
R12	010542	RES METAL 100 K 1% 1/10W	81349	RN55E1003F
R13	010542	RES METAL 100 K 1% 1/10W	81349	RN55E1003F
R14	001737	RES CARBON FSV 5% 1/4W	21793	001737
R15	000101	RES CARBON 100 OHM 5% 1/4W	81349	RC07GF101J
R16	000514	RES CARBON 510 K 5% 1/4W	81349	RC07GF514J
R17	040235	POT CERMET 100 K 10%	73138	89PR100K

403774 – Assy., PCB, RMS AC CONVERTER *continued*

REF DES	DANA P/N	DESCRIPTION					FSC	MANU P/N
R18	000183	RES	CARBON	18 K	5%	1/4W	81349	RC07GF183J
R23	000101	RES	CARBON	100 OHM	5%	1/4W	81349	RC07GF101J
R24	010586	RES	METAL	5 K	.02%		18612	V53-1M
R25	010586	RES	METAL	5 K	.02%		18612	V53-1M
R26	010654	RES	METAL	20 K	.05%		18612	V53-1
R27	010615	RES	METAL	10 K	.02%		18612	V53-1M
R28	040225	POT	CERMET	50 OHM	20%		73138	89PR50
R29	000512	RES	CARBON	5.1 K	5%	1/4W	81349	RC07GF512J
R30	000105	RES	CARBON	1 M	5%	1/4W	81349	RC07GF105J
R31	040235	POT	CERMET	100 K	10%		73138	89PR100K
R32	000184	RES	CARBON	180 K	5%	1/4W	81349	RC07GF184J
R33	040225	POT	CERMET	50 OHM	20%		73138	89PR50
R34	010684	RES	METAL	487 OHM	1%	1/10W	81349	RN55E4870F
R35	010684	RES	METAL	487 OHM	1%	1/10W	81349	RN55E4870F
R36	000101	RES	CARBON	100 OHM	5%	1/4W	81349	RC07GF101J
R37	000123	RES	CARBON	12 K	5%	1/4W	81349	RC07GF123J
R38	000272	RES	CARBON	2.7 K	5%	1/4W	81349	RC07GF272J
R39	000101	RES	CARBON	100 OHM	5%	1/4W	81349	RC07GF101J
R40	000681	RES	CARBON	680 OHM	5%	1/4W	81349	RC07GF681J
R41	040239	POT	CERMET	1 M	20%		73138	89PR1M
R42	040232	POT	CERMET	10 K	10%		73138	89PR10K
R43	010621	RES	METAL	49.9 K	1%	1/10W	81349	RN55C4992F
R44	000476	RES	CARBON	47 M	5%	1/4W	81349	RC07GF476J
R45	000103	RES	CARBON	10 K	5%	1/4W	81349	RC07GF103J
R46	010533	RES	METAL	28.7 K	1%	1/10W	81349	RN55C2872F
R47	000392	RES	CARBON	3.9 K	5%	1/4W	81349	RC07GF392J
R49	000107	RES	CARBON	100 M	5%	1/4W	81349	RC07GF107J
R50	000104	RES	CARBON	100 K	5%	1/4W	81349	RC07GF104J
R51	020667	RES	WW	100 K	.1%	.15W	22045	J-110
R52	001737	RES	CARBON	FSV	5%	1/4W	21793	001737
R53	001737	RES	CARBON	FSV	5%	1/4W	21793	001737
R54	040225	POT	CERMET	50 OHM	20%		73138	89PR50
R55	010654	RES	METAL	20 K	.05%		18612	V53-1
R56	001737	RES	CARBON	FSV	5%	1/4W	21793	001737
R57	010654	RES	METAL	20 K	.05%		18612	V53-1
R58	040227	POT	CERMET	200 OHM	10%		73138	89PR200
R59	001737	RES	CARBON	FSV	5%	1/4W	21793	001737
R60	010615	RES	METAL	10 K	.02%		18612	V53-1M
R61	000101	RES	CARBON	100 OHM	5%	1/4W	81349	RC07GF101J
R62	000104	RES	CARBON	100 K	5%	1/4W	81349	RC07GF104J
R63	000470	RES	CARBON	47 OHM	5%	1/4W	81349	RC07GF470J
R64	000820	RES	CARBON	82 OHM	5%	1/4W	81349	RC07GF820J

403774 – Assy., PCB, RMS AC CONVERTER *continued*

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
R70	000513	RES CARBON 51 K 5% 1/4W	81349	RC07GF513J
R71	010621	RES METAL 49.9 K 1% 10 W	81349	RN55C4992F
R72	010618	RES METAL 200 K .25% 1/10W	81349	RN55C2003C
R73	010536	RES METAL 100 K 1% 1/10W	81349	RN55C1003F
R74	403865	TRANS MATCHED LOG TRANS KIT	21793	403865
R75	403865	TRANS MATCHED LOG TRANS KIT	21793	403865
R76	010496	RES METAL 1 M 1% 1/8W	81349	RN60D1004F
R77	000221	RES CARBON 220 OHM 5% 1/4W	81349	RC07GF221J
R78	000221	RES CARBON 220 OHM 5% 1/4W	81349	RC07GF221J
R79	040232	POT CERMET 10 K 10%	73138	89PR10K
R80	001737	RES CARBON FSV 5% 1/4W	21793	001737
R81	000682	RES CARBON 6.8 K 5% 1/4W	81349	RC07GF682J
R82	001737	RES CARBON FSV 5% 1/4W	21793	001737
S1	600742	SWITCH SLIDE GS-111	79727	GS-111
S2	600742	SWITCH SLIDE GS-111	79727	GS-111
TP1	600591	POST TEST POINT 85931-6	00779	85931-6
TP2	600591	POST TEST POINT 85931-6	00779	85931-6
TP3	600591	POST TEST POINT 85931-6	00779	85931-6
TP4	600591	POST TEST POINT 85931-6	00779	85931-6
TP5	600591	POST TEST POINT 85931-6	00779	85931-6

403674 – Assy., PCB, SCALING AMPLIFIER (Figure 6.21)

REF DES	DANA P/N	DESCRIPTION					FSC	MANU P/N
B1	920563	BEAD	SHIELDING		56-59065-4B		02114	56-59065-4B
B2	920563	BEAD	SHIELDING		56-59065-4B		02114	56-59065-4B
C1	130127	CAP	TRIMMER	10-40 PFD			52763	10S-TRIKO-24N750
C2	130116	CAP	MICA	3900 PFD	500 V	2%	72136	DM19F392G0
C3	120275	CAP	POLY	1000 PFD	500/630 V	2.5%	08257	KSC Series
C4	130115	CAP	MICA	346 PFD	500 V	2%	72136	DM15F3460G0
C5	120274	CAP	POLY	87 PFD	500/630 V	2.5%	08257	KSO Series
C6	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C7	100100	CAP	CERAM	FSV	1000 V	5%	21793	100100
C8	130124	CAP	TRIMMER	1.7-10 PFD	250 V		52763	R-TRIKO-122-09SD
C9	100100	CAP	CERAM	FSV	100100		21793	100100
C10	130123	CAP	TRIMMER	1-3 PFD	250 V		52763	R-TRIKO-122-09SD
C11	100050	CAP	CERAM	2.2 PFD	1000 V	5%	56289	C030B102S2R2D
C12	100061	CAP	CERAM	39 PFD	1000 V	5%	56289	C030B102G390J
C13	100100	CAP	CERAM	FSV	1000 V		21793	100100
C14	120280	CAP	MYLAR	.22 MFD	1000 V	10%	27556	ZA2J224K
C15	100077	CAP	GLASS	7.5 PFD	500 V	5%	95275	VY10CA7R5JA
C16	100075	CAP	CERAM	10 PFD	1000 V	5%	56289	C030B102E100J
C17	100075	CAP	CERAM	10 PFD	1000 V	5%	56289	C030B102E100J
C18	100077	CAP	GLASS	7.5 PFD	500 V	5%	95275	VY10CA7R5JA
C19	130125	CAP	PORCE	.8-10 PFD	250 V		91293	JMC2951
C20	130125	CAP	PORCE	.8-10 PFD	250 V		91293	JMC2951
C21	110141	CAP	TANTA	22 MFD	15 V	20%	05397	T368B226M015AS
C22	110140	CAP	TANTA	47 MFD	6 V	20%	05397	T368B476M006AS
C23	110140	CAP	TANTA	47 MFD	6 V	20%	05397	T368B476M006AS
C24	110140	CAP	TANTA	47 MFD	6 V	20%	05397	T368B476M006AS
C25	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C26	101174	CAP	CERAM	.001 MFD	500 V	10%	04222	SCD-DI-2X5F-1000
C28	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C29	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C31	110140	CAP	TANTA	47 MFD	6 V	20%	05397	T368B476M006AS
C32	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C33	100081	CAP	CERAM	4.7 PFD	1000 V	5%	56289	C030B102E4R7D
C34	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C35	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C36	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C37	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS
C38	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C39	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C40	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C41	110126	CAP	TANTA	6.8 MFD	35 V	20%	05397	T368B685M035AS

403674 – Assy., PCB, SCALING AMPLIFIER *continued*

REF DES	DANA P/N	DESCRIPTION						FSC	MANU P/N
C42	100100	CAP	CERAM	FSV				21793	100100
C45	100017	CAP	CERAM	.01 MFD	100 V	20%		56289	C023B101F103M
C46	100081	CAP	CERAM	4.7 PFD	1000 V	5%		56289	C030B102E4R7D
CR1	211083	DIODE	SILICO		018			21793	211083
CR2	211083	DIODE	SILICO		018			21793	211083
CR3	211083	DIODE	SILICO		018			21793	211083
CR4	200197	TRANS	SILICO	NPN	MPS-H10			04713	MPS-H10
CR6	200197	TRANS	SILICO	NPN	MPS-H10			04713	MPS-H10
CR9	211083	DIODE	SILICO		018			21793	211083
CR10	211083	DIODE	SILICO		018			21793	211083
CR11	220004	DIODE	SILICO	ZENER	1N961B			81349	1N961B
CR12	220004	DIODE	SILICO	ZENER	1N961B			81349	1N961B
CR13	220004	DIODE	SILICO	ZENER	1N961B			81349	1N961B
CR15	220020	DIODE	SILICO	ZENER	1N4370			81349	1N4370
CR16	210017	DIODE	MATCHED PAIR					21793	210017
CR17	210017	DIODE	MATCHED PAIR					21793	210017
CR18	210017	DIODE	MATCHED PAIR					21793	210017
CR19	210017	DIODE	MATCHED PAIR					21793	210017
K1	310078	RELAY	REED		28 V			15636	R2690-3
K2	310077	RELAY	REED		28 V			15636	R2691-3
K3	310078	RELAY	REED		28 V			15636	R2690-3
L1	310068	CHOKE	RF	1 µh	10%			99800	1537-12
Q1	200197	TRANS	SILICO	NPN	MPS-H10			04713	MPS-H10
Q2	200161	TRANS	SELECTED	FET	E415			17856	E415
Q3	200197	TRANS	SILICO	NPN	MPS-H10			04713	MPS-H10
Q4	200197	TRANS	SILICO	NPN	MPS-H10			04713	MPS-H10
Q5	200197	TRANS	SILICO	NPN	MPS-H10			04713	MPS-H10
Q6	200197	TRANS	SILICO	NPN	MPS-H10			04713	MPS-H10
Q7	200178	TRANS		PNP	2N5910			81349	2N5910
Q8	200197	TRANS	SILICO	NPN	MPS-H10			04713	MPS-H10
Q9	200178	TRANS		PNP	2N5910			81349	2N5910
Q11	200197	TRANS	SILICO	NPN	MPS-H10			04713	MPS-H10
R1	010721	RES	METAL		MATCHED SET			21793	010721
R2	010720	RES	METAL	9.09 K		.25%	1/10W	81349	RN55C9091C
R3	040228	POT	CERMET	500 OHM		10%		73138	89PR500
R4	000152	RES	CARBON	1.5 K		5%	1/4W	81349	RC07GF152J
R5	010721	RES	METAL		MATCHED SET			21793	010721

403674 – Assy., PCB, SCALING AMPLIFIER *continued*

REF DES	DANA P/N	DESCRIPTION					FSC	MANU P/N
R6	010798	RES	METAL	82.5 OHM	1%	1/10W	81349	RN55C82R5F
R7	040225	POT	CERMET	50 OHM	20%		73138	89PR50
R8	040228	POT	CERMET	500 OHM	10%		73138	89PR500
R9	010721	RES	METAL		MATCHED SET		21793	010721
R10	010721	RES	METAL		MATCHED SET		21793	010721
R11	041179	POT	WW	5 K	50-1-1-502		02111	50-1-1-502
R12	010721	RES	METAL		MATCHED SET		21793	010721
R13	000163	RES	CARBON	16 K	5%	1/4W	81349	RC07GF163J
R14	010536	RES	METAL	100 K	1%	1/10W	81349	RN55C1003F
R15	000510	RES	CARBON	51 OHM	5%	1/4W	81349	RC07GF510J
R16	000241	RES	CARBON	240 OHM	5%	1/4W	81349	RC07GF241J
R17	000202	RES	CARBON	2 K	5%	1/4W	81349	RC07GF202J
R18	010583	RES	METAL	182 OHM	1%	1/10W	81349	RN55C1820F
R19	040258	POT	CERMET	100 OHM	20%	.5 W	73138	72XW100
R20	010827	RES	METAL	8.25 K	1%	1/10W	81349	RN55C8251F
R21	010813	RES	METAL	7.87 K	1%	1/10W	81349	RN55C7871F
R22	010583	RES	METAL	182 OHM	1%	1/10W	81349	RN55C1820F
R23	000510	RES	CARBON	51 OHM	5%	1/4W	81349	RC07GF510J
R24	010536	RES	METAL	100 K	1%	1/10W	81349	RN55C1003F
R25	000101	RES	CARBON	100 OHM	5%	1/4W	81349	RC07GF101J
R26	000183	RES	CARBON	18 K	5%	1/4W	81349	RC07GF183J
R27	000183	RES	CARBON	18 K	5%	1/4W	81349	RC07GF183J
R28	000102	RES	CARBON	1 K	5%	1/4W	81349	RC07GF102J
R29	000223	RES	CARBON	22 K	5%	1/4W	81349	RC07GF223J
R30	000621	RES	CARBON	620 OHM	5%	1/4W	81349	RC07GF621J
R31	000682	RES	CARBON	6.8 K	5%	1/4W	81349	RC07GF682J
R32	000680	RES	CARBON	68 OHM	5%	1/4W	81349	RC07GF680J
R33	000680	RES	CARBON	68 OHM	5%	1/4W	81349	RC07GF680J
R34	000160	RES	CARBON	16 OHM	5%	1/4W	81349	RC07GF160J
R35	000241	RES	CARBON	240 OHM	5%	1/4W	81349	RC07GF241J
R36	001737	RES	CARBON	FSV	5%	1/4W	21793	001737
R39	000301	RES	CARBON	300 OHM	5%	1/4W	81349	RC07GF301J
R40	000103	RES	CARBON	10 K	5%	1/4W	81349	RC07GF103J
R41	000101	RES	CARBON	100 OHM	5%	1/4W	81349	RC07GF101J

403627 – Assy., PCB, OHMS CONVERTER (Figure 6.23)

REF DES	DANA P/N	DESCRIPTION					FSC	MANU P/N
C1	120034	CAP	POLY	100 PFD	630 V	5%	08257	KSO Series
C2	120004	CAP	POLY	.001 MFD	500 V		08257	KSO Series
C3	120004	CAP	POLY	.001 MFD	500 V		08257	KSO Series
C4	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C5	100012	CAP	CERAM	33 PFD	500 V	10%	71471	TCD-DI-1(N750)
C6	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
C7	120227	CAP	POLY	220 PFD	630 V	5%	08257	KSO Series
C8	120227	CAP	POLY	220 PFD	630 V	5%	08257	KSO Series
C9	100012	CAP	CERAM	33 PFD	500 V	10%	71471	TCD-DI-1(N750)
C10	100071	CAP	CERAM	.001 MFD	1000 V	20%	56289	C023B102E102M
C11	100017	CAP	CERAM	.01 MFD	100 V	20%	56289	C023B101F103M
CR1	220011	DIODE	SILICO	ZENER	1N968B		81349	1N968B
CR2	220015	DIODE	SILICO	ZENER	1N967B		81349	1N967B
CR3	211083	DIODE	SILICO		018		21793	211083
CR4	211083	DIODE	SILICO		018		21793	211083
CR5	211083	DIODE	SILICO		018		21793	211083
CR6	211083	DIODE	SILICO		018		21793	211083
CR7	211083	DIODE	SILICO		018		21793	211083
CR8	211083	DIODE	SILICO		018		21793	211083
CR9	210048	DIODE			600 V		81349	1N4005
CR10	211083	DIODE	SILICO		018		21793	211083
CR11	211083	DIODE	SILICO		018		21793	211083
CR12	211083	DIODE	SILICO		018		21793	211083
CR13	211083	DIODE	SILICO		018		21793	211083
CR14	211083	DIODE	SILICO		018		21793	211083
CR15	211083	DIODE	SILICO		018		21793	211083
CR16	211083	DIODE	SILICO		018		21793	211083
CR17	211083	DIODE	SILICO		018		21793	211083
CR18	211083	DIODE	SILICO		018		21793	211083
CR19	211083	DIODE	SILICO		018		21793	211083
CR20	211083	DIODE	SILICO		018		21793	211083
CR21	211083	DIODE	SILICO		018		21793	211083
K1	310077	RELAY	REED		28 V		15636	R2691-3
K2	310077	RELAY	REED		28 V		15636	R2691-3
K3	310113	RELAY	REED		26 V		15636	R4092-3
K4	310077	RELAY	REED		28 V		15636	R2691-3
K5	310077	RELAY	REED		28 V		15636	R2691-3
K6	310077	RELAY	REED		28 V		15636	R2691-3

403627 – Assy., PCB, OHMS CONVERTER *continued*

REF DES	DANA P/N	DESCRIPTION						FSC	MANU P/N
Q1	200101	TRANS	FET		2N5245			81349	2N5245
Q2	200200	TRANS		NPN	200200			21793	200200
Q3	200191	TRANS		PNP	2N1305			81349	2N1305
Q4	200200	TRANS		NPN	200200			21793	200200
Q5	200200	TRANS		NPN	200200			21793	200200
Q6	200201	TRANS	DUAL	NPN	200201			21793	200201
Q7	200201	TRANS	DUAL	NPN	200201			21793	200201
Q8	200201	TRANS	DUAL	NPN	200201			21793	200201
Q9	200088	TRANS	SILICO	PNP	2N4248			80131	2N4248
Q10	200200	TRANS		NPN	200200			21793	200200
Q11	200088	TRANS	SILICO	PNP	2N4248			80131	2N4248
Q12	200068	TRANS		PNP	2N4250			80131	2N4250
Q13	200198	TRANS		PNP	MPS-A92			04713	MPS-A92
Q14	200198	TRANS		PNP	MPS-A92			04713	MPS-A92
Q15	200068	TRANS		PNP	2N4250			80131	2N4250
Q16	200198	TRANS		PNP	MPS-A92			04713	MPS-A92
Q17	200198	TRANS		PNP	MPS-A92			04713	MPS-A92
Q18	200200	TRANS		NPN	200200			21793	200200
Q19	200200	TRANS		NPN	200200			21793	200200
Q20	200088	TRANS	SILICO	PNP	2N4248			80131	2N4248
Q21	200088	TRANS	SILICO	PNP	2N4248			80131	2N4248
Q22	200088	TRANS	SILICO	PNP	2N4248			80131	2N4248
Q23	200088	TRANS	SILICO	PNP	2N4248			80131	2N4248
Q24	200200	TRANS		NPN	200200			21793	200200
Q25	200200	TRANS		NPN	200200			21793	200200
R1	000104	RES	CARBON	100 K		5%	1/4W	81349	RC07GF104J
R2	000103	RES	CARBON	10 K		5%	1/4W	81349	RC07GF103J
R3	001737	RES	CARBON	FSV		5%	1/4W	21793	001737
R4	030005	RES	CARBON	100 K		5%	2 W	81349	RC42GF104J
R5	040235	POT	CERMET	100 K		10%		73138	89PR100K
R6	010598	RES	METAL	20 K		1%	1/8W	81349	RN60C2002F
R7	010137	RES	METAL	100 OHM	T-0	1%	1/8W	81349	RN60D1000F
R8	010780	RES	METAL	499 K		1%	1/8W	81349	RN60E4993F
R9	040235	POT	CERMET	100 K		10%		73138	89PR100K
R10	000107	RES	CARBON	100 M		5%	1/4W	81349	RC07GF107J
R11	000755	RES	CARBON	7.5 M		5%	1/4W	81349	RC07GF755J
R12	012033	RES		MATCHED SET				21793	012033
R13	012033	RES		MATCHED SET				21793	012033
R14	010598	RES	METAL	20 K		1%	1/8W	81349	RN60C2002F
R15	010337	RES	CARBON	10 M		1%	1/2W	81349	RN20X1005F
R16	010777	RES	METAL	39.99 K		.1%	1/10W	81349	RN55E

403627 – Assy., PCB, OHMS CONVERTER *continued*

REF DES	DANA P/N	DESCRIPTION					FSC	MANU P/N
R17	010337	RES	CARBON	10 M	1%	1/2W	81349	RN20X1005F
R18	000755	RES	CARBON	7.5 M	5%	1/4W	81349	RC07GF755J
R19	010780	RES	METAL	499 K	1%	1/8W	81349	RN60E4993F
R20	010120	RES	METAL	68.1 K T-0	1%	1/8W	81349	RN60D6812F
R21	010431	RES	METAL	511 K	1%	1/8W	81349	RN60C5113F
R22	040235	POT	CERMET	100 K	10%		73138	89PR100K
R23	010598	RES	METAL	20 K	1%	1/8W	81349	RN60C2002F
R24	010780	RES	METAL	499 K	1%	1/8W	81349	RN60E4993F
R25	010780	RES	METAL	499 K	1%	1/8W	81349	RN60E4993F
R26	010784	RES	CARBON	4.7 M	1%		91637	DC1-4
R27	010780	RES	METAL	499 K	1%	1/8W	81349	RN60E4993F
R28	000393	RES	CARBON	39 K	5%	1/4W	81349	RC07GF393J
R29	000112	RES	CARBON	1.1 K	5%	1/4W	81349	RC07GF112J
R30	001816	RES	CARBON	47 K	5%	2 W	01121	See Descrpt.
R31	001816	RES	CARBON	47 K	5%	2 W	01121	See Descrpt.
R32	000202	RES	CARBON	2 K	5%	1/4W	81349	RC07GF202J
R33	000205	RES	CARBON	2 M	5%	1/4W	81349	RC07GF205J
R34	000205	RES	CARBON	2 M	5%	1/4W	81349	RC07GF205J
R35	020699	RES	WW	4000.4 OHM	.01%		22045	J-110
R36	040235	POT	CERMET	100 K	10%		73138	89PR100K
R37	010536	RES	METAL	100 K	1%	1/10W	81349	RN55C1003F
R38	000205	RES	CARBON	2 M	5%	1/4W	81349	RC07GF205J
R39	000205	RES	CARBON	2 M	5%	1/4W	81349	RC07GF205J
R40	020698	RES	WW	445.71 OHM	.05%		22045	J-110
R41	000203	RES	CARBON	20 K	5%	1/4W	81349	RC07GF203J
R42	000203	RES	CARBON	20 K	5%	1/4W	81349	RC07GF203J
R43	040226	POT	CERMET	100 OHM	10%		73138	89PR100
R44	020669	RES	WW	100.050 K	.02%		22045	J-160
R45	040230	POT	CERMET	2 K	10%		73138	89PR2K
R46	020612	RES	WW	1.009 M	.05%	1/4W	22045	J-120
R47	040234	POT	CERMET	50 K	10%		73138	89PR50K
R48	010655	RES	METAL	11.086 M	.1%	1/2W	81349	RN70E
R49	020710	RES	METAL	20 K	.05%		22045	PB32
R50	020711	RES	METAL	5 K	.01%		22045	PB32
R51	040235	POT	CERMET	100 K	10%		73138	89PR100K
R52	010656	RES	METAL	19.948 M	.1%	1/2W	81349	RN70E
R53	000203	RES	CARBON	20 K	5%	1/4W	81349	RC07GF203J
R54	000203	RES	CARBON	20 K	5%	1/4W	81349	RC07GF203J
R55	000203	RES	CARBON	20 K	5%	1/4W	81349	RC07GF203J
R56	040234	POT	CERMET	50 K	10%		73138	89PR50K
R57	001759	RES	CARBON	5.1 OHM	5%	1/4W	81349	RC07GF5R1J
R58	000470	RES	CARBON	47 OHM	5%	1/4W	81349	RC07GF470J

403627 – Assy., PCB, OHMS CONVERTER *continued*

REF DES	DANA P/N	DESCRIPTION		FSC	MANU P/N
AR1	230054	INTEGRATED CIRCUIT	LM301A	27014	LM301A
AR2	230054	INTEGRATED CIRCUIT	LM301A	27014	LM301A

403673 – Assy., PCB, 4-WIRE RATIO (Figure 6.25)

REF DES	DANA P/N	DESCRIPTION						FSC	MANU P/N
C1	100012	CAP	CERAM	33 PFD	500 V	10%		71471	TCD-DI-1(N750)
C2	100017	CAP	CERAM	.01 MFD	100 V	20%		56289	C023B101F103M
C3	100012	CAP	CERAM	33 PFD	500 V	10%		71471	TCD-DI-1(N750)
C4	110158	CAP	TANTA	10 MFD	50 V	10%		05397	T362C106K050A
C5	100017	CAP	CERAM	.01 MFD	100 V	20%		56289	C023B101F103M
C6	120308	CAP	POLY	.047 MFD	250 V	10%		73445	C280MAE/A47K
C7	120312	CAP	POLY	.022 MFD	250 V	10%		73445	C280MAE/A22K
Q1	200200	TRANS		NPN	200200			21793	200200
Q2	200200	TRANS		NPN	200200			21793	200200
R1	010033	RES	METAL	49.9 K	T-0	1%	1/8W	81349	RN60D4992F
R2	010033	RES	METAL	49.9 K	T-0	1%	1/8W	81349	RN60D4992F
R3	010621	RES	METAL	49.9 K		1%	1/10W	81349	RN55C4992F
R4	010621	RES	METAL	49.9 K		1%	1/10W	81349	RN55C4992F
R5	010787	RES	METAL	20 K		.02%		18612	V53-1
R6	010787	RES	METAL	20 K		.02%		18612	V53-1
R7	000100	RES	CARBON	10 OHM		5%	1/4W	81349	RC07GF100J
R8	010787	RES	METAL	20 K		.02%		18612	V53-1
R9	040229	POT	CERMET	1 K		10%		73138	89PR1K
R10	040229	POT	CERMET	1 K		10%		73138	89PR1K
R11	000100	RES	CARBON	10 OHM		5%	1/4W	81349	RC07GF100J
R12	040235	POT	CERMET	100 K		10%		73138	89PR100K
R13	000100	RES	CARBON	10 OHM		5%	1/4W	81349	RC07GF100J
R14	000390	RES	CARBON	39 OHM		5%	1/4W	81349	RC07GF390J
R15	010787	RES	METAL	20 K		.02%		18612	V53-1
R16	000336	RES	CARBON	33 M		5%	1/4W	81349	RC07GF336J
AR1	230145	INTEGRATED CIRCUIT			MATCHED SET			21793	230145
AR2	230127	INTEGRATED CIRCUIT			SSS725CJ			06665	SSS725CJ
AR3	230145	INTEGRATED CIRCUIT			MATCHED SET			21793	230145

403659 - Assy., REAR PANEL

REF DESIG	DANA P/N	DESCRIPTION	FSC	MANU P/N
C201	100046	CAP CERAM .01 MFD 1000 V 20%	56289	C023B102K103M
C202	100046	CAP CERAM .01 MFD 1000 V 20%	56289	C023B102K103M
F201	920204	FUSE SLOW .50A 250V	75915	3AG1/2ASB
J203	600619	CONNECTOR RE CPT	82389	EAC-301
S101	600583	SWITCH PWR SPST	10389	26-099-100
S201	600521	SWITCH DPDT	82389	46256LFE
T201	300073	TRANSFORMER POWER 115V	21793	300073
X201	920099	FUSEHOLDER 3AG MINATURE	75915	342 004

403680 – Assy., PARALLEL FRONT-REAR INPUT

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
J210	600693	CONNECTOR, REAR INPUT 7 P	05574	TKR7-101
H1	600672	TERMINAL PIN (7*)	05574	TP-100
P210	600692	CONNECTOR, MATING REAR INPUT 7 P	05574	TKP7-100
H2	600622	SOCKET, CONTACT (7*)	05574	TS-100

403850 – Assy., SWITCHABLE FRONT-REAR INPUT

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
K2	310096	RELAY	24796	R10-E2033-1
S6	920457	SWITCH, SLIDE, DPDT	79727	G126

*Quantity Required

403956 – Assy., PARALLEL FRONT-REAR INPUT (–1B)

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
J204	600259	CONNECTOR, 6S	02660	MS3102A

403955 – Assy., SWITCHABLE FRONT-REAR INPUT (–1SB)

REF DES	DANA P/N	DESCRIPTION	FSC	MANU P/N
J204	600259	CONNECTOR, 6S	02660	MS3102A
K2	310096	RELAY	24796	R10-E2033-1
S6	920457	SWITCH, SLIDE, DPDT	79727	G126

SECTION 8

PART NUMBER – NATIONAL STOCK NUMBER CROSS REFERENCE INDEX

PART NUMBER	FSCM	NATIONAL STOCK NUMBER	PART NUMBER	FSCM	NATIONAL STOCK NUMBER	SMR CODE
C023B101F103M	56289	5910-00-810-4849	RC07GF161J	81349	5905-00-825-5592	
C023B101H203M	56289	5910-00-497-9021	RC07GF163J	81349	5905-00-752-3186	
C023B102E102M	56289	5910-00-126-1593	RC07GF182J	81349	5905-00-688-3738	
C023B102K103M	56289	5910-00-828-5705	RC07GF183J	81349	5905-00-687-0000	
C030B102E100J	56289	5910-00-222-8207	RC07GF184J	81349	5905-00-681-8819	
C030B102G390J	56289	5910-00-110-0888	RC07GF200J	81349	5905-00-835-1631	
C030B102S2R2D	56289	5910-00-989-3595	RC07GF201J	81349	5905-00-683-2239	
DD561	71590	5910-00-849-1486	RC07GF202J	81349	5905-00-686-3370	
DM15-201J	72136	5910-00-543-9171	RC07GF203J	81349	5905-00-686-3368	
EAC-301	82389	5935-00-233-6728	RC07GF204J	81349	5905-00-681-8821	
JMC2951	91293	5910-00-089-0694	RC07GF205J	81349	5905-00-755-2754	
MJE3055	04713	5961-00-497-4280	RC07GF220J	81349	5905-00-755-8389	
MJE370	04713	5961-00-230-7993	RC07GF221J	81349	5905-00-683-2240	
MJE520	04713	5961-00-104-8394	RC07GF222J	81349	5905-00-723-5251	
RC07GF100J	81349	5905-00-817-7971	RC07GF223J	81349	5905-00-687-0002	
RC07GF101J	81349	5905-00-683-7721	RC07GF224J	81349	5905-00-682-4105	
RC07GF102J	81349	5905-00-681-6462	RC07GF241J	81349	5905-00-682-4108	
RC07GF103J	81349	5905-00-683-2238	RC07GF242J	81349	5905-00-683-7724	
RC07GF104J	81349	5905-00-110-0388	RC07GF272J	81349	5905-00-686-3798	
RC07GF105J	81349	5905-00-681-8817	RC07GF273J	81349	5905-00-686-3838	
RC07GF106J	81349	5905-00-803-3468	RC07GF301J	81349	5905-00-686-3122	
RC07GF112J	81349	5905-00-800-8063	RC07GF302J	81349	5905-00-682-4097	
RC07GF113J	81349	5905-00-686-3128	RC07GF303J	81349	5905-00-803-2908	
RC07GF121J	81349	5905-00-683-2247	RC07GF331J	81349	5905-00-686-3369	
RC07GF122J	81349	5905-00-686-9994	RC07GF332J	81349	5905-00-681-9969	
RC07GF123J	81349	5905-00-726-4413	RC07GF333J	81349	5905-00-686-3903	
RC07GF124J	81349	5905-00-686-9993	RC07GF361J	81349	5905-00-808-6136	
RC07GF132J	81349	5905-00-686-3119	RC07GF390J	81349	5905-00-820-9124	
RC07GF151J	81349	5905-00-683-2243	RC07GF391J	81349	5905-00-683-2236	
RC07GF152J	81349	5905-00-683-7723	RC07GF392J	81349	5905-00-682-4098	
RC07GF153J	81349	5905-00-681-8818	RC07GF393J	81349	5905-00-686-3358	
RC07GF154J	81349	5905-00-686-9995	RC07GF432J	81349	5905-00-682-4099	
RC07GF160J	81349	5905-00-835-1630	RC07GF470J	81349	5905-00-802-6730	

PART NUMBER – NATIONAL STOCK NUMBER CROSS REFERENCE INDEX

PART NUMBER	FSCM	NATIONAL STOCK NUMBER	REPLACEMENT PART NUMBER	REPLACEMENT FSCM	REPLACEMENT NATIONAL STOCK NUMBER	SMR CODE
RC07GF472J	81349	5905-00-686-9998	RN55C4991F	81349	5905-00-913-6325	
RC07GF473J	81349	5905-00-683-2246	RN55C4992F	81349	5905-00-982-0200	
RC07GF5R1J	81349	5905-00-730-0331	RN55C7871F	81349	5905-00-921-0346	
RC07GF510J	81349	5905-00-683-7720	RN55C82R5F	81349	5905-00-903-5706	
RC07GF511J	81349	5905-00-116-2394	RN55C8251F	81349	5905-00-982-0213	
RC07GF512J	81349	5905-00-683-2241	RN55D2672F	81349	5905-00-965-9091	
RC07GF513J	81349	5905-00-682-4103	RN55E1003F	81349	5905-00-943-6449	
RC07GF514J	81349	5905-00-681-8856	RN55E4870F	81349	5905-00-760-4896	
RC07GF562J	81349	5905-00-691-0195	RN60C2002F	81349	5905-00-142-3316	
RC07GF565J	81349	5905-00-822-4856	RN60C6042F	81349	5905-00-812-1606	
RC07GF621J	81349	5905-00-801-6998	RN60D1000F	81349	5905-00-988-2310	
RC07GF622J	81349	5905-00-682-4100	RN60D1004F	81349	5905-00-060-2470	
RC07GF680J	81349	5905-00-683-2235	RN60D1100F	81349	5905-00-774-7971	
RC07GF681J	81349	5905-00-727-8001	RN60D1926F	81349	5905-00-985-5465	
RC07GF682J	81349	5905-00-686-9997	RN60D2152F	81349	5905-00-006-6984	
RC07GF684J	81349	5905-00-681-8823	RN60D2493F	81349	5905-00-058-3344	
RC07GF750J	81349	5905-00-801-2377	RN60D3243F	81349	5905-00-059-5627	
RC07GF751J	81349	5905-00-807-4954	RN60D4420F	81349	5905-00-078-8288	
RC07GF753J	81349	5905-00-800-3478	RN60D4753F	81349	5905-00-079-3197	
RC07GF755J	81349	5905-00-835-2406	RN60D4990F	81349	5905-00-682-7773	
RC07GF820J	81349	5905-00-686-3121	RN60D4992F	81349	5905-00-052-2395	
RC07GF821J	81349	5905-00-686-9996	RN60D6812F	81349	5905-00-069-3922	
RC07GF822J	81349	5905-00-831-6134	RN60E4993F	81349	5905-00-088-1026	
RC07GF911J	81349	5905-00-801-6444	T368A105M035AS	05397	5910-00-536-0767	
RC42GF104J	81349	5905-00-254-7101	T368A224K050AS	05397	5910-00-594-1903	
RN55C1002B	81349	5905-00-833-6279	T368A474K050AS	05397	5910-00-594-1904	
RN55C1002F	81349	5905-00-982-0937	ZA1101J	27556	5910-00-008-4446	
RN55C1003F	81349	5905-00-982-0198	010027	21793	5905-00-497-1035	
RN55C1502F	81349	5905-00-982-0214	1N4005	81349	5961-00-722-1480	
RN55C1820F	81349	5905-00-982-0204	1N965B	81349	5961-01-011-7296	
RN55C2491F	81349	5905-00-984-1467	1N967B	81349	5961-00-851-8296	
RN55C2741F	81349	5905-00-964-0395	1N973B	81349	5961-00-847-5244	
RN55C2872F	81349	5905-00-975-1654	100012	21793	5910-00-435-6483	

PART NUMBER – NATIONAL STOCK NUMBER CROSS REFERENCE INDEX

			REPLACEMENT			
PART NUMBER	FSCM	NATIONAL STOCK NUMBER	PART NUMBER	FSCM	NATIONAL STOCK NUMBER	SMR CODE
100017	21793	5910-00-405-7687	89PR50K	73138	5905-00-138-1089	
101098	21793	5910-00-188-5313	89PR500	73138	5905-00-434-2579	
101099	21793	5910-00-456-4100	920116	21793	6240-00-731-5071	
101175	21793	5910-00-435-6484				
101182	21793	5910-00-435-6485				
101641	21793	5910-00-188-5327				
110067	21793	5910-00-009-3149				
110125	21793	5910-00-549-1269				
110143	21793	5910-00-536-0767				
120034	21793	5910-00-351-6207				
120090	21793	5910-00-008-4446				
121091	21793	5910-00-185-5740				
121092	21793	5910-00-435-6508				
1537-12	99800	5950-00-925-5249				
2N3568	81349	5961-00-947-8263				
2N3646	80131	5961-00-062-3133				
2N4248	80131	5961-00-487-8132				
2N4250	80131	5961-00-021-7849				
200087	21793	5961-00-008-8405				
210004	21793	5961-00-914-6005				
211083	21793	5961-00-221-3350				
252-10-30-160	71785	5935-00-414-1281				
252-18-30-160	71785	5935-00-351-5928				
273-0001-002	74970	5910-00-412-4228				
342004	75915	5920-00-849-6564				
85931-6	00779	5999-00-427-0787				
89PR1K	73138	5905-00-243-1612				
89PR100	73138	5905-00-003-2538				
89PR100K	73138	5905-00-007-9135				
89PR2K	73138	5905-00-003-2539				
89PR200K	73138	5905-00-003-2541				
89PR50	73138	5905-00-243-1716				

APPENDIX A

REFERENCES

DA Pam 310-4	Index of Technical Manuals, Technical Bulletins, Supply Manuals (Types 7, 8, and 9), Supply Bulletins, and Lubrication Orders.
DA Pam 310-7	Index of Modification Work Orders.
TB 43-180	Calibration Requirements for the Maintenance of Army Materiel.
TB 385-4	Safety Precautions for Maintenance of Electrical Electronic Equipment.
TM 38-750	The Army Maintenance Management System (TAMMS).
TM 740-90-1	Administrative Storage of Equipment.
TM 750-244-2	Procedures for Destruction of Electronics Materiel to Prevent Enemy Use (Electronics Command)

APPENDIX D

MAINTENANCE ALLOCATION

Section I. INTRODUCTION

D-1. General

This appendix provides a summary of the maintenance operations for the ME-510/U. It authorizes categories of maintenance for specific maintenance functions on repairable items and components and the tools and equipment required to perform each function. This appendix may be used as an aid in planning maintenance operations.

D-2. Maintenance Function

Maintenance functions will be limited to and defined as follows:

a. Inspect. To determine the serviceability of an item by comparing its physical, mechanical, and/or electrical characteristics with established standards through examination.

b. Test. To verify serviceability and to detect incipient failure by measuring the mechanical or electrical characteristics of an item and comparing those characteristics with prescribed standards.

c. Service. Operations required periodically to keep an item in proper operating conditions, i.e., to clean (decontaminate), to preserve, to drain, to paint, or to replenish fuel, lubricants, hydraulic fluids, or compressed air supplies.

d. Adjust To maintain, within prescribed limits, by bringing into proper or exact position, or by setting the operating characteristics to the specified parameters.

e. Align. To adjust specified variable elements of an item to bring about optimum or desired performance.

f. Calibrate. To determine and cause corrections to be made or to be adjusted on instruments or test measuring and diagnostic equipments used in precision measurement. Consists of comparisons of two instruments, one of which is a certified standard of known accuracy, to detect and adjust any discrepancy in the accuracy of the instrument being compared.

g. Install. The act of emplacing, seating, or fixing into position an item, part, module (component or assembly) in a manner to allow the proper functioning of the equipment or system.

h. Replace. The act of substituting a serviceable like type part, subassembly, or module (component or assembly) for an unserviceable counterpart.

i. Repair. The application of maintenance services (inspect, test, service, adjust, align, calibrate, replace) or other maintenance actions (welding, grinding, riveting, straightening, facing, remachining, or resurfacing) to restore serviceability to an item by correcting specific damage, fault, malfunction, or failure in a part, subassembly, module (component or assembly), end item, or system.

j. Overhaul. That maintenance effort (service/action) necessary to restore an item to a completely serviceable/operational condition as prescribed by maintenance standards (i.e., DMWR) in appropriate technical publications. Overhaul is normally the highest degree of maintenance performed by the Army. Overhaul does not normally return an item to like new condition.

k. Rebuild. Consists of those services actions necessary for the restoration of unserviceable equipment to a like new condition in accordance with original manufacturing standards. Rebuild is the highest degree of materiel maintenance applied to Army equipment. The rebuild operation includes the act of returning to zero those age measurements (hours, miles, etc.) considered in classifying Army equipments/components.

D-3. Column Entries

a. Column 1, Group Number. Column 1 lists group numbers, the purpose of which is to identify components, assemblies, subassemblies, and modules with the next higher assembly.

b. Column 2, Component/Assembly. Column 2 contains the noun names of components, assemblies, subassemblies, and modules for which maintenance is authorized.

c. Column 3, Maintenance Functions. Column 3 lists the functions to be performed on the item listed in column 2. When items are listed without maintenance functions, it is solely for purpose of having the group numbers in the MAC and RPSTL coincide.

d. Column 4, Maintenance Category. Column 4 specifies, by the listing of a "worktime" figure in the appropriate subcolumn(s), the lowest level of maintenance authorized to perform the function listed in column 3. This figure represents the active time required to perform that maintenance function at the indicated category of maintenance. If the number or complexity of the tasks within the listed maintenance function vary at different maintenance categories, appropriate "worktime" figures will be shown for each category. The number of task-hours specified by the "worktime" figure represents the average time required to restore an item (assembly, subassembly, component, module, end item or system) to a serviceable condition under typical field operating conditions. This time includes preparation time, troubleshooting time, and quality assurance/quality control time in addition to the time required to perform the specific tasks identified for the maintenance functions authorized in the maintenance allocation chart. Subcolumns of column 4 are as follows:

C – Operator/Crew
O – Organizational
F – Direct Support
H – General Support
D – Depot

e. Column 5, Tools and Equipment. Column 5 specifies by code, those common tool sets (not individual tools) and special tools, test, and support equipment required to perform the designated function.

f. Column 6, Remarks. Column 6 contains an alphabetic code which leads to the remark in section IV, Remarks, which is pertinent to the item opposite the particular code.

D-4. Tool and Test Equipment Requirement (sect III)

a. Tool or Test Equipment Reference Code. The numbers in this column coincide with the numbers used in the tools and equipment column of the MAC. The numbers indicate the applicable tool or test equipment for the maintenance functions.

b. Maintenance Category. The codes in this column indicate the maintenance category allocated the tool or test equipment.

c. Nomenclature. This column lists the noun name and nomenclature of the tools and test equipment required to perform the maintenance functions.

d. National/NATO Stock Number. This column lists the National/NATO stock number of the specified tool or test equipment.

e. Tool Number. This column lists the manufacturer's part number of the tool followed by the Federal Supply Code for manufacturers (5-digit) in parentheses.

D-5. Remarks (sect IV)

a. Reference Code. This code refers to the appropriate item in section II, column 6.

b. Remarks. This column provides the required explanatory information necessary to clarify items appearing in section II.

SECTION II MAINTENANCE ALLOCATION CHART FOR MULTIMETER ME-510/U

(1) GROUP NUMBER	(2) COMPONENT/ASSEMBLY	(3) MAINTENANCE FUNCTION	(4) MAINTENANCE CATEGORY					(5) TOOLS AND EQPT.	(6) REMARKS
			C	O	F	H	D		
00	Voltmeter, ME-510/U	Inspect		0.5				11	A
		Test					0.5	1 through 10	C
		Service				0.5		11	A and B
		Adjust					2.0	1 through 10	D
		Repair					3.0	11	D
		Overhaul					6.0	1 through 11	D
		Calibrate (S level)					2.0	1 through 10	D
	NOTE: S Level Calibration - calibration support responsibility is assigned to the Army Area Calibration Laboratory.								

SECTION III TOOL AND TEST EQUIPMENT REQUIREMENTS FOR MULTIMETER ME-510/U

TOOL OR TEST EQUIPMENT REF CODE	MAINTENANCE CATEGORY	NOMENCLATURE		NATIONAL/NATO STOCK NUMBER	TOOL NUMBER
1	D	Saturated Standard Cell Bank		4931-00-491-0252	
2	D	DC Voltage Sources (2 required)		6625-00-239-8924	
3	D	Adjustable Voltage Divider (2 required)		4931-00-130-5383	
4	D	Null Detector/uVoltmeter (2 required)		6625-00-445-3333	
5	D	Thermal Transfer Standard		6625-00-115-2582	
6	D	AC Voltage Source		4931-00-438-1619	
7	D	Resistance Standards	10 Ohms	6625-00-540-8845	
			100 Ohms	6625-00-490-3075	
			1 KOhm	6625-00-649-2781	
			10 KOhm	6625-00-097-0425	
			100 KOhm	6625-00-807-0259	
			1 MOhm		
			10 MOhm	6625-00-678-9678	
			100 MOhm	6625-00-678-9677	
8	D	1 uFD non polar capacitor			
9	D	1.5 volt battery(with binding posts) (2 required)		Common stock	
10	D	Carbon Resistors 100 Ohm, 10 Ohm, 100 KOhm, 1 MOhm 1/4 Watt 5%		Common stock	
11	O, F, H, D	Tools and material allotted to the repairer because of the assigned mission.			

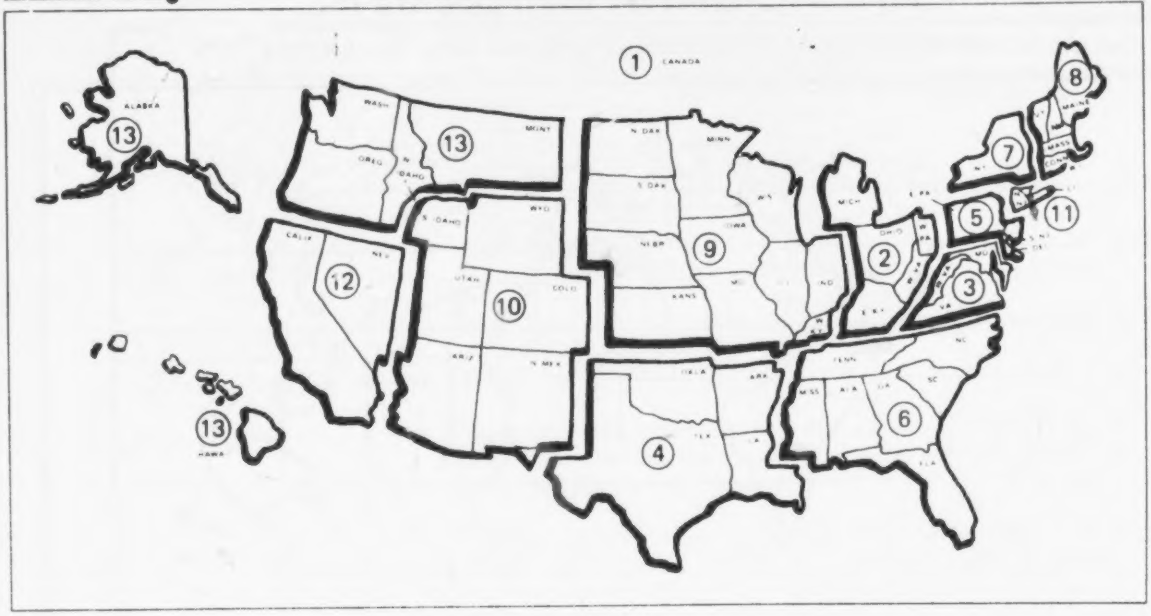
SECTION IV. REMARKS

REFERENCE CODE	REMARKS
A	Visual (External)
B	Replace fuses, knobs, etc.
C	Performance tests only.
D	All

DANA

APPENDIX E

U.S. AND CANADIAN REPRESENTATIVES



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Scarborough, Ontario, MIW 2S7.
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TWX: 610-492-0122

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TWX: 910-860-5483

8622 Crownhill, Suite 105
San Antonio, Texas 78209
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TWX: None

300 Huntland Avenue, Suite 210
Austin, Texas 78752
Telephone: (512) 451-5174
TWX: None

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TWX: 710-670-0949

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TWX: 810-850-0136

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Burlington, North Carolina 27215
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TWX: 510-921-5751

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Paramus, New Jersey 07652
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TWX: 710-990-5168

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(Answering Service)
TWX: None

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TWX: None

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Minneapolis, Minnesota 55422
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TWX: None

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TWX: None

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TWX: None

1259 West 86th Street
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TWX: 810-341-3308

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TWX: None

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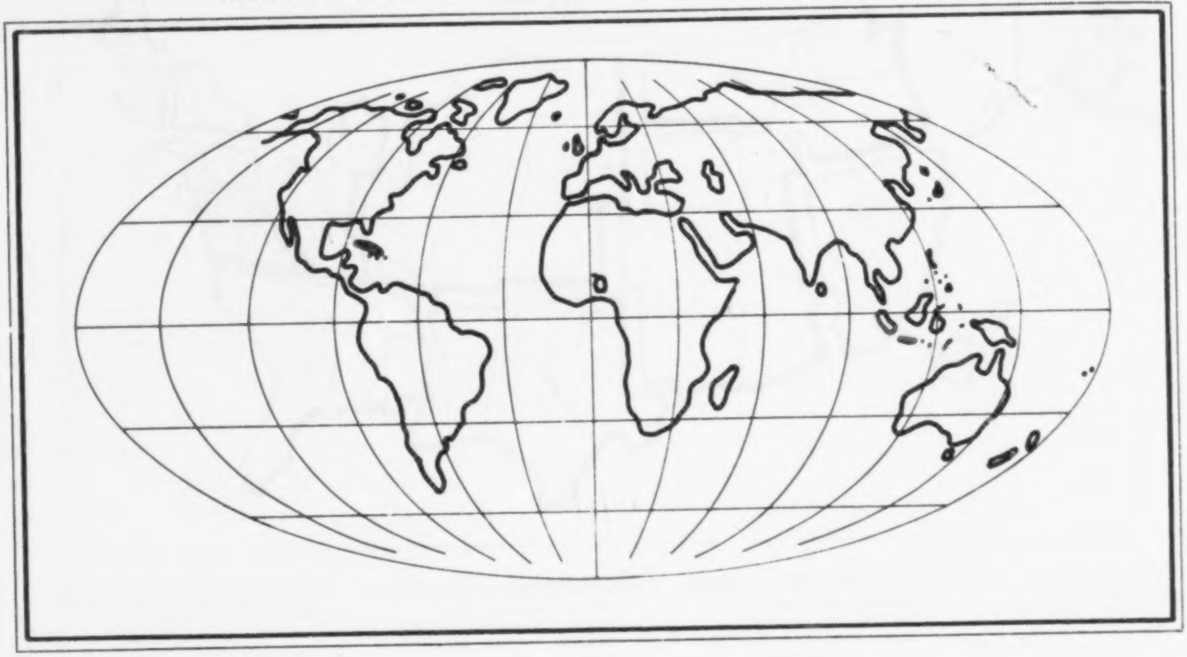

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TM 11-5840-340-14&P	23 Jan 74	Radar Set AN/PRC-76

TEAR ALONG PERFORATED LINE

BE EXACT PIN-POINT WHERE IT IS PAGE NO	PARA-GRAPH	FIGURE NO	TABLE NO	IN THIS SPACE TELL WHAT IS WRONG AND WHAT SHOULD BE DONE ABOUT IT:
2-25	2-28			Recommend that the installation antenna alignment procedure be changed throughout to specify a 2° IFF antenna lag rather than 1°. REASON: Experience has shown that with only a 1° lag, the antenna servo system is too sensitive to wind gusting in excess of 25 knots, and has a tendency to rapidly accelerate and decelerate as it hunts, causing strain to the drive train. Hunting is minimized by adjusting the lag to 2° without degradation of operation.
3-10	3-3		3-1	Item 5, Function column. Change "2 db" to "3db." REASON: The adjustment procedure the TRANS POWER FAULT indicator calls for a 3 db (500 watts) adjustment to light the TRANS POWER FAULT indicator.
5-6	5-8			Add new step f.1 to read, "Replace cover plate removed in step e.1, above." REASON: To replace the cover plate.
E-5				For item 2, change the NSN to read: 5835-00-134-9186. REASON: Accuracy.
E-8		E-3		Identify the cover on the junction box (item no. 5). REASON: It is a separate item and is not called out on figure 19.
E-9				Add the cover of the junction box as an item in the listing for figure 19. REASON: Same as above.

SAMPLE

PRINTED NAME, GRADE OR TITLE, AND TELEPHONE NUMBER
SSG I. M. DeSpiritof 999-1776

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SSG I.M. DeSpiritof

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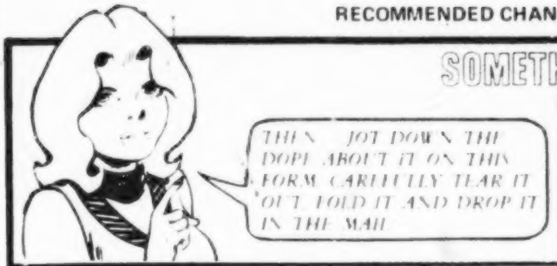
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ATTN: DRSEL-ME-MQ
Fort Monmouth, New Jersey 07703

By Order of the Secretary of the Army

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General, United States Army
Chief of Staff

Official:

J. C. PENNINGTON
Major General, United States Army
The Adjutant General

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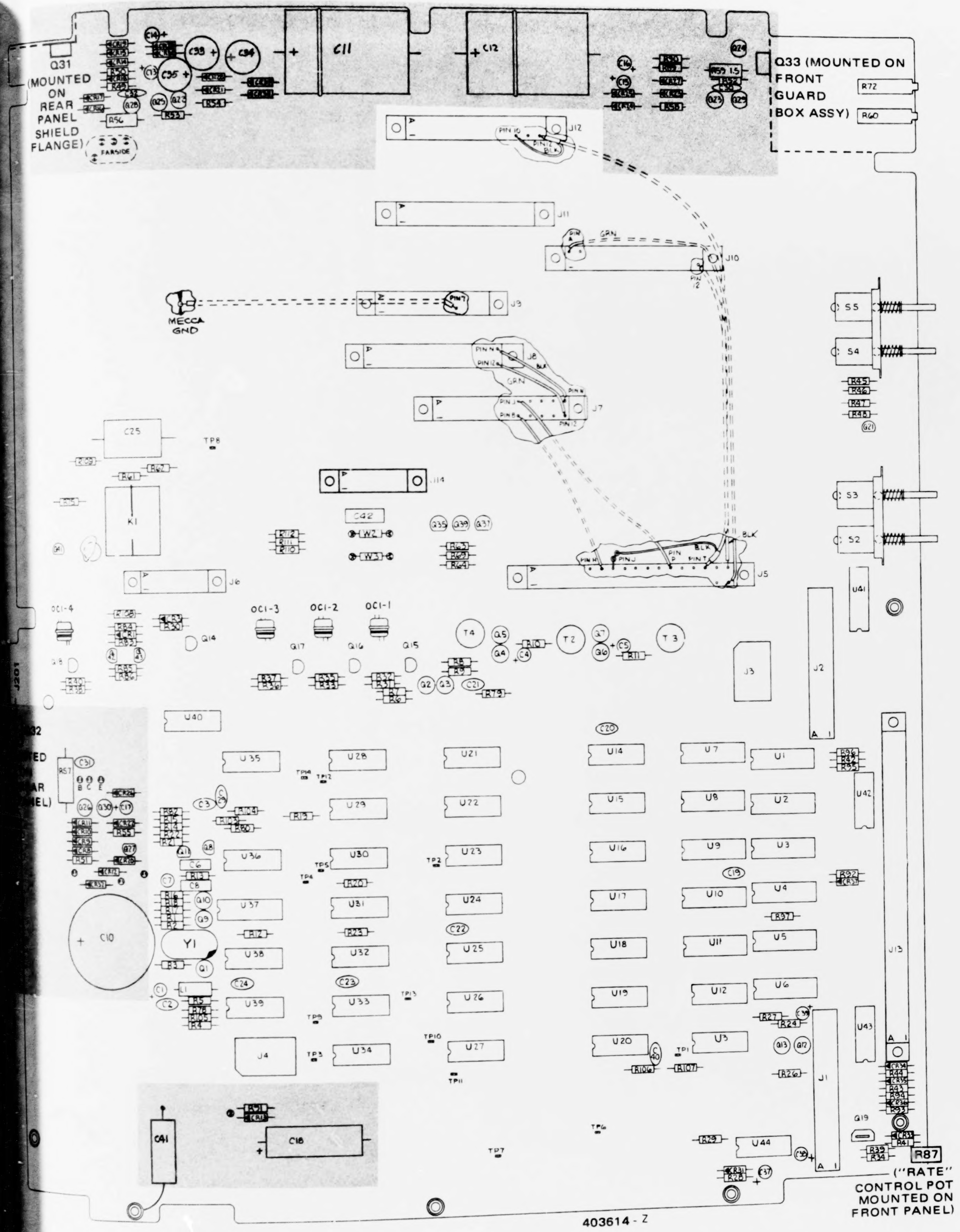
Table 6.1 - Signal Interconnection List

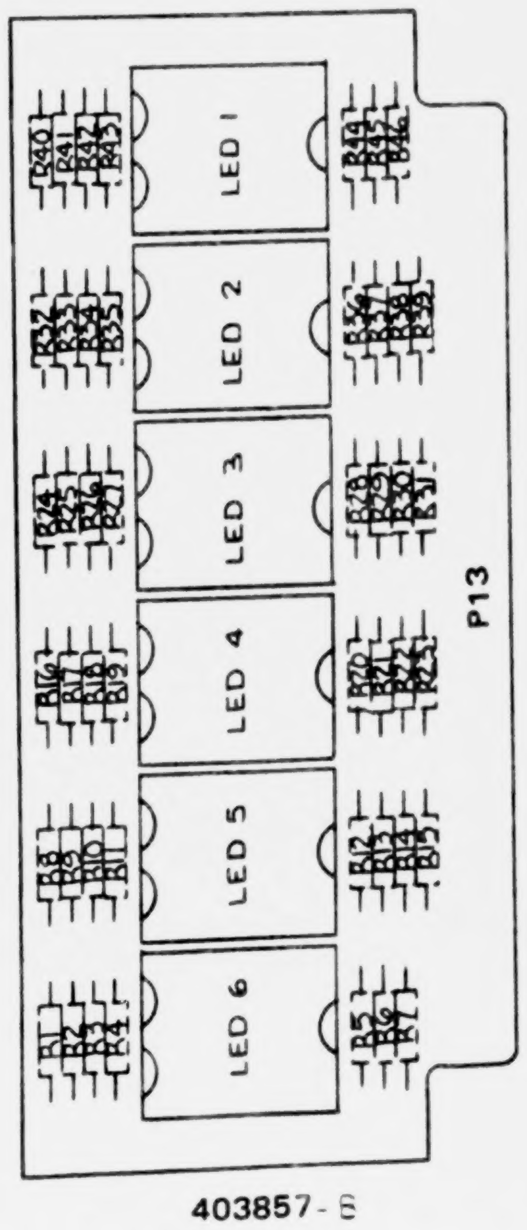
	DISPLAY (Figure 6.17)		PROGRAM (Figure 6.15)		SWITCHING (Figure 6.8)		ATTENUATOR (Figure 6.6)	ISOLATOR (Figure 6.10)	DIGITIZER (Figure 6.13)	OHMS CONVERTER (Figure 6.25)	AC CONVERTER (Figure 6.19)	RMS BYPASS *	SCALING AMPLIFIER (Figure 6.23)	RMS CONVERTER (Figure 6.21) *	READOUT (Figure 6.3)	4-WIRE RATIO (Figure 6.27)	PRINTER OUTPUT (Figure 6.3)	
	J1	J2	J3/J3A	J4/J4A	J5	J6	J7	J8	J9	J10	J11	J11	J12	J12	J13	J14	J201	OTHER
POWER & COMMONS																		
+5V Unregulated																13		Power Supply (Figure 6.4)
+5V	8			14		E											B3	Power Supply (Figure 6.4)
+20V							A	3	C	J	2		D	D		F		Power Supply (Figure 6.4)
−20V							1	A	1	H	1		5	5		6		Power Supply (Figure 6.4)
+26V								C										Power Supply (Figure 6.4)
−26V					A			1		C								Power Supply (Figure 6.4)
−40V									4									Power Supply (Figure 6.4)
+150V	A																	Power Supply (Figure 6.4)
Conv. Common											9		3					
Digital Common	10			1		B								C			B1	
Mecca					6		D	E	A	2	3		A			4		Mecca (Common for analog circuits, +20V, −20V, −26, and −40V supplies)
					E		F	F	2	7	F							
							3		3	11	J		C					
									6	B								Digital Common (Common for +5V and +150V supplies)
									7	N								
NUMERICAL DATA																		
1_1																	A21	Figure 6.3, zone C-15
1_2																	A18	Figure 6.3, zone C-15
1_4																	A20	Figure 6.3, zone D-15
1_8																	A19	Figure 6.3, zone D-15
10_1																	B21	Figure 6.3, zone C-14
10_2																	A22	Figure 6.3, zone C-14
10_4																	B20	Figure 6.3, zone D-14
10_8																	B22	Figure 6.3, zone D-14
100_1																	B17	Figure 6.3, zone C-13
100_2																	B19	Figure 6.3, zone C-13
100_4																	B16	Figure 6.3, zone D-13
100_8																	B18	Figure 6.3, zone D-13
1 K_1																	B13	Figure 6.3, zone C-12
1 K_2																	B15	Figure 6.3, zone C-12
1 K_4																	B12	Figure 6.3, zone D-12
1 K_8																	B14	Figure 6.3, zone D-12
10 K_1																	B6	Figure 6.3, zone C-11
10 K_2																	B10	Figure 6.3, zone C-11
10 K_4																	B7	Figure 6.3, zone C-12
10 K_8																	B9	Figure 6.3, zone C-12
100K																	B8	Figure 6.3, zone B-2
CONTROL SIGNALS																		
.1 + 1V							4											Figure 6.3, zone A-6
$\overline{\text{.1 + 1V Range}_1}$		F																Figure 6.3, zone A-5
$\overline{\text{.1 + 1V Range}_2}$		4																Figure 6.3, zone A-5
.1 + 1V (+ Input)					H		8											
.1 + 1V (− Input)					7		E											(hard-wired)
1 + 100V Rng Rly		E																Figure 6.3, zone B-5
1 + 100V Range							2											Figure 6.3, zone B-6
10 + 100 + 1 KV (+ In)					P		J											(hard-wired)
+10V Ref									B	3								Figure 6.3, zone B-4
−10V Ref									J									
100 + 1 KV Rng Rly		C																Figure 6.3, zone A-6
100 + 1000V Rng							5											Figure 6.3, zone A-7
4-Wire Ratio Out																3		Figure 6.3, zone B-4
+5V B/S (Bootstrap)								5										Figure 6.3, zone A-2
−5V B/S (Bootstrap)								6										Figure 6.3, zone A-2
6 MHz	7																	Figure 6.3, zone D-2
$\overline{\text{A1 Range}}$		M	12			3												Figure 6.3, zone C-8
AC																	A1	Figure 6.3, zone B-2
$\overline{\text{AC}}$		H	5			A												Figure 6.3, zone B-1, B-6
AC Common					J								10	10/A				+ Input Shield
AC Conv. Out					8						12		B	2				
AC In (Input)					T								12	12				Shield to J5-J
AC Zero											C			3				Figure 6.3, zone B-2
$\overline{\text{Auto}}$		J	8															
Axis (T1-T4)									9									Figure 6.3, zone C-1
$\overline{\text{B2 Range}}$		N				4												Figure 6.3, zone C-8
Bootstrap Com							L	10										
Bridge Tap									F									Figure 6.3, zone B-4
$\overline{\text{C4 Range}}$		L				5												Figure 6.3, zone C-8
Clear	9		2															Figure 6.3, zone D-4
$\overline{\text{Command}}$				7														Figure 6.3, zone E-2
+ Current (+I)					S													Front Panel Input Term; Shield to J5-E
− Current (−I)					11													Front Panel Input Term.

	DISPLAY (Figure 6.17)		PROGRAM (Figure 6.15)		SWITCHING (Figure 6.8)		ATTENUATOR (Figure 6.6)	ISOLATOR (Figure 6.10)	DIGITIZER (Figure 6.13)	OHMS CONVERTER (Figure 6.25)	AC CONVERTER (Figure 6.19)	RMS BYPASS *	SCALING AMPLIFIER (Figure 6.23)	RMS CONVERTER (Figure 6.21) *	READOUT (Figure 6.3)	4-WIRE RATIO (Figure 6.27)	PRINTER OUTPUT (Figure 6.3)	
	J1	J2	J3/J3A	J4/J4A	J5	J6	J7	J8	J9	J10	J11	J11	J12	J12	J13	J14	J201	OTHER
$\overline{\text{D8 Range}}$		K				10												Figure 6.3, zone D-8
DC																	A3	Figure 6.3, zone B-2
$\overline{\text{DC}}$	4			6														Figure 6.3, zone B-1
DC Wiper							A											Figure 6.3, zone A-2
$\overline{\text{Data Disable}}$				9													A7	Figure 6.3, zone A-2, C-5
$\overline{\text{Data Ready Out}}$																	A11	Figure 6.3, zone C-6
Downrange (15%)	11																	Figure 6.3, zone E-8
$\overline{\text{Downrange}}$	D			5														
Ext Ref Hi																A		Figure 6.3, zone B-3
Ext Ref Lo																1		Figure 6.3, zone B-3
Ext Supply Volt																	A13	Figure 6.3, zone A-2
Filter																	B5	Figure 6.3, zone A-2
$\overline{\text{Filter}}$		6	6											E				Figure 6.3, zone A-1, B-6
Filter Select							K	D										Figure 6.3, zone B-7
$\overline{\text{Filter Switch}}$		2																Figure 6.3, zone A-1
Hold																	A4	Figure 6.3, zone B-2
$\overline{\text{Hold}}$		3		12														Figure 6.3, zone B-1
+ In									B									Figure 6.3, zone B-4
− In									D									Figure 6.3, zone B-4
+ Input					15													Front Panel Input Term; Shield to J5-J
− Input					L													Front Panel Input Term.
Iso +In							12	12										
Iso −In							N	N										
Iso Out							C	[illegible]	[illegible]									Figure 6.3, zone B-3
No																	A12	Figure 6.3, zone B-2
$\overline{\text{No}}$	1																	Figure 6.3, zone B-1
Ohms (Ω)																	B2	Figure 6.3, zone A-2
$\overline{\text{Ohms}}$ ($\overline{\Omega}$)		8	3			1												Figure 6.3, zone A-1
Ohms Amp In					V					12								
Ohms Amp Out					M					[illegible]								
+ Ohms I (+ Ω I)					18					A								
$\overline{\text{PGMB}}$				2														Figure 6.3, zone B-5
PGMD	6																	Figure 6.3, zone C-4
PGM (Switch)		3																Figure 6.3, zone A-3
− Polarity	2																A10	Figure 6.3, zone C-3
+ Polarity	3																A9	Figure 6.3, zone C-3
$\overline{\text{Prog 1}}$ ($\overline{\text{P1}}$)		9	9															
$\overline{\text{Prog 2}}$ ($\overline{\text{P2}}$)		10	11															
$\overline{\text{Prog 4}}$ ($\overline{\text{P4}}$)		11	13															
$\overline{\text{Prog 8}}$ ($\overline{\text{P8}}$)		12	14															
Prog (Switch)			1															Figure 6.3, zone A-3
Prog A thru E (A-E)				8														Figure 6.3, zone E-4
Range A_1																	A15	Figure 6.3, zone C-8
Range B_2																	A17	Figure 6.3, zone C-8
Range C_4																	A16	Figure 6.3, zone D-8
Range D_8																	A14	Figure 6.3, zone D-8
Ratio																	A2	Figure 6.3, zone A-2
$\overline{\text{Ratio}}$		5	7															Figure 6.3, zone A-1
$\overline{\text{Ratio SW}}$		D																Figure 6.3, zone A-1
Ratio Rly Dr		A																Figure 6.3, zone B-4
Reset (T2-T3)									K									Figure 6.3, zone C7
$\overline{\text{Reset}}$				11														Figure 6.3, zone C-6
$\overline{\text{r.01}}$					5					B								
$\overline{\text{r.1}}$					2					D								
$\overline{\text{r1}}$					4					E			8					
$\overline{\text{r10}}$					3					[illegible]			[illegible]	F				
$\overline{\text{r100}}$					D					[illegible]			[illegible]	H				
$\overline{\text{r1K}}$					B					[illegible]			[illegible]	7				
$\overline{\text{r10K}}$					C					[illegible]								
Sig Int (T3-T2)									L									Figure 6.3, zone C-7
$\overline{\text{Sig Integrate}}$																	B11	Figure 6.3, zone B-2
Scaling Amp (S/A) Out											E		2					
$\overline{\text{Superfast}}$				10														Figure 6.3, zone D-5
$\overline{\text{Superfast In}}$																	A6	Figure 6.3, zone D-5
System Control																	B4	Figure 6.3, zone A-2
$\overline{\text{System Control}}$		7	4															Figure 6.3, zone A-1
$\overline{\text{System Dir. Cmd}}$																	A8	Figure 6.3, zone E-2
System Ready Out																	A5	Figure 6.3, zone C-6
Uprange (160%)	5																	Figure 6.3, zone D-8
$\overline{\text{Uprange}}$	C			4														
Zener									H									Figure 6.3, zone B-4

* WHEN INSTRUMENT IS EQUIPPED WITH RMS CONVERTER (J12) THE RMS BYPASS BOARD (J11) IS ALSO USED



Q31 (MOUNTED ON REAR PANEL SHIELD FLANGE)
FARSIDE
C11
C12
C33
C34
C35
Q33 (MOUNTED ON FRONT GUARD BOX ASSY)
R72
R60
J12
J11
J10
J9
J8
J7
J6
J5
J14
MECCA GND
GRN
BLK
S5
S4
S3
S2
C25
TP8
K1
C42
W2
W3
OCI-4
OCI-3
OCI-2
OCI-1
T4
T2
T3
J3
J2
J201
U41
U40
U35
U28
U21
U14
U7
U1
U42
U36
U29
U22
U15
U8
U2
U30
U23
U16
U9
U3
U37
U31
U24
U17
U10
U4
U38
U32
U25
U18
U11
U5
U39
U33
U26
U19
U12
U6
U34
U27
U20
U13
U43
U44
J13
J4
J1
C10
Y1
C41
C18
Q19
TP7
TP6
R87
("RATE" CONTROL POT MOUNTED ON FRONT PANEL)
403614 - Z



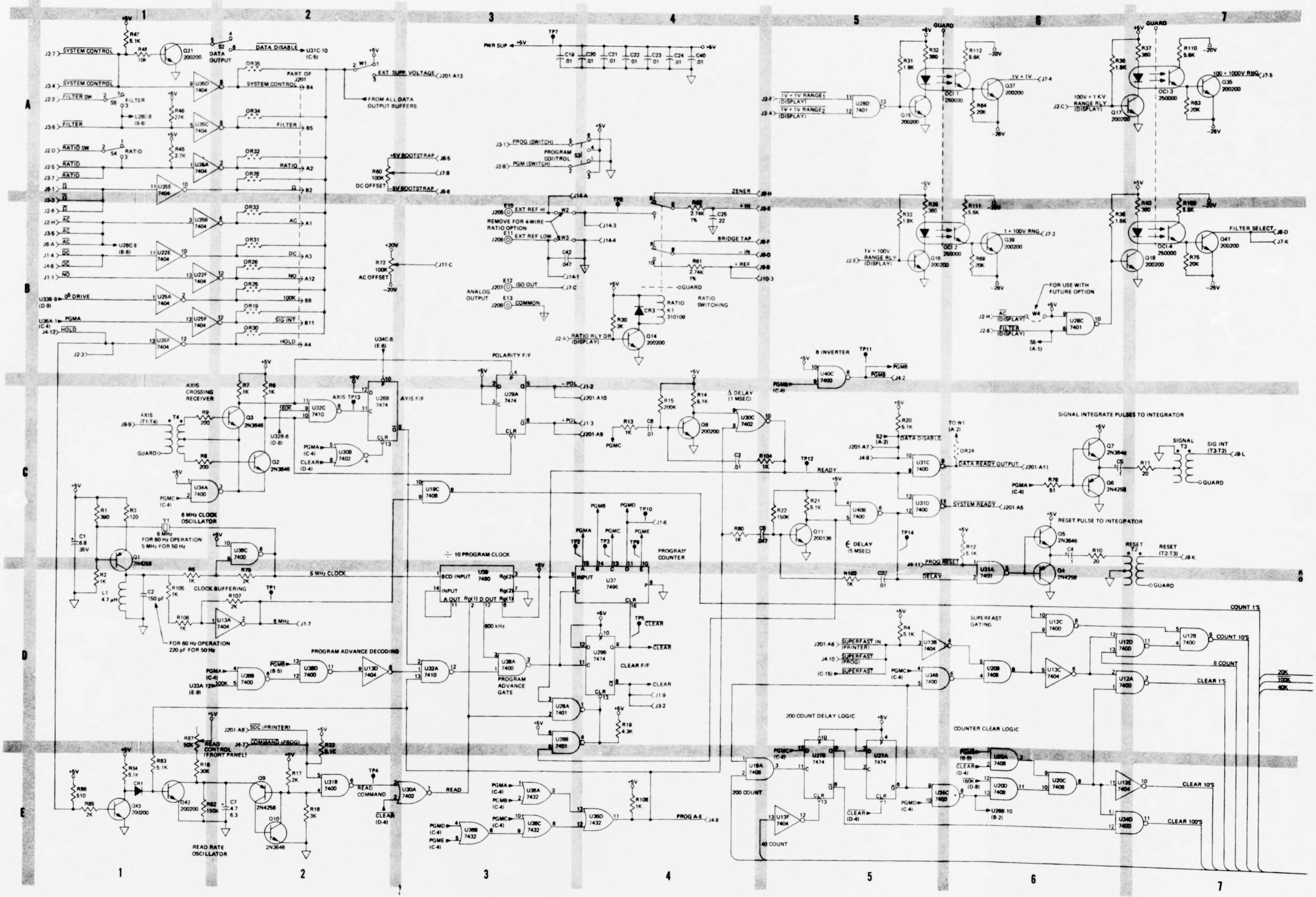
LED 1
LED 2
LED 3
LED 4
LED 5
LED 6
P13
403857 - 8

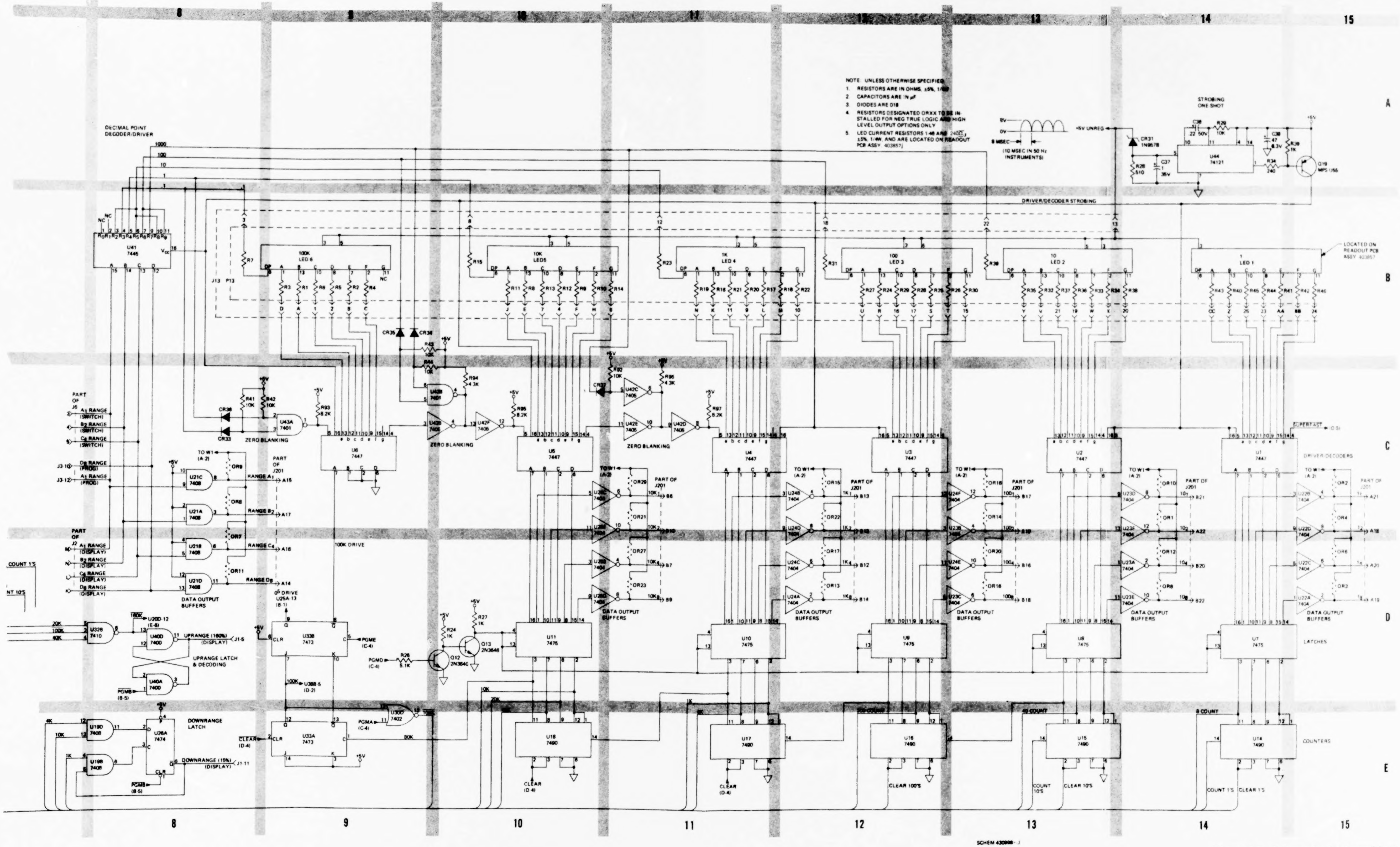
NOTES:

1. Reference designators shown as "U" on schematic and parts list may be identified on Logic and Interconnection Board as "M".
2. Power Supply Components Shaded.

Figure 6.1 - Layout, Logic and Interconnection (403614)

Figure 6.2 - Layout, Readout (403857)


1
2
3
4
5
6
7
A
B
C
D
E
SYSTEM CONTROL
DATA DISABLE
DATA OUTPUT
U31C-10 (C-5)
EXT. SUPP. VOLTAGE
J201 A13
FROM ALL DATA OUTPUT BUFFERS
PART OF J201
FILTER SW
FILTER
RATIO SW
RATIO
AC
DC
NO
Ω DRIVE
PGMA
HOLD
SIG INT
100K
PWR SUP
+5V BOOTSTRAP
-5V BOOTSTRAP
DC OFFSET
AC OFFSET
PROG (SWITCH)
PGM (SWITCH)
PROGRAM CONTROL
EXT REF HI
EXT REF LOW
REMOVE FOR 4-WIRE RATIO OPTION
ANALOG OUTPUT
ISO OUT
COMMON
ZENER
+ IN
BRIDGE TAP
- IN
+ REF
- REF
GUARD
RATIO K1 310109
RATIO SWITCHING
RATIO RLY DR (DISPLAY)
1V + 1V RANGE1 (DISPLAY)
1V + 1V RANGE2 (DISPLAY)
.1V + 1V
100V + 1 KV RANGE RLY (DISPLAY)
100 + 1000V RNG
1V + 100V RANGE RLY (DISPLAY)
1 + 100V RNG
FILTER SELECT
FOR USE WITH FUTURE OPTION
AC (DISPLAY)
FILTER (DISPLAY)
OCI-1 250000
OCI-2 250000
OCI-3 250000
OCI-4 250000
AXIS CROSSING RECEIVER
AXIS (T1-T4)
AXIS F/F
POLARITY F/F
- POL
+ POL
Δ DELAY (1 MSEC)
8 INVERTER
SIGNAL INTEGRATE PULSES TO INTEGRATOR
SIGNAL
SIG INT (T3-T2)
DATA READY OUTPUT
SYSTEM READY
READY
TO W1 (A-2)
RESET PULSE TO INTEGRATOR
RESET (T2-T3)
PROG RESET
DELAY
ε DELAY (5 MSEC)
6 MHz CLOCK OSCILLATOR
6 MHz FOR 60 Hz OPERATION 5 MHz FOR 50 Hz
CLOCK BUFFERING
6 MHz CLOCK
FOR 60 Hz OPERATION 220 pF FOR 50 Hz
÷ 10 PROGRAM CLOCK
BCD INPUT
U39 7490
PROGRAM COUNTER
U37 7496
600 kHz
CLEAR F/F
CLEAR
PROGRAM ADVANCE DECODING
PROGRAM ADVANCE GATE
SUPERFAST GATING
SUPERFAST IN (PRINTER)
SUPERFAST (PROG)
SUPERFAST
COUNT 1'S
COUNT 10'S
8 COUNT
CLEAR 1'S
20K
100K
40K
200 COUNT DELAY LOGIC
200 COUNT
40 COUNT
COUNTER CLEAR LOGIC
CLEAR 10'S
CLEAR 100'S
READ CONTROL (FRONT PANEL)
SDC (PRINTER)
COMMAND (PROG)
READ COMMAND
READ
READ RATE OSCILLATOR
PROG A-E

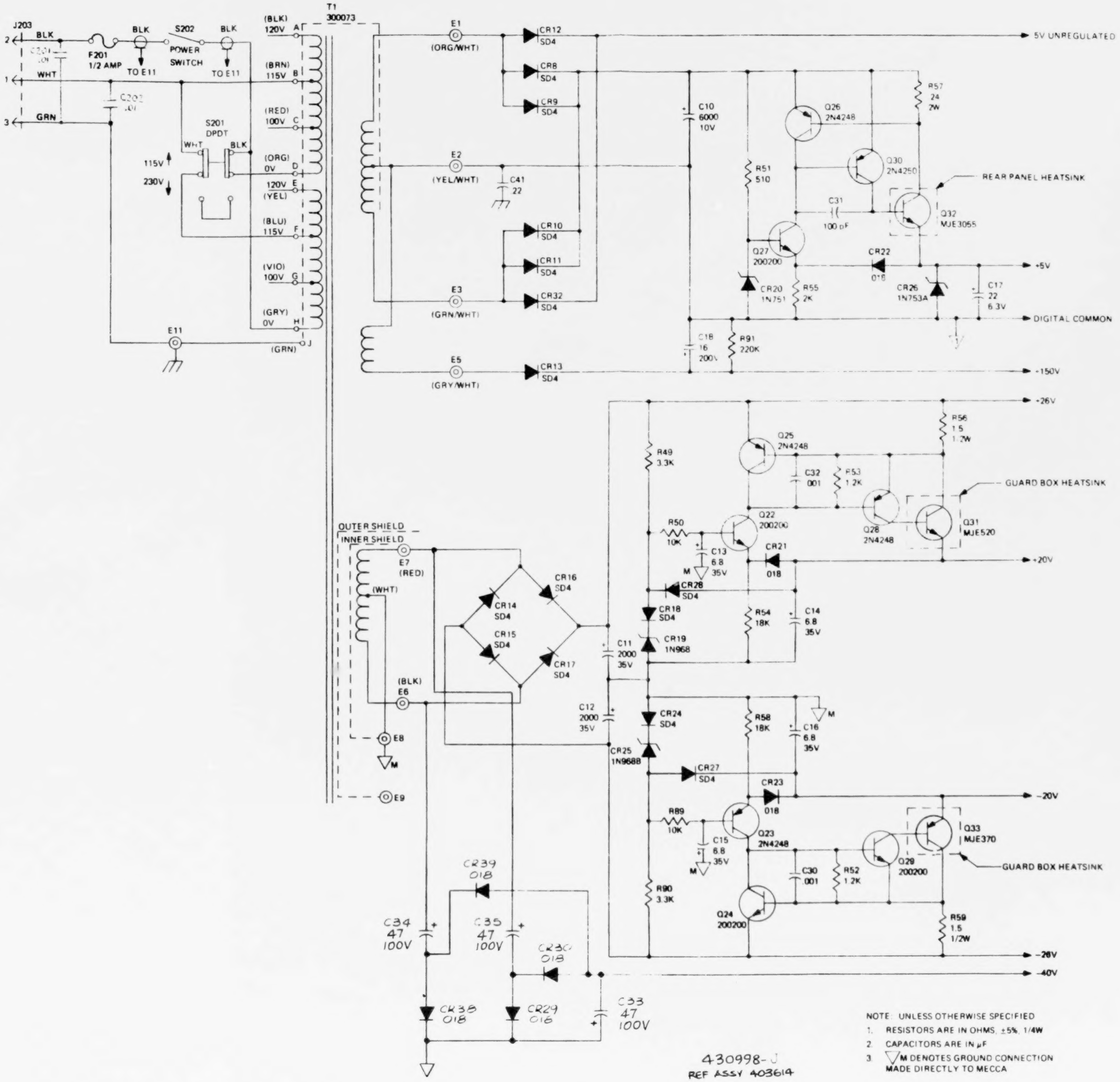


NOTE: UNLESS OTHERWISE SPECIFIED

1. RESISTORS ARE IN OHMS, ±5%, 1/4W
2. CAPACITORS ARE IN µF
3. DIODES ARE 018
4. RESISTORS DESIGNATED ORXX TO BE INSTALLED FOR NEG TRUE LOGIC AND HIGH LEVEL OUTPUT OPTIONS ONLY
5. LED CURRENT RESISTORS 1-46 ARE 240Ω, ±5%, 1/4W, AND ARE LOCATED ON READOUT PCB ASSY. 403857J

8
9
10
11
12
13
14
15
A
B
C
D
E
DECIMAL POINT DECODER/DRIVER
1000
100
10
1
U41 7445
J13 P13
100K LED 6
10K LED5
1K LED 4
100 LED 3
10 LED 2
1 LED 1
LOCATED ON READOUT PCB ASSY. 403857
DRIVER/DECODER STROBING
STROBING ONE SHOT
U44 74121
CR31 1N9678
Q19 MPS U56
+5V UNREG
8 MSEC
(10 MSEC IN 50 Hz INSTRUMENTS)
ZERO BLANKING
U6 7447
U5 7447
U4 7447
U3 7447
U2 7447
U1 7447
DRIVER DECODERS
SUPERFAST
PART OF J5
A1 RANGE (SWITCH)
B2 RANGE (SWITCH)
C4 RANGE (SWITCH)
D8 RANGE (PROG)
A1 RANGE (PROG)
PART OF J2
A1 RANGE (DISPLAY)
B2 RANGE (DISPLAY)
C4 RANGE (DISPLAY)
D8 RANGE (DISPLAY)
COUNT 1'S
NT 10'S
DATA OUTPUT BUFFERS
RANGE A1
RANGE B2
RANGE C4
RANGE D8
PART OF J201
TO W1 (A-2)
100K DRIVE
D5 DRIVE U25A-13 (B-1)
U32B 7410
U20D-12 (E-6)
UPRANGE (160%) (DISPLAY)
UPRANGE LATCH & DECODING
U40D 7400
U40A 7400
PGMB (B-5)
U33B 7473
PGME (C-4)
PGMD (C-4)
Q12 2N3646
Q13 2N3646
U11 7475
U10 7475
U9 7475
U8 7475
U7 7475
LATCHES
DOWNRANGE LATCH
U19D 7408
U19B 7408
U26A 7474
DOWNRANGE (15%) (DISPLAY)
CLEAR (D-4)
U33A 7473
PGMA (C-4)
U30D 7402
U18 7490
U17 7490
U16 7490
U15 7490
U14 7490
COUNTERS
CLEAR 100'S
COUNT 10'S
CLEAR 10'S
COUNT 1'S
CLEAR 1'S
8 COUNT
SCHEM 430998-J

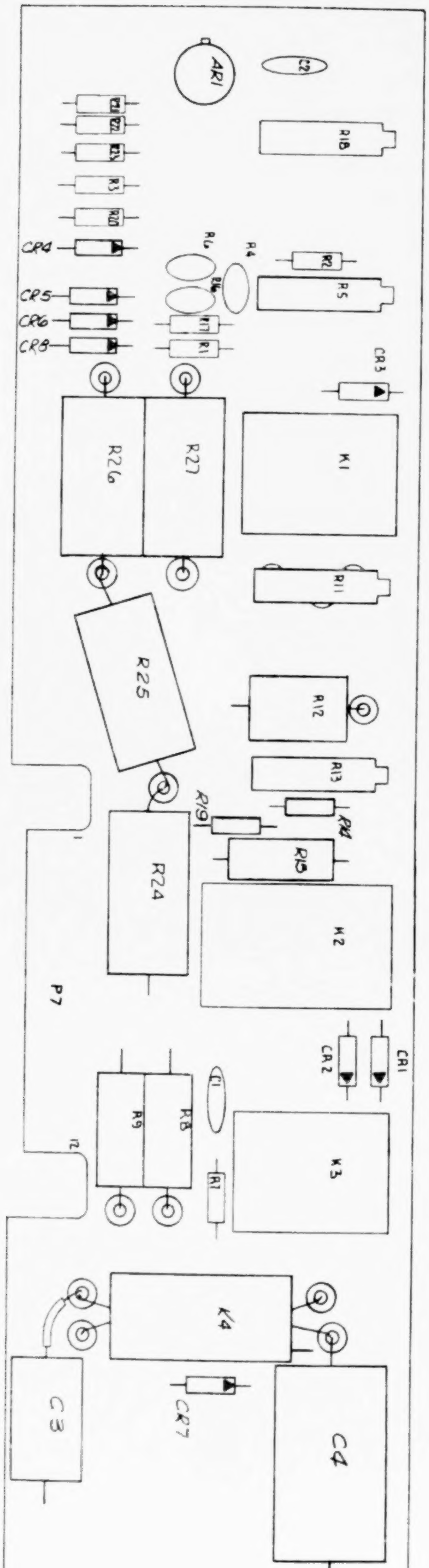
Figure 6.3 - Schematic, Logic and Interconnection


J203
2
1
3
BLK
WHT
GRN
C201
.01
C202
.01
F201
1/2 AMP
BLK
TO E11
S202
POWER
SWITCH
BLK
TO E11
S201
DPDT
WHT
BLK
115V
230V
E11
T1
300073
(BLK)
120V
A
(BRN)
115V
B
(RED)
100V
C
(ORG)
0V
D
120V
E
(YEL)
(BLU)
115V
F
(VIO)
100V
G
(GRY)
0V
H
J
(GRN)
E1
(ORG/WHT)
CR12
SD4
CR8
SD4
CR9
SD4
E2
(YEL/WHT)
C41
22
CR10
SD4
CR11
SD4
E3
(GRN/WHT)
CR32
SD4
E5
(GRY/WHT)
CR13
SD4
5V UNREGULATED
C10
6000
10V
R51
510
Q26
2N4248
Q30
2N4250
R57
24
2W
REAR PANEL HEATSINK
C31
100 pF
Q32
MJE3055
Q27
200200
CR22
018
+5V
CR20
1N751
R55
2K
CR26
1N753A
C17
22
6.3V
DIGITAL COMMON
C18
16
200V
R91
220K
+150V
+26V
R49
3.3K
Q25
2N4248
C32
.001
R53
1.2K
R56
1.5
1/2W
GUARD BOX HEATSINK
R50
10K
Q22
200200
C13
6.8
35V
M
Q28
2N4248
Q31
MJE520
CR21
018
+20V
OUTER SHIELD
INNER SHIELD
E7
(RED)
(WHT)
CR16
SD4
CR14
SD4
CR15
SD4
CR17
SD4
CR28
SD4
CR18
SD4
R54
18K
C14
6.8
35V
CR19
1N968
C11
2000
35V
(BLK)
E6
E8
M
E9
C12
2000
35V
CR24
SD4
R58
18K
C16
6.8
35V
M
CR25
1N968B
CR27
SD4
CR23
018
−20V
R89
10K
Q23
2N4248
C15
6.8
35V
M
Q33
MJE370
Q29
200200
GUARD BOX HEATSINK
C30
.001
R52
1.2K
R90
3.3K
Q24
200200
R59
1.5
1/2W
−26V
−40V
CR39
018
C34
47
100V
C35
47
100V
CR30
018
C33
47
100V
CR38
018
CR29
018
430998-J
REF ASSY 403614

NOTE: UNLESS OTHERWISE SPECIFIED

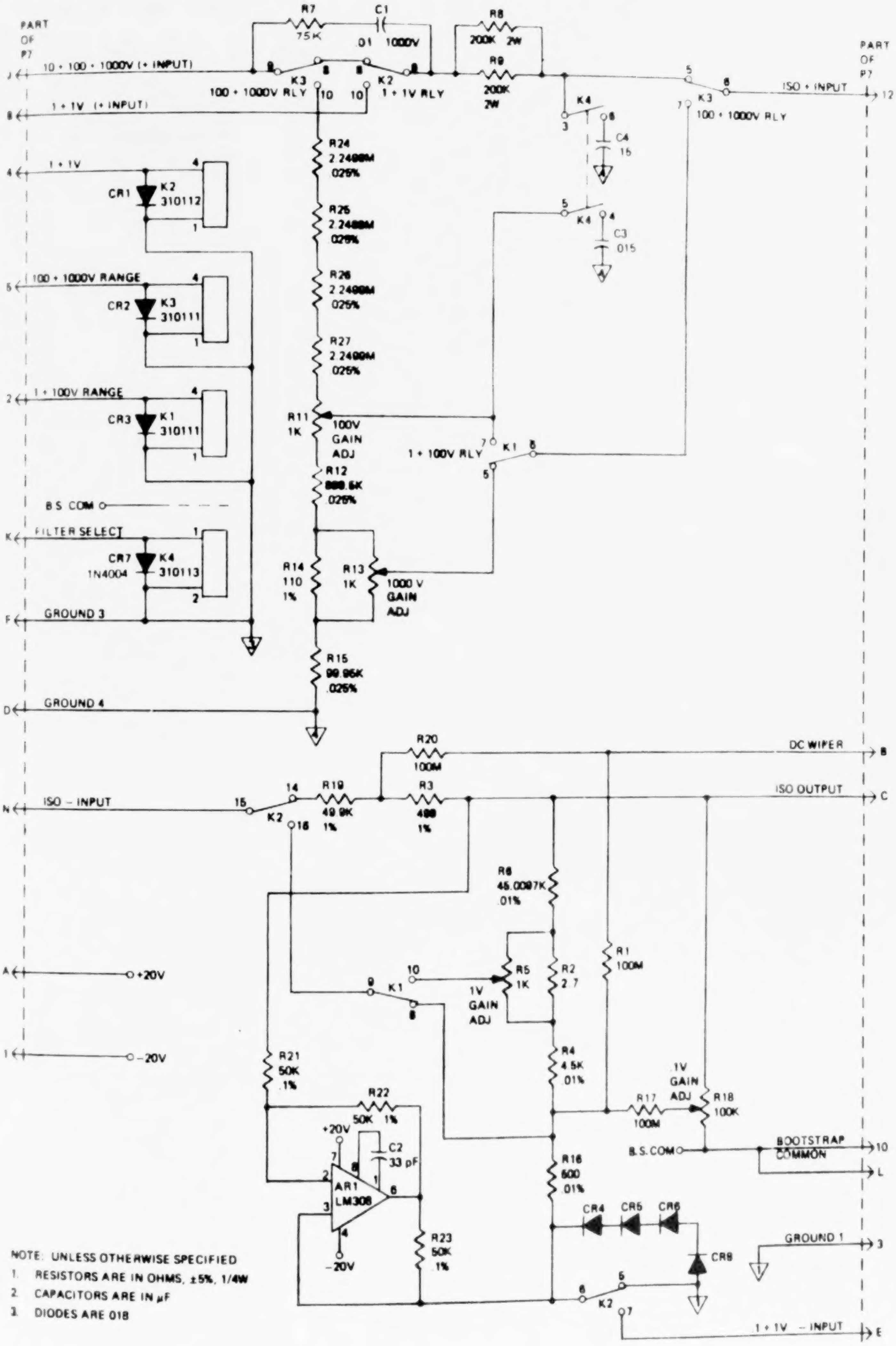
1. RESISTORS ARE IN OHMS, ±5%, 1/4W
2. CAPACITORS ARE IN μF
3. ▽M DENOTES GROUND CONNECTION MADE DIRECTLY TO MECCA

Figure 6.4 - Schematic, Power Supply


AR1
C2
R18
R23
R22
R21
R3
R20
CR4
R6
R4
R16
R2
R5
CR5
CR6
CR8
R17
R1
CR3
R26
R27
K1
R11
R25
R12
R13
R19
R14
R15
R24
K2
P7
CR1
CR2
C1
R8
R9
R7
K3
K4
C3
CR7
C4

403623-J

Figure 6.5 - Layout, Attenuator (403623)


PART OF P7
10 + 100 + 1000V (+ INPUT)
1 + 1V (+ INPUT)
1 + 1V
100 + 1000V RANGE
1 + 100V RANGE
B.S. COM
FILTER SELECT
GROUND 3
GROUND 4
ISO – INPUT
+20V
–20V
R7
7.5K
C1
.01 1000V
R8
200K 2W
R9
200K 2W
K3
100 + 1000V RLY
K2
1 + 1V RLY
K4
C4
15
C3
.015
ISO + INPUT
100 + 1000V RLY
CR1
K2 310112
CR2
K3 310111
CR3
K1 310111
CR7
1N4004
K4 310113
R24
2.2488M
.025%
R25
2.2488M
.025%
R26
2.2488M
.025%
R27
2.2488M
.025%
R11
1K
100V GAIN ADJ
R12
888.6K
.025%
1 + 100V RLY
K1
R14
110
1%
R13
1K
1000 V GAIN ADJ
R15
99.96K
.025%
R20
100M
DC WIPER
R19
49.9K
1%
R3
499
1%
ISO OUTPUT
R6
45.0097K
.01%
R5
1K
1V GAIN ADJ
R2
2.7
R1
100M
R4
4.5K
.01%
.1V GAIN ADJ
R17
100M
R18
100K
B.S.COM
BOOTSTRAP COMMON
R21
50K
.1%
R22
50K 1%
C2
33 pF
AR1
LM308
R23
50K
.1%
R16
500
.01%
CR4
CR5
CR6
CR8
GROUND 1
1 + 1V – INPUT
J
8
4
6
2
K
F
D
N
A
1
12
B
C
10
L
3
E

NOTE: UNLESS OTHERWISE SPECIFIED
1. RESISTORS ARE IN OHMS, ±5%, 1/4W
2. CAPACITORS ARE IN μF
3. DIODES ARE 018

SCHEM 432005 D
REF ASSY 403623

Figure 6.6 - Schematic, Attenuator

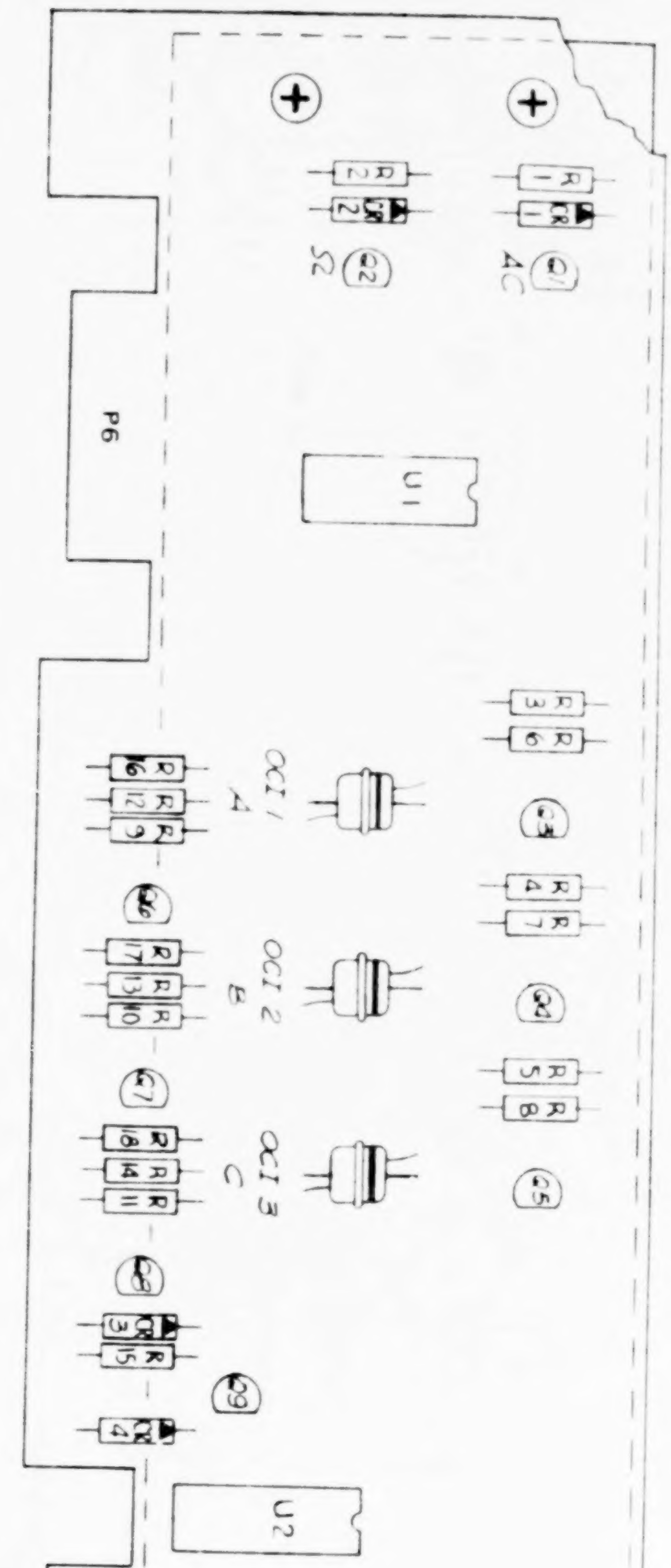

P6
U1
U2
P5
Q1
Q2
Q3
Q4
Q5
Q6
Q7
Q8
Q9
OCI 1
OCI 2
OCI 3
K1 AC
K2 Ω
E1
E2
E3
E4
E5
E6
E7
E11
E13
E15
E16
E18
403625-F

Figure 6.7 - Layout, Switching (403625)

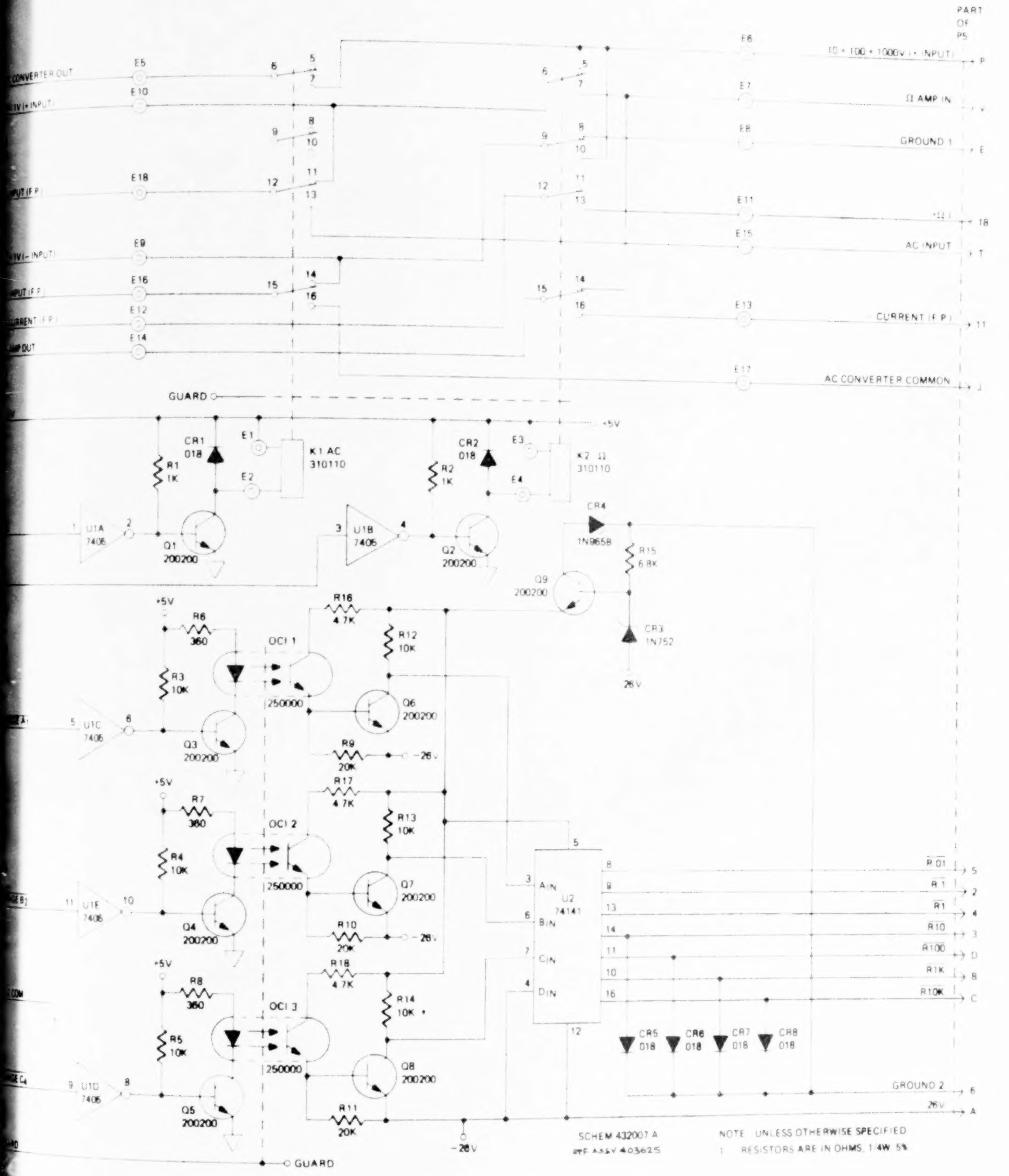
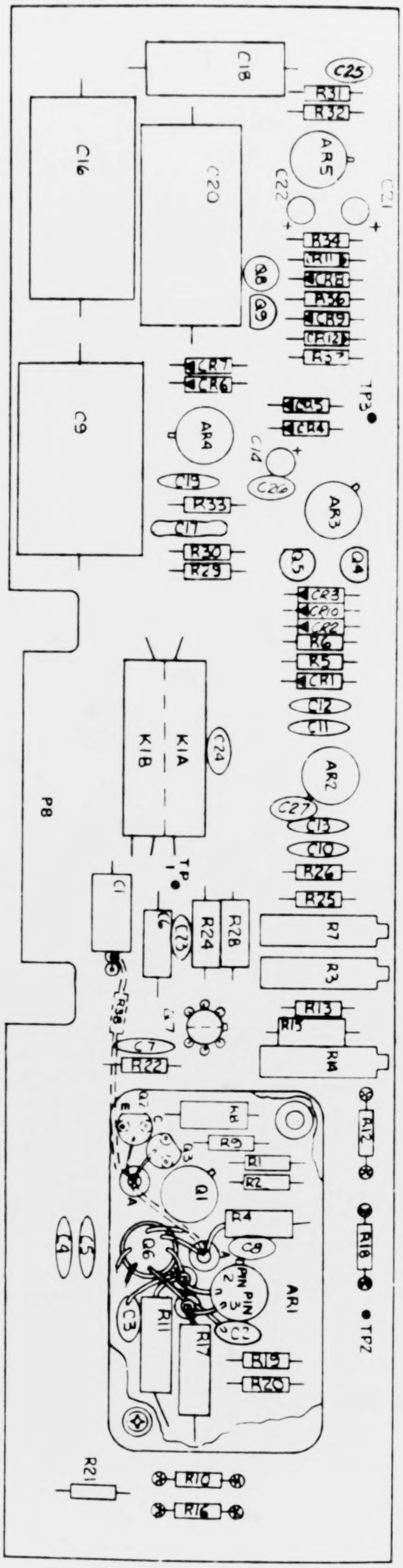
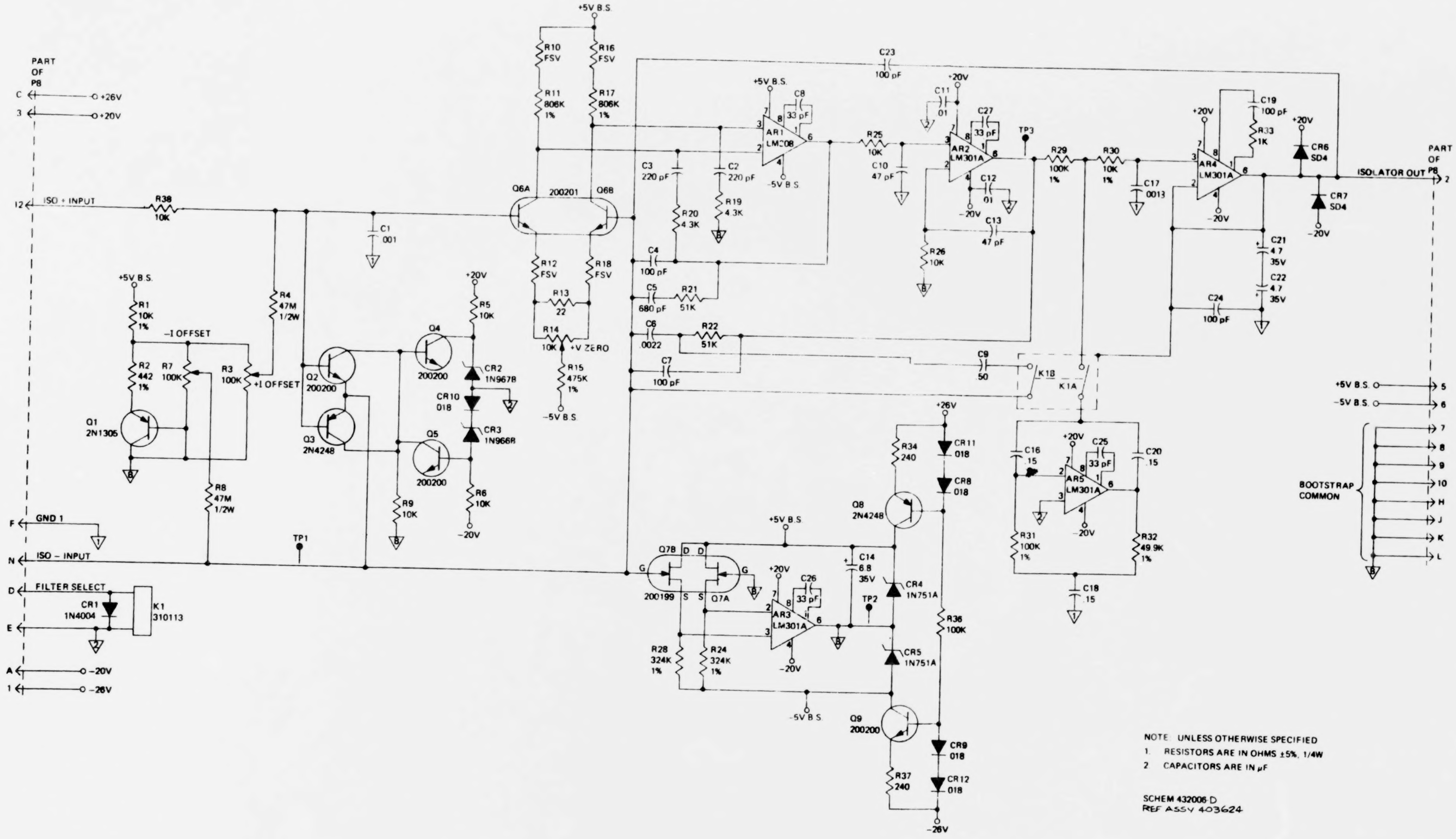

PART OF P5
CONVERTER OUT
V (+ INPUT)
INPUT (F.P.)
V (- INPUT)
INPUT (F.P.)
CURRENT (F.P.)
AMP OUT
E5
E10
E18
E9
E16
E12
E14
E6
E7
E8
E11
E15
E13
E17
10 + 100 + 1000V (+ INPUT)
Ω AMP IN
GROUND 1
AC INPUT
CURRENT (F.P.)
AC CONVERTER COMMON
GUARD
+5V
CR1 018
CR2 018
R1 1K
R2 1K
E1
E2
E3
E4
K1 AC 310110
K2 Ω 310110
U1A 7405
U1B 7405
Q1 200200
Q2 200200
CR4 1N965B
R15 6.8K
Q9 200200
CR3 1N752
26V
R16 4.7K
R12 10K
R6 360
R3 10K
OCI 1
250000
Q6 200200
U1C 7406
Q3 200200
R9 20K
-26V
R17 4.7K
R13 10K
R7 360
R4 10K
OCI 2
Q7 200200
U1E 7406
Q4 200200
R10 20K
R18 4.7K
R14 10K
R8 360
R5 10K
OCI 3
Q8 200200
U1D 7406
Q5 200200
R11 20K
U2 74141
AIN
BIN
CIN
DIN
R 01
R 1
R1
R10
R100
R1K
R10K
CR5 018
CR6 018
CR7 018
CR8 018
GROUND 2
26V
-26V
GUARD
SCHEM 432007 A
REF ASSY 403625
NOTE: UNLESS OTHERWISE SPECIFIED
1. RESISTORS ARE IN OHMS, 1/4W, 5%

Figure 6.8 - Schematic, Switching


C18
C25
R31
R32
C16
C20
AR5
C22
C21
R34
CR11
CR8
R36
CR9
CR12
R35
Q8
Q9
CR7
CR6
TP3
C9
AR4
CR5
CR4
C14
C26
C19
R33
C17
AR3
R30
R29
Q5
Q4
CR3
CR10
CR2
R6
R5
CR1
C12
C11
K1A
K1B
C24
AR2
P8
C27
C13
C10
R26
R25
TP1
C1
K6
C23
R24
R28
R7
R3
R36
Q7
R13
R15
C7
R22
R14
R8
R9
R1
R2
Q1
Q3
R12
R4
C8
Q6
C4
C5
AR1
R18
C3
R11
R17
C2
TP2
R19
R20
R21
R10
R16

403624-P

Figure 6.9 - Layout, Isolator (403624)


PART OF P8
C
3
+26V
+20V
12
ISO + INPUT
R38 10K
+5V B.S.
R1 10K 1%
-I OFFSET
R2 442 1%
R7 100K
R3 100K
+I OFFSET
R4 47M 1/2W
Q1 2N1305
R8 47M 1/2W
C1 .001
Q2 200200
Q3 2N4248
Q4 200200
Q5 200200
+20V
R5 10K
CR2 1N9678
CR10 018
CR3 1N9668
R6 10K
-20V
R9 10K
F
GND 1
TP1
N
ISO - INPUT
D
FILTER SELECT
CR1 1N4004
K1 310113
E
A
-20V
1
-26V
R10 FSV
R16 FSV
R11 806K 1%
R17 806K 1%
Q6A
200201
Q6B
R12 FSV
R18 FSV
R13 22
R14 10K
+V ZERO
R15 475K 1%
-5V B.S.
C3 220 pF
C2 220 pF
R20 4.3K
R19 4.3K
C4 100 pF
C5 680 pF
R21 51K
C6 .0022
R22 51K
C7 100 pF
AR1 LM208
C8 33 pF
C23 100 pF
R25 10K
C10 47 pF
C11 .01
AR2 LM301A
C27 33 pF
C12 .01
C13 47 pF
R26 10K
TP3
R29 100K 1%
R30 10K 1%
C17 .0013
AR4 LM301A
C19 100 pF
R33 1K
CR6 SD4
CR7 SD4
C21 4.7 35V
C22 4.7 35V
C24 100 pF
ISOLATOR OUT
PART OF P8
2
C9 50
K1B
K1A
+26V
R34 240
CR11 018
CR8 018
Q8 2N4248
C16 .15
AR5 LM301A
C25 33 pF
C20 .15
R31 100K 1%
R32 49.9K 1%
C18 .15
+5V B.S.
5
-5V B.S.
6
7
8
9
10
H
J
K
L
BOOTSTRAP COMMON
Q7B
200199
Q7A
G
D
S
AR3 LM301A
C26 33 pF
C14 6.8 35V
TP2
CR4 1N751A
CR5 1N751A
R36 100K
R28 324K 1%
R24 324K 1%
Q9 200200
R37 240
CR9 018
CR12 018
-26V

NOTE UNLESS OTHERWISE SPECIFIED
1. RESISTORS ARE IN OHMS ±5%, 1/4W
2. CAPACITORS ARE IN μF

SCHEM 432006 D
REF ASSY 403624

Figure 6.10 - Schematic, Isolator

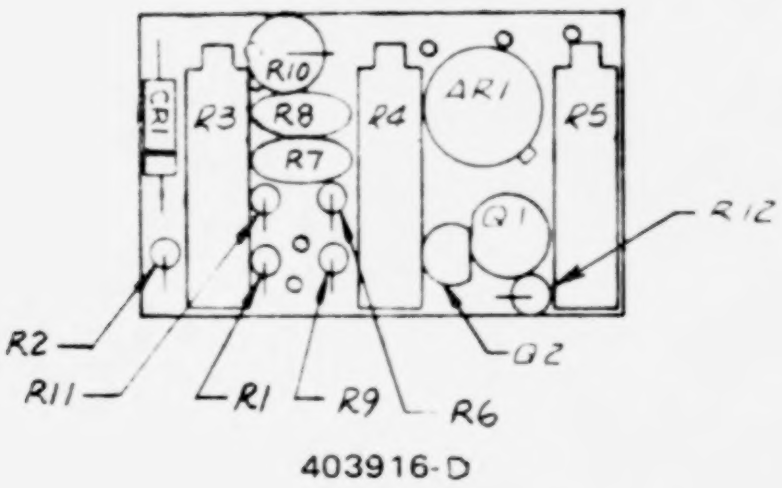

CR1
R10
R8
R7
R3
R4
AR1
R5
Q1
R12
R2
R11
R1
R9
R6
Q2
403916-D

Figure 6.11 - Layout, 10V Reference Amplifier (403916)

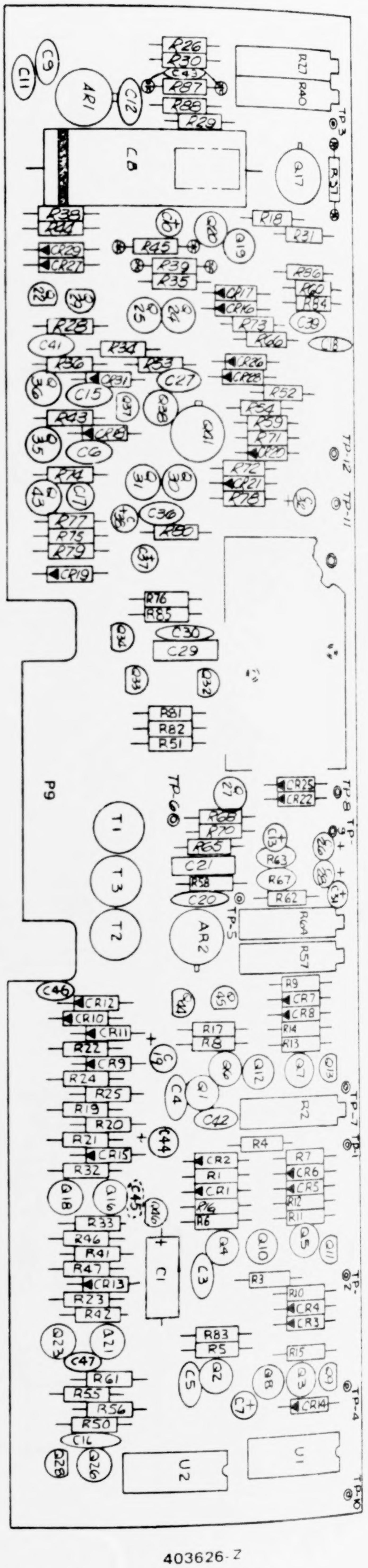
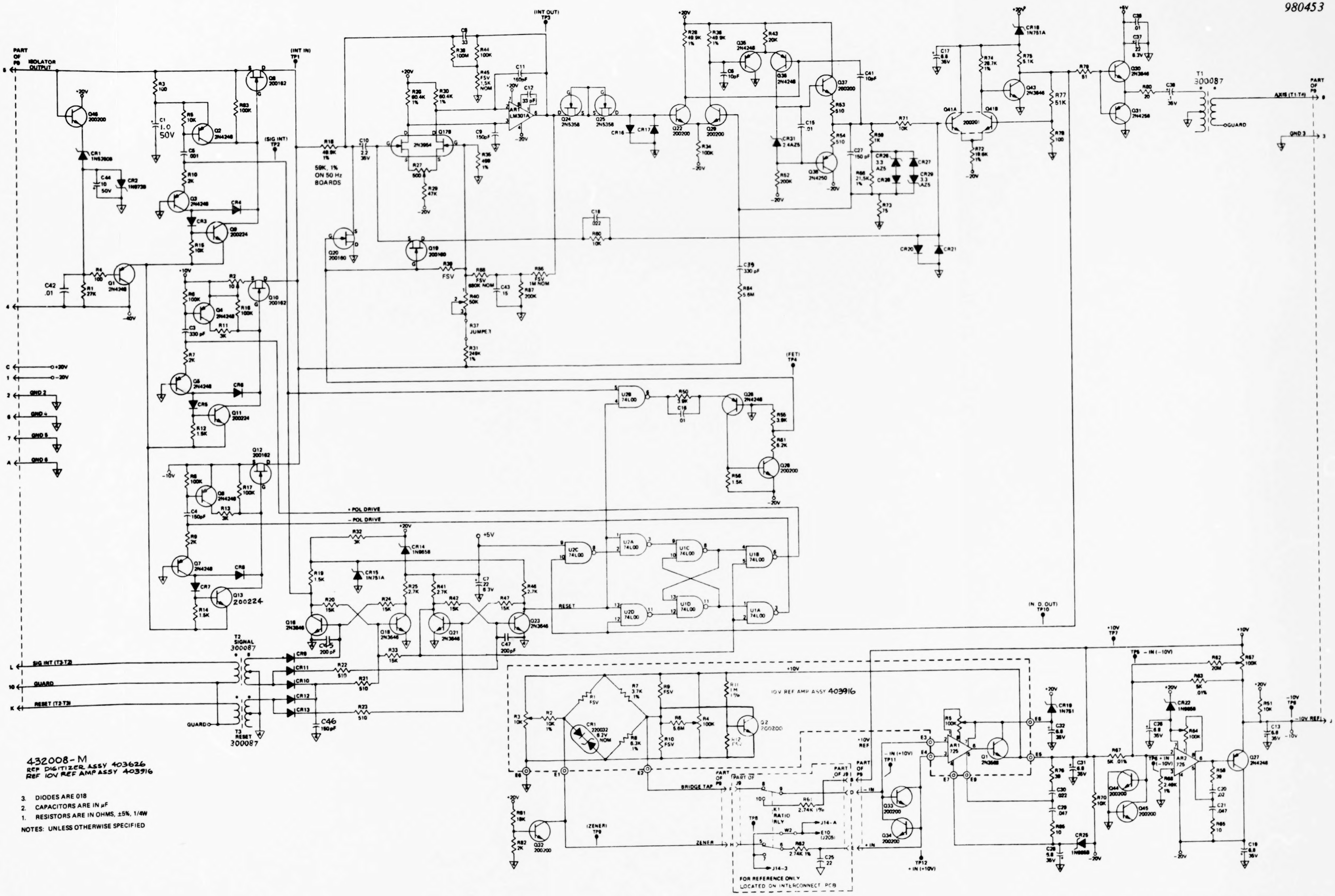

AR1
AR2
C8
C9
C11
C12
Q17
R26
R30
R87
R86
R29
R37
R40
R18
R31
R45
R39
R35
TP-3
TP-12
TP-11
TP-6
TP-8
TP-5
TP-7
TP-1
TP-2
TP-4
TP-10
P9
T1
T2
T3
C29
C30
R76
R85
R81
R82
R51
C21
C20
R57
R62
R63
R67
C1
C3
C4
C5
C7
C16
C42
C44
C46
C47
CR1
CR2
CR3
CR4
CR5
CR6
CR7
CR8
CR9
CR10
CR11
CR12
CR13
CR14
CR15
CR19
CR22
CR25
CR27
CR29
R1
R3
R4
R5
R9
R10
R11
R12
R13
R14
R15
R17
R19
R20
R21
R22
R23
R24
R25
R32
R33
R41
R42
R46
R47
R50
R55
R56
R61
R83
U1
U2
403626-Z

Figure 6.12 - Layout, Digitizer (403626)


PART OF P9
ISOLATOR OUTPUT
(INT IN) TP1
(SIG INT) TP2
(INT OUT) TP3
(FET) TP4
(IN D. OUT) TP10
59K, 1% ON 50 Hz BOARDS
Q46 200200
CR1 1N5608
C44 10 50V
CR2 1N973B
C1 1.0 50V
R3 100
Q2 2N4248
Q3 2N4248
Q8 200162
Q9 200224
Q1 2N4248
C42 .01
R1 27K
Q10 200162
Q4 2N4248
Q5 2N4248
Q11 200224
Q12 200162
Q6 2N4248
Q7 2N4248
Q13 200224
Q17A 2N3954
Q17B
AR1 LM301A
Q24 2N5358
Q25 2N5358
Q22 200200
Q29 200200
Q35 2N4248
Q36 2N4248
Q37 200200
Q38 2N4250
CR31 2.4AZ5
CR26 3.3 AZ5
CR27
CR28
CR29 3.3 AZ5
Q41A
Q41B
200201
Q43 2N3646
CR18 1N751A
Q30 2N3646
Q31 2N4258
T1 300087
AXIS (T1-T4)
GUARD
GND 3
Q20 200160
Q19 200160
R39 FSV
R40 50K
R37 JUMPER
R31 249K 1%
CR20
CR21
C39 330 pF
R84 5.6M
U2B 74L00
Q26 2N4248
Q28 200200
+ POL DRIVE
− POL DRIVE
CR14 1N658
CR15 1N751A
+5V
U2C 74L00
U2A 74L00
U1C 74L00
U1B 74L00
U2D 74L00
U1D 74L00
U1A 74L00
RESET
Q16 2N3646
Q18 2N3646
Q21 2N3646
Q23 2N3646
C47 200pF
T2 SIGNAL 300087
T3 RESET 300087
SIG INT (T2-T2)
GUARD
RESET (T2-T3)
C46 150pF
+20V
−20V
+10V
−10V
GND 2
GND 4
GND 5
GND 6
10V REF AMP ASSY 403916
CR1 220032 6.2V NOM
R1 FSV
R7 3.7K 1%
R8 6.3K 1%
R9 FSV
R10 FSV
R11 1M 1%
R12 FSV
R6 5.6M
R4 100K
Q2 200200
R3 10K
R2 10K 1%
E6
E1
E2
BRIDGE TAP
ZENER
(ZENER) TP9
Q32 200200
R81 18K
R82 2K
PART OF P9
PART OF J9
K1 RATIO RLY
R61 2.74K 1%
R62 2.74K 1%
C25 22
TP8
W2
J14-A
E10 (J205)
J14-3
FOR REFERENCE ONLY LOCATED ON INTERCONNECT PCB
+10V REF
− IN (+10V) TP11
+ IN (+10V) TP12
Q33 200200
Q34 200200
AR1 725
Q1 2N3646
CR19 1N751
E3
E4
E5
E7
E8
E9
TP7
TP5 − IN (−10V)
TP6 + IN (−10V)
AR2 725
CR22 1N658
Q44 200200
Q45 200200
CR25 1N658
Q27 2N4248
−10V TP8
−10V REF
R62 20M
R67 100K
R63 5K .01%

432008-M
REF DIGITIZER ASSY 403626
REF 10V REF AMP ASSY 403916

3. DIODES ARE 018
2. CAPACITORS ARE IN µF
1. RESISTORS ARE IN OHMS, ±5%, 1/4W

NOTES: UNLESS OTHERWISE SPECIFIED

Figure 6.13 - Schematic, Digitizer and 10V Reference Amplifier

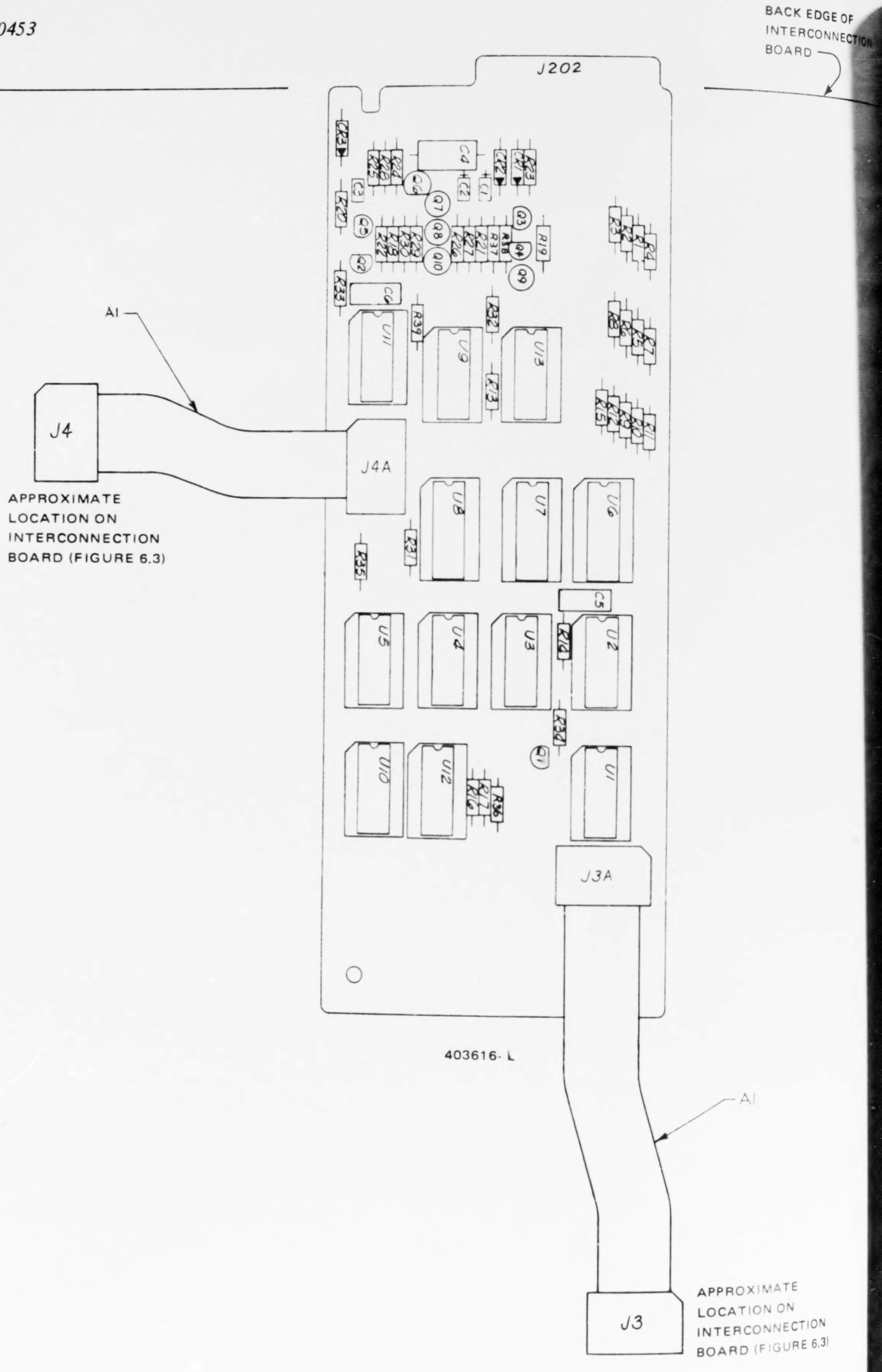
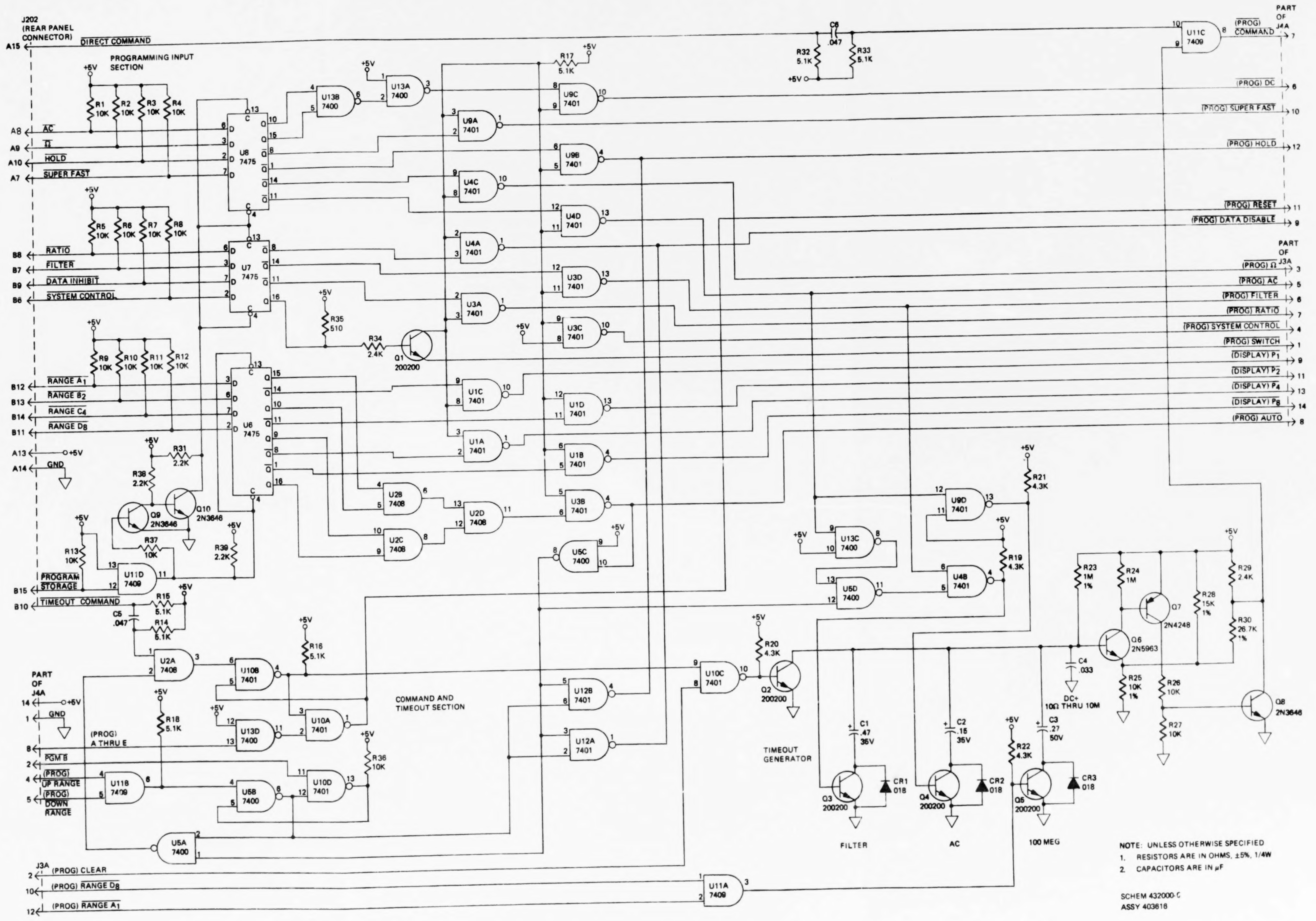

BACK EDGE OF INTERCONNECTION BOARD
J202
CR3
C4
CR2
CR1
R23
R25
R28
R24
Q6
C3
C2
C1
Q7
R20
Q5
Q3
Q8
R19
R22
R29
R30
R26
R27
R21
R37
R38
Q4
R3
R2
R1
R4
Q2
Q10
Q9
R33
C6
R32
R39
U11
U9
U13
R8
R6
R5
R7
R13
R15
R12
R9
R10
R11
A1
J4
J4A
APPROXIMATE LOCATION ON INTERCONNECTION BOARD (FIGURE 6.3)
U8
U7
U6
R31
R35
C5
U5
U4
U3
R14
U2
R34
Q1
U10
U12
R17
R16
R36
U1
J3A
403616- L
A1
J3
APPROXIMATE LOCATION ON INTERCONNECTION BOARD (FIGURE 6.3)

Figure 6.14 - Layout, Program (403616)


J202
(REAR PANEL
CONNECTOR)
A15
DIRECT COMMAND
PROGRAMMING INPUT
SECTION
+5V
R1 10K
R2 10K
R3 10K
R4 10K
A8
AC
A9
Ω
A10
HOLD
A7
SUPER FAST
U8
7475
R5 10K
R6 10K
R7 10K
R8 10K
B8
RATIO
B7
FILTER
B9
DATA INHIBIT
B6
SYSTEM CONTROL
U7
7475
R9 10K
R10 10K
R11 10K
R12 10K
B12
RANGE A1
B13
RANGE B2
B14
RANGE C4
B11
RANGE D8
U6
7475
A13
A14
GND
R31 2.2K
R38 2.2K
Q9 2N3646
Q10 2N3646
R13 10K
R37 10K
R39 2.2K
U11D 7409
B15
PROGRAM STORAGE
B10
TIMEOUT COMMAND
C5 .047
R15 5.1K
R14 5.1K
U2A 7408
PART OF J4A
14
1
GND
8
(PROG) A THRU E
PGM 8
2
4
(PROG) UP RANGE
5
(PROG) DOWN RANGE
U11B 7409
R18 5.1K
U10B 7401
R16 5.1K
U13D 7400
U10A 7401
U5B 7400
U10D 7401
R36 10K
U5A 7400
J3A
(PROG) CLEAR
10
(PROG) RANGE D8
12
(PROG) RANGE A1
U11A 7409
U13B 7400
U13A 7400
R17 5.1K
U9A 7401
U4C 7401
U4A 7401
U3A 7401
R35 510
R34 2.4K
Q1 200200
U1C 7401
U1A 7401
U2B 7408
U2C 7408
U2D 7408
U9C 7401
U9B 7401
U4D 7401
U3D 7401
U3C 7401
U1D 7401
U1B 7401
U3B 7401
U5C 7400
COMMAND AND
TIMEOUT SECTION
U12B 7401
U12A 7401
U10C 7401
R20 4.3K
Q2 200200
C6 .047
R32 5.1K
R33 5.1K
U11C 7409
PART OF J4A
(PROG) COMMAND
7
(PROG) DC
6
(PROG) SUPER FAST
10
(PROG) HOLD
12
(PROG) RESET
11
(PROG) DATA DISABLE
9
PART OF J3A
(PROG) Ω
3
(PROG) AC
5
(PROG) FILTER
6
(PROG) RATIO
7
(PROG) SYSTEM CONTROL
4
(PROG) SWITCH
1
(DISPLAY) P1
9
(DISPLAY) P2
11
(DISPLAY) P4
13
(DISPLAY) P8
14
(PROG) AUTO
8
R21 4.3K
U9D 7401
U13C 7400
U5D 7400
U4B 7401
R19 4.3K
R23 1M 1%
R24 1M
Q7 2N4248
R28 15K 1%
R29 2.4K
R30 26.7K 1%
Q6 2N5963
C4 .033
R25 10K 1%
R26 10K
R27 10K
Q8 2N3646
DC+
10Ω THRU 10M
TIMEOUT
GENERATOR
C1 .47 35V
C2 .15 35V
C3 .27 50V
R22 4.3K
Q3 200200
CR1 018
Q4 200200
CR2 018
Q5 200200
CR3 018
FILTER
AC
100 MEG

NOTE: UNLESS OTHERWISE SPECIFIED
1. RESISTORS ARE IN OHMS, ±5%, 1/4W
2. CAPACITORS ARE IN μF

SCHEM 432000-C
ASSY 403618

Figure 6.15 - Schematic, Program

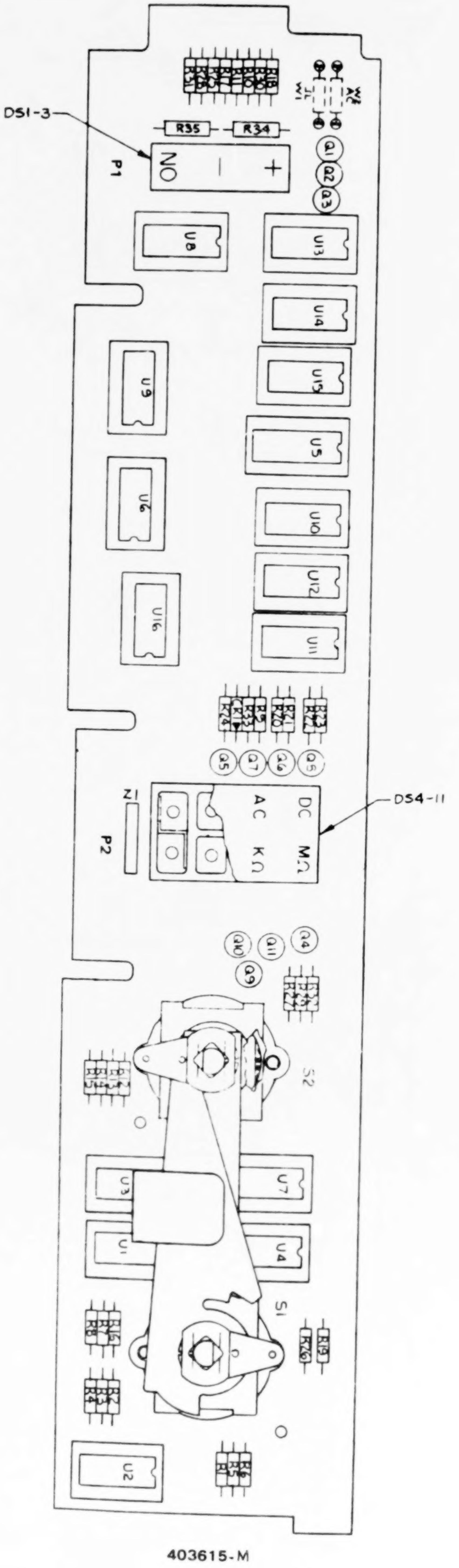
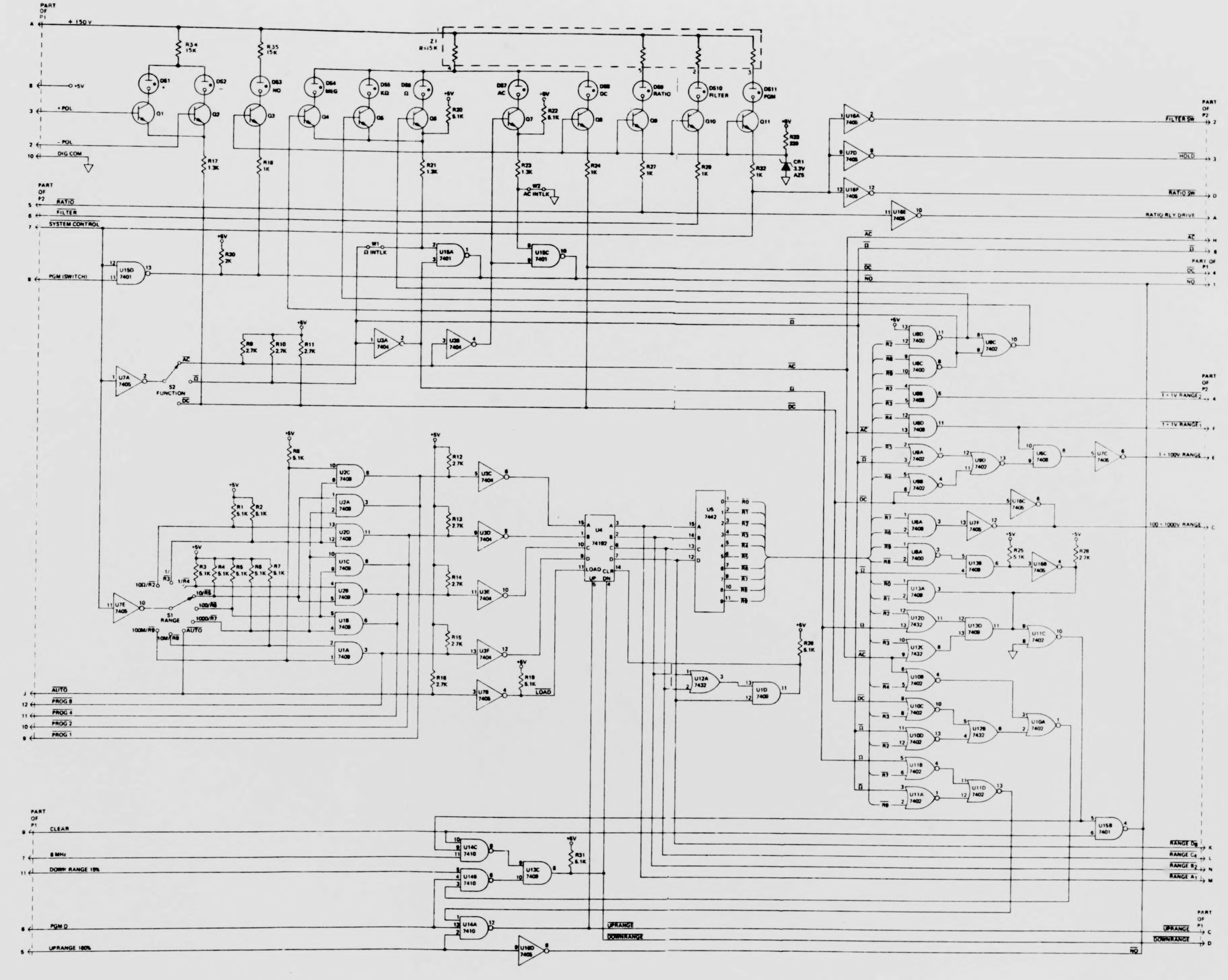

DS1-3
R35
R34
W1
W2
P1
NO
−
+
Q1
Q2
Q3
U8
U13
U14
U15
U9
U5
U6
U10
U12
U16
U11
Q5
Q7
Q6
Q8
Z1
P2
DC
AC
MΩ
KΩ
DS4-11
Q4
Q10
Q11
Q9
S2
U3
U7
U1
U4
S1
U2
403615-M

Figure 6.16 - Layout, Display (403615)


PART OF P1
A
+ 150 V
R34 15K
R35 15K
Z1 R=15K
B
+5V
+ POL
- POL
DIG COM
DS1 +
DS2 -
DS3 NO
DS4 MEG
DS5 KΩ
DS6 Ω
DS7 AC
DS8 DC
DS9 RATIO
DS10 FILTER
DS11 PGM
Q1
Q2
Q3
Q4
Q5
Q6
Q7
Q8
Q9
Q10
Q11
R20 5.1K
R22 5.1K
R17 1.3K
R18 1K
R21 1.3K
R23 1.3K
R24 1K
R27 1K
R29 1K
R32 1K
R33 220
CR1 3.3V AZ5
W2 AC INTLK
PART OF P2
RATIO
FILTER
SYSTEM CONTROL
PGM (SWITCH)
U15D 7401
R30 2K
W1 Ω INTLK
U15A 7401
U15C 7401
U16A 7405
U7D 7405
U16F 7405
U16E 7405
FILTER SW
HOLD
RATIO SW
RATIO RLY DRIVE
AC
Ω
DC
NO
R9 2.7K
R10 2.7K
R11 2.7K
U3A 7404
U3B 7404
U7A 7405
S2 FUNCTION
U8D 7400
U8C 7402
U6C 7400
U6B 7400
U6D 7400
U9A 7402
U9B 7402
U9D 7402
U6C 7408
U7C 7405
1-1V RANGE
1-100V RANGE
U16C 7405
U8A 7400
U7F 7405
100-1000V RANGE
R25 5.1K
R26 2.7K
U13B 7400
U16B 7405
U13A 7408
U12D 7432
U13D 7408
U11C 7402
U12C 7432
U10B 7402
U10C 7402
U10D 7402
U12B 7432
U10A 7402
U11B 7402
U11D 7402
U11A 7402
U15B 7401
RANGE D8
RANGE C4
RANGE B2
RANGE A1
UPRANGE
DOWNRANGE
U2C 7408
U2A 7408
U2D 7408
U1C 7408
U2B 7408
U1B 7408
U1A 7408
R8 5.1K
R1 5.1K
R2 5.1K
R3 5.1K
R4 5.1K
R5 5.1K
R6 5.1K
R7 5.1K
R12 2.7K
R13 2.7K
R14 2.7K
R15 2.7K
R16 2.7K
U3C 7404
U3D 7404
U3E 7404
U3F 7404
U7B 7405
R19 5.1K
LOAD
U4 74192
U5 7442
R0
R1
R2
R3
R4
R5
R6
R7
R8
R9
U12A 7432
U1D 7408
R28 5.1K
S1 RANGE
10Ω/R2
1/R3
1/R4
10/R5
100/R6
1000/R7
100M/R9
10M/R8
AUTO
U7E 7405
PROG 8
PROG 4
PROG 2
PROG 1
CLEAR
6 MHz
DOWN RANGE 15%
PGM D
UPRANGE 100%
U14C 7410
U14B 7410
U13C 7408
R31 5.1K
U14A 7410
U16D 7405

4. ASSY. 403615-E
3. NEON BULBS ARE NE2H
2. TRANSISTORS ARE Q21
1. RESISTORS ARE IN OHMS, ±5% 1/4W

NOTES: UNLESS OTHERWISE SPECIFIED

SCHEM 430999-C
ASSY 403615

Figure 6.17 - Schematic, Display

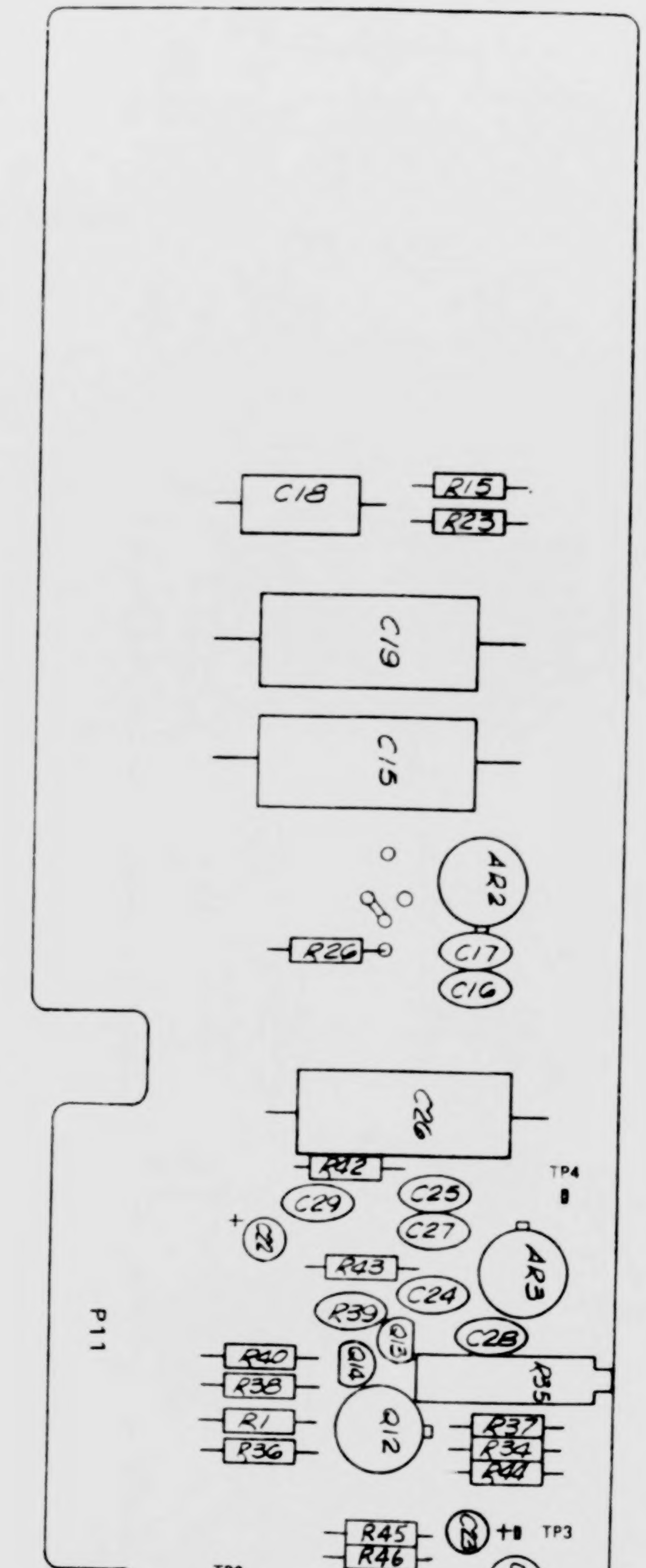
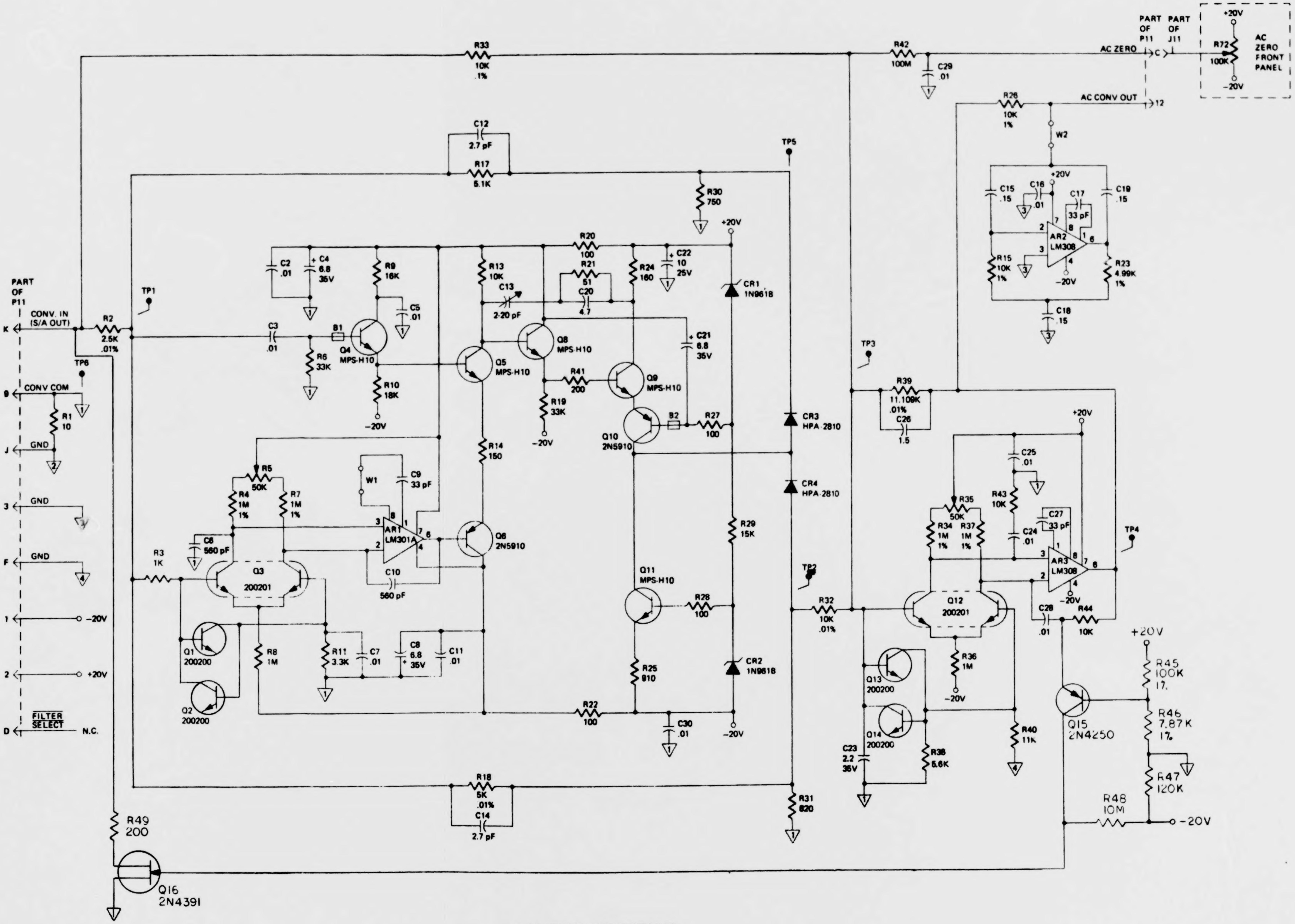

C18
R15
R23
C19
C15
AR2
R26
C17
C16
C26
R42
TP4
C29
C25
C27
C22
AR3
R43
C24
R39
Q13
C28
P11
R40
Q14
R38
R35
R1
Q12
R37
R34
R44
R36
R45
C23
TP3
R46
R47
TP6
Q15
R48
R33
R49
Q16
C30
R32
TP5
TP1
R2
R18
R17
C14
R31
C12
C3
R3
R30
R9
Q3
R4
R7
C2
Q4
C5
Q2
R5
R6
C4
Q1
W1
R10
R13
C7
C9
AR1
R11
R19
R41
C6
R21
Q8
Q5
C8
R8
Q9
C20
C13
C11
C10
R14
Q10
Q11
Q6
R20
CR3
CR4
R22
TP2
R25
CR1
R27
C21
R29
R24
R28
CR2
403667-F

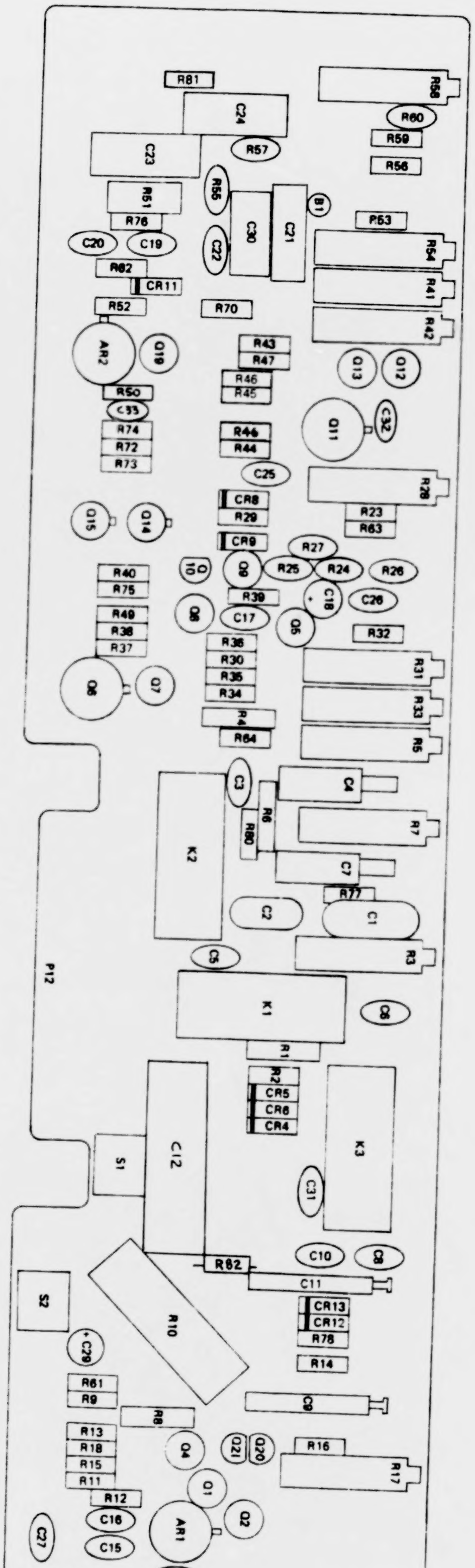
Figure 6.18 - Layout, AC Converter (403667)


PART OF P11
CONV. IN (S/A OUT)
K
R2 2.5K .01%
TP1
TP6
9
CONV COM
R1 10
J
GND
3
GND
F
GND
1
-20V
2
+20V
D
FILTER SELECT
N.C.
R33 10K .1%
R42 100M
C29 .01
AC ZERO
PART OF P11
PART OF J11
C
+20V
R72 100K
-20V
AC ZERO FRONT PANEL
R26 10K 1%
AC CONV OUT
12
W2
+20V
C15 .15
C16 .01
C17 33 pF
C19 .15
AR2 LM308
R15 10K 1%
-20V
R23 4.99K 1%
C18 .15
C12 2.7 pF
R17 5.1K
TP5
R30 750
+20V
C2 .01
C4 6.8 35V
R9 16K
C5 .01
R13 10K
C13 2-20 pF
R20 100
R21 51
C20 4.7
R24 160
C22 10 25V
CR1 1N961B
C3 .01
B1
R6 33K
Q4 MPS-H10
R10 18K
-20V
Q5 MPS-H10
Q8 MPS-H10
R41 200
Q9 MPS-H10
R19 33K
-20V
C21 6.8 35V
B2
R27 100
Q10 2N5910
R5 50K
R4 1M 1%
R7 1M 1%
W1
C9 33 pF
AR1 LM301A
C6 560 pF
R3 1K
Q3 200201
C10 560 pF
R14 150
Q6 2N5910
CR3 HPA 2810
CR4 HPA 2810
R29 15K
Q11 MPS-H10
R28 100
R25 910
CR2 1N961B
R8 1M
R11 3.3K
C7 .01
C8 6.8 35V
C11 .01
Q1 200200
Q2 200200
R22 100
C30 .01
-20V
R18 5K .01%
C14 2.7 pF
R31 820
R49 200
Q16 2N4391
TP3
R39 11.109K .01%
C26 1.5
+20V
C25 .01
R43 10K
R35 50K
R34 1M 1%
R37 1M 1%
C27 33 pF
C24 .01
AR3 LM308
TP4
TP2
R32 10K .01%
Q12 200201
C28 .01
-20V
R44 10K
R36 1M
-20V
Q13 200200
Q14 200200
R38 5.6K
R40 11K
C23 2.2 35V
+20V
R45 100K 1%
Q15 2N4250
R46 7.87K 1%
R47 120K
R48 10M
-20V

NOTE: UNLESS OTHERWISE SPECIFIED
1. RESISTORS ARE IN OHMS, ±5%, 1/4W
2. CAPACITORS ARE IN µF

SCHEM 432022 B
REF ASSY 403667

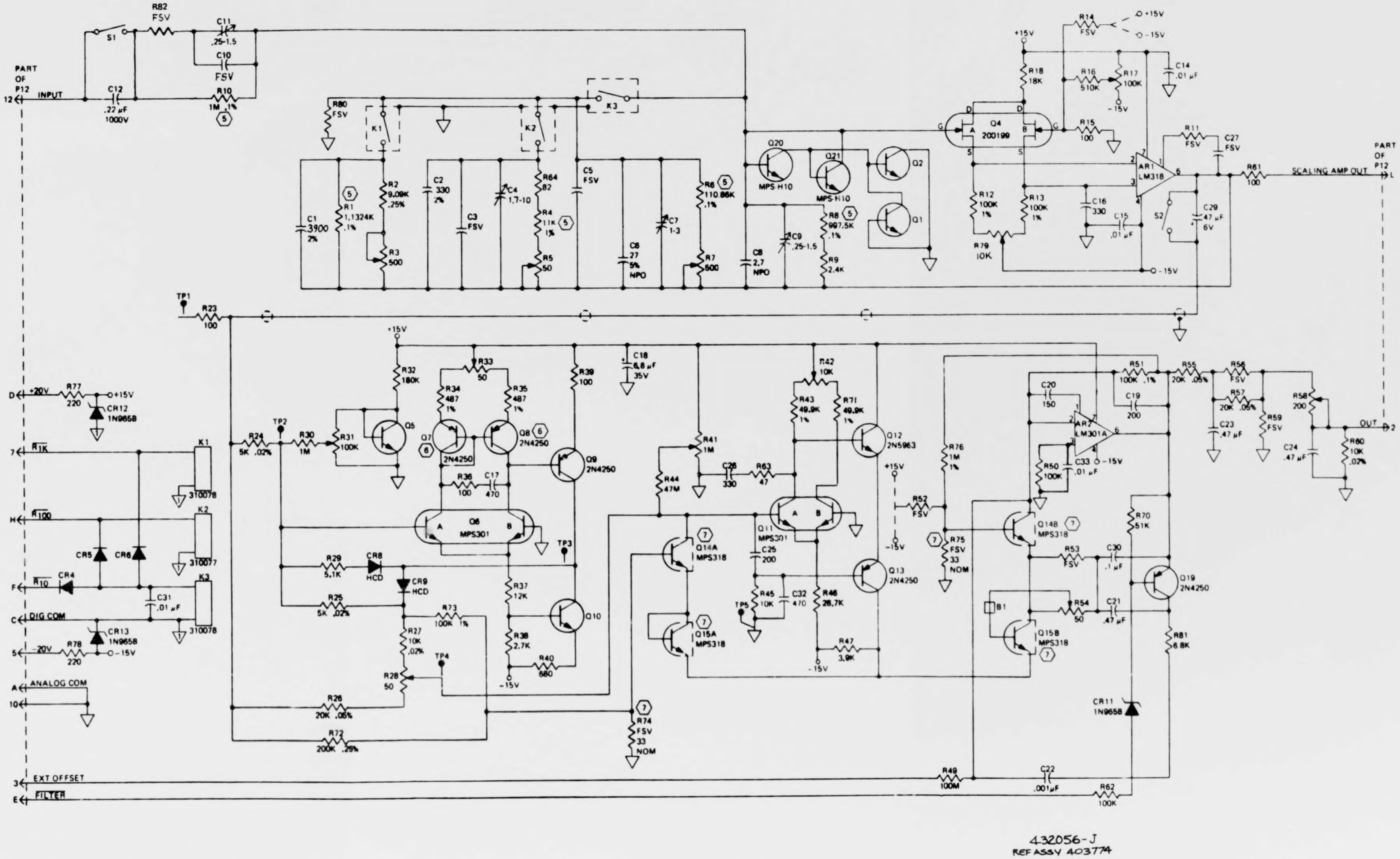
Figure 6.19 - Schematic, AC Converter


R81
R58
C24
R60
R57
R59
C23
R56
R51
R55
B1
C30
C21
R53
R76
C20
C19
C22
R54
R62
CR11
R41
R52
R70
R42
R43
AR2
Q19
R47
R46
R45
Q13
Q12
R50
C33
C32
R74
R46
Q11
R72
R44
R73
C25
R28
CR8
R29
Q15
Q14
R23
R63
CR9
R27
Q10
Q9
R25
R24
R26
R40
R75
R39
C18
C26
Q8
R49
C17
Q5
R38
R32
R37
R36
R30
R31
Q6
Q7
R35
R34
R33
R4
R64
R5
C3
C4
R6
K2
R80
R7
C7
R77
C2
C1
R3
C5
P12
K1
C6
R1
R2
CR5
CR6
CR4
C12
S1
K3
C31
C10
C8
R82
C11
S2
CR13
CR12
R10
R78
+C29
R14
R61
R9
C9
R8
R13
Q21
Q20
R16
R18
Q4
R15
R17
R11
Q1
R12
C16
Q2
AR1
C27
C15
C14
R79

SHIELD OMITTED FOR CLARITY

Figure 6.20 - Layout, RMS Converter (403774)

403774-R


PART OF P12
12 INPUT
S1
R82 FSV
C11 .25-1.5
C10 FSV
C12 .22 µF 1000V
R10 1M .1%
K1
K2
K3
R80 FSV
C1 3900 2%
R1 1.1324K .1%
R2 9.09K .25%
R3 500
C2 330 2%
C3 FSV
C4 1.7-10
R64 82
R4 11K 1%
R5 50
C5 FSV
C6 27 5% NPO
C7 1-3
R6 110.86K .1%
R7 500
C8 2.7 NPO
C9 .25-1.5
R8 997.5K .1%
R9 2.4K
Q20 MPS-H10
Q21 MPS-H10
Q2
Q1
Q4 200199
R18 18K
R16 510K
R17 100K
R14 FSV
+15V
-15V
C14 .01 µF
R15 100
R12 100K 1%
R13 100K 1%
R79 10K
C16 330
C15 .01 µF
AR1 LM318
S2
R11 FSV
C27 FSV
C29 47 µF 6V
R61 100
SCALING AMP OUT
TP1
R23 100
D +20V
R77 220
CR12 1N9658
7 R1K
H R100
F R10
C DIG COM
5 -20V
R78 220
CR13 1N9658
CR4
CR5
CR6
C31 .01 µF
310078
310077
A ANALOG COM
10
TP2
R24 5K .02%
R30 1M
R31 100K
R32 180K
Q5
Q7 2N4250
Q8 2N4250
R33 50
R34 487 1%
R35 487 1%
R36 100
C17 470
Q6 MPS301
R39 100
Q9 2N4250
TP3
R29 5.1K
CR8 HCD
CR9 HCD
R25 5K .02%
R73 100K 1%
R37 12K
R38 2.7K
R40 680
Q10
R27 10K .02%
R28 50
TP4
R26 20K .05%
R72 200K .25%
C18 6.8 µF 35V
R41 1M
R44 47M
C28 330
R63 47
R42 10K
R43 49.9K 1%
R71 49.9K 1%
Q11 MPS301
Q12 2N5963
Q13 2N4250
Q14A MPS318
Q15A MPS318
C25 200
R45 10K
C32 470
R46 28.7K
R47 3.9K
TP5
R74 FSV 33 NOM
R76 1M 1%
R52 FSV
R75 FSV 33 NOM
C20 150
AR2 LM301A
R50 100K
C33 .01 µF
R51 100K .1%
C19 200
Q14B MPS318
Q15B MPS318
B1
R53 FSV
C30 .1 µF
R54 50
C21 .47 µF
R70 51K
Q19 2N4250
R81 6.8K
CR11 1N9658
R55 20K .05%
R56 FSV
R57 20K .05%
R59 FSV
C23 .47 µF
R58 200
C24 .47 µF
R60 10K .02%
OUT 2
3 EXT OFFSET
E FILTER
R49 100M
C22 .001µF
R62 100K
432056-J
REF ASSY 403774

8. ASSY. 403774

⑦ Q14, Q15, R74, AND R75 ARE LOG TRANSISTOR KIT 403865

⑥ Q7 AND Q8 ARE MATCHED PAIR 200112

⑤ R1, R4, R6, R8, AND R10 ARE RESISTOR SET 010721

4. TRANSISTORS ARE 200200

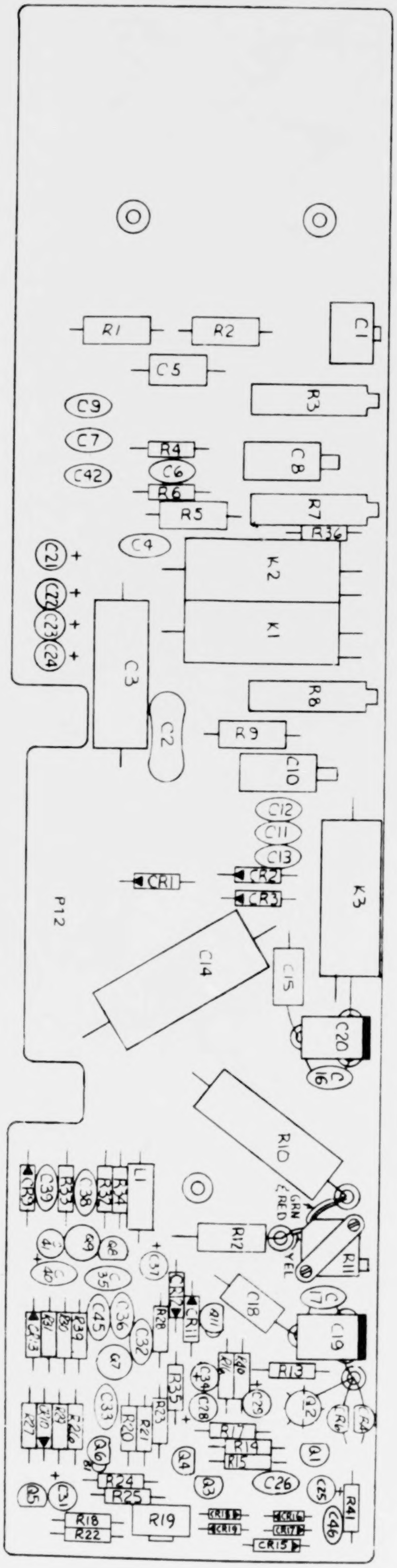
3. DIODES ARE 018

2. CAPACITORS ARE IN pF

1. RESISTORS ARE IN OHMS, ±5%, 1/4W

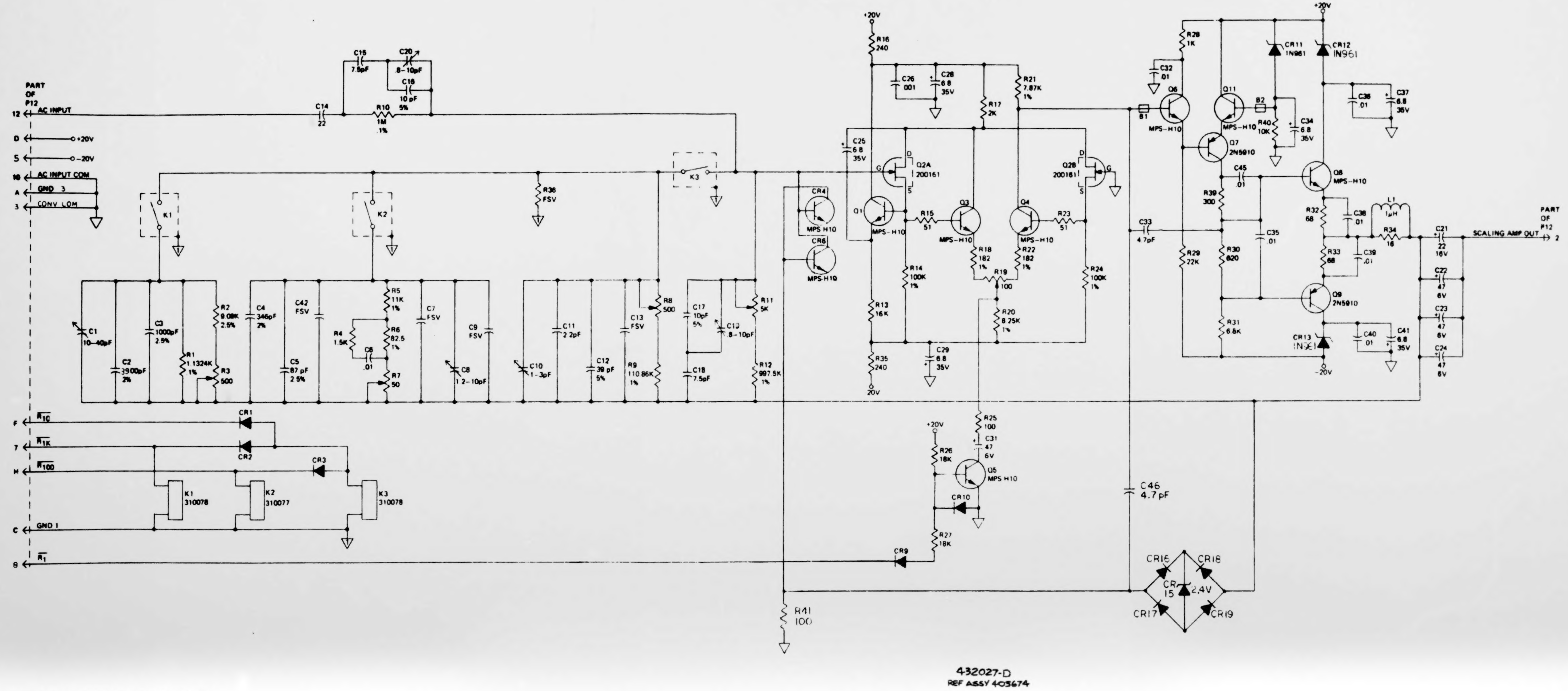
NOTES: UNLESS OTHERWISE SPECIFIED

Figure 6.21 - Schematic, RMS Converter


R1
R2
C1
C5
R3
C9
C7
R4
C8
C42
C6
R6
R5
R7
R36
C4
K2
C21
C22
C23
C24
K1
C3
R8
C2
R9
C10
C12
C11
C13
CR1
CR2
CR3
P12
K3
C14
C15
C20
C16
R10
L1
R34
R37
C38
R33
C39
GRN
& RED
YEL
R12
R11
C37
C35
C40
C41
CR12
CR11
C18
C17
C19
R28
Q11
C36
C45
C32
Q7
R31
R30
R39
CR13
R13
C34
C28
C29
Q2
R35
R23
R21
R20
C33
R17
R19
R15
Q1
Q4
Q3
Q6
C26
C25
R41
C46
R24
R25
C31
Q5
R18
R22
R19
CR18
CR19
CR16
CR17
CR15

403674-P

Figure 6.22 - Layout, Scaling Amplifier (403674)


PART OF P12
12 AC INPUT
D +20V
5 -20V
10 AC INPUT COM
A GND 3
3 CONV COM
C14 22
R10 1M 1%
C15 7.5pF
C20 8-10pF
C16 10 pF 5%
K1
K2
K3
R36 FSV
C1 10-40pF
C2 3900pF 2%
C3 1000pF 2.5%
R1 1.1324K 1%
R2 9.08K 2.5%
R3 500
C4 346pF 2%
C5 87 pF 2.5%
C42 FSV
R4 1.5K
C6 .01
R5 11K 1%
R6 82.5 1%
R7 50
C7 FSV
C8 1.2-10pF
C9 FSV
C10 1-3pF
C11 2.2pF
C12 39 pF 5%
C13 FSV
R9 110.86K 1%
R8 500
C17 10pF 5%
C19 8-10pF
C18 7.5pF
R11 5K
R12 997.5K 1%
F R10
7 R1K
H R100
CR1
CR2
CR3
K1 310078
K2 310077
K3 310078
C GND 1
B R1
CR9
R41 100
+20V
R16 240
C26 .001
C28 6.8 35V
R17 2K
R21 7.87K 1%
C25 6.8 35V
Q2A 200161
Q2B 200161
CR4 MPS H10
CR6 MPS H10
Q1 MPS-H10
R15 51
Q3 MPS-H10
Q4 MPS-H10
R23 51
R18 182 1%
R19 100
R22 182 1%
R14 100K 1%
R24 100K 1%
R13 16K
R20 8.25K 1%
C29 6.8 35V
R35 240
-20V
R25 100
C31 47 6V
+20V
R26 18K
Q5 MPS H10
CR10
R27 18K
C46 4.7 pF
CR16
CR18
CR15 2.4V
CR17
CR19
+20V
R28 1K
C32 .01
B1
Q6 MPS-H10
Q11 MPS-H10
B2
R40 10K
C34 6.8 35V
CR11 1N961
CR12 1N961
Q7 2N5910
C36 .01
C37 6.8 35V
C45 .01
Q8 MPS-H10
R39 300
C33 4.7pF
R29 22K
R30 820
C35 .01
R32 68
C38 .01
L1 1μH
R34 18
R33 68
C39 .01
Q9 2N5910
R31 6.8K
CR13 1N961
C40 .01
C41 6.8 35V
-20V
C21 22 16V
C22 47 6V
C23 47 6V
C24 47 6V
SCALING AMP OUT
PART OF P12 2
432027-D
REF ASSY 403674

5. CR16 & CR17, CR18 & CR19 ARE MATCHED HP 5082-2800 DIODES
4. ASSY 403674
3. DIODES ARE 018
2. CAPACITORS ARE IN μF
1. RESISTORS ARE IN OHMS, ±5%, 1/4W

NOTES: UNLESS OTHERWISE SPECIFIED

Figure 6.23 - Schematic, Scaling Amplifier

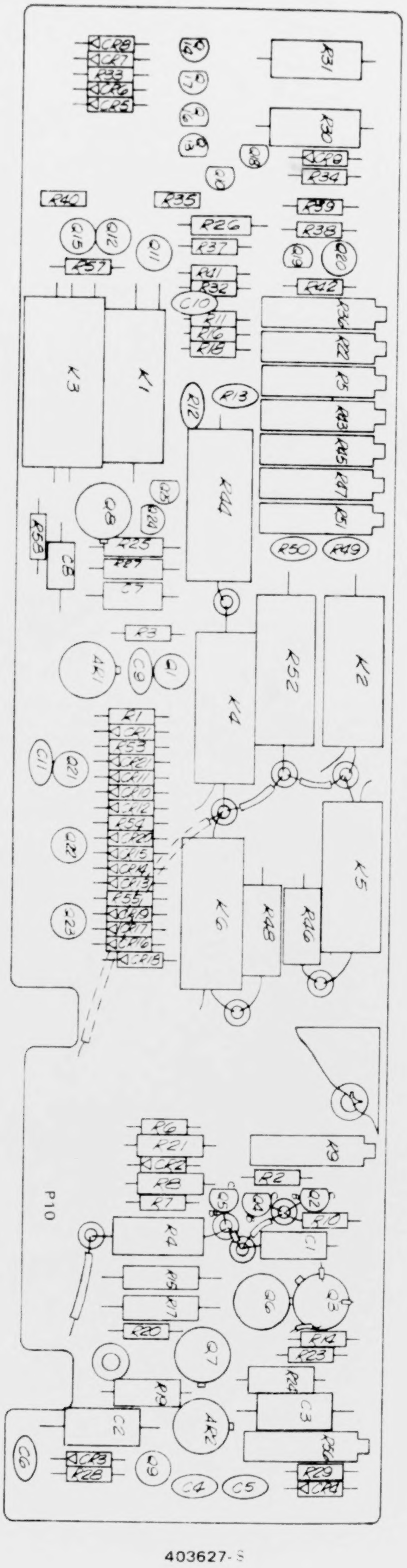
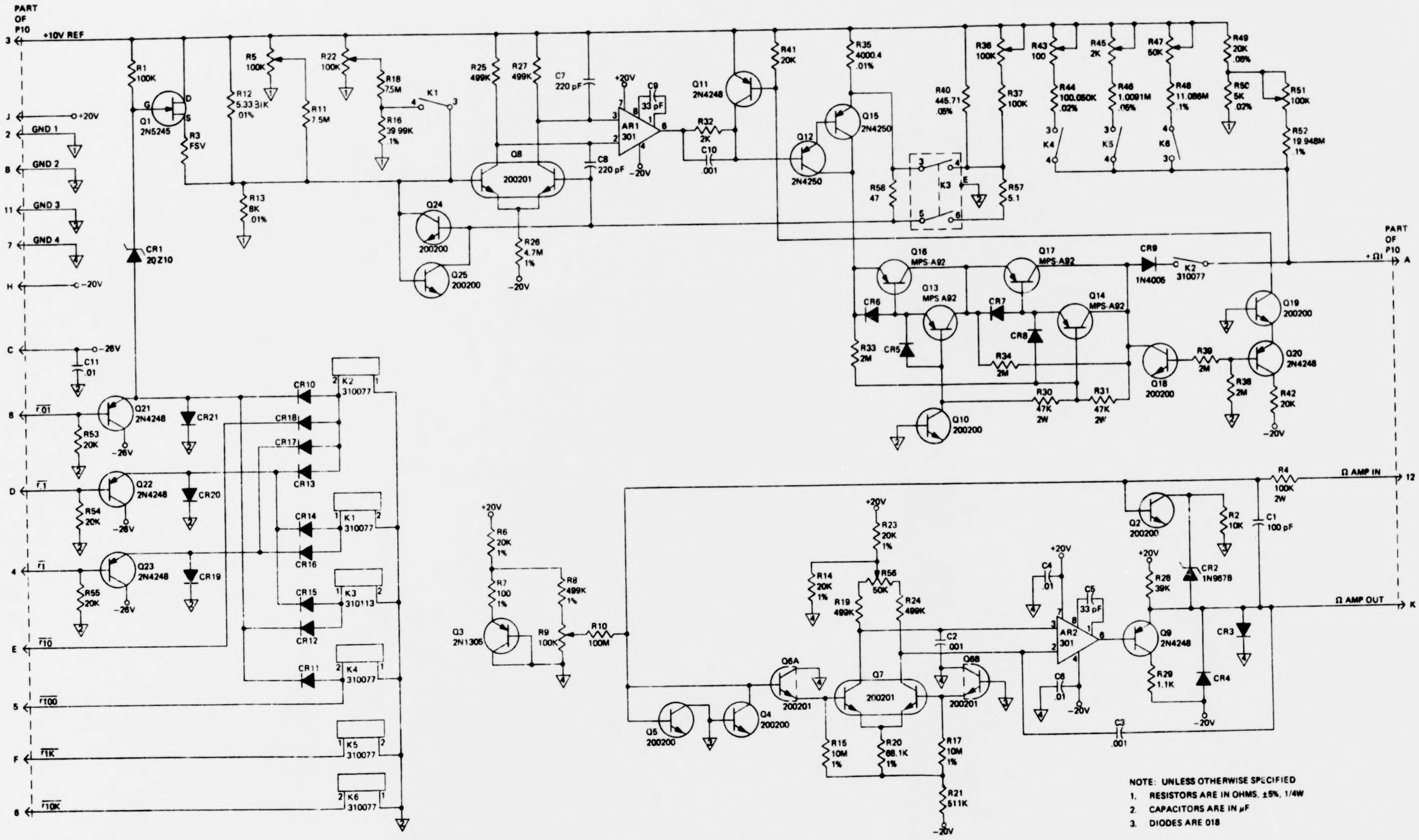

R31
R30
CR8
CR7
R33
CR6
CR5
Q14
Q17
Q16
Q13
Q18
Q10
CR9
R34
R40
R35
R39
R26
R38
Q15
Q12
Q11
R37
Q19
Q20
R57
R41
R32
R42
C10
R11
R16
R18
R36
R22
R5
R43
R45
R47
R51
K3
K1
R12
R13
R44
Q8
R24
R25
R27
C7
R53
C8
R50
R49
R3
AR1
C9
Q1
K4
R52
K2
R1
CR1
R53
CR21
CR11
CR10
CR12
R54
CR20
CR15
CR14
CR13
R55
CR19
CR17
CR16
CR18
C11
Q21
Q22
Q23
K6
R48
R46
K5
R6
R21
CR2
R8
R7
R9
R2
Q5
Q4
Q2
R10
C1
R4
P10
R15
R17
Q6
Q3
R20
R14
R23
Q7
R19
C3
AR2
C2
C6
CR3
R28
Q9
C4
C5
R29
CR4
403627-S

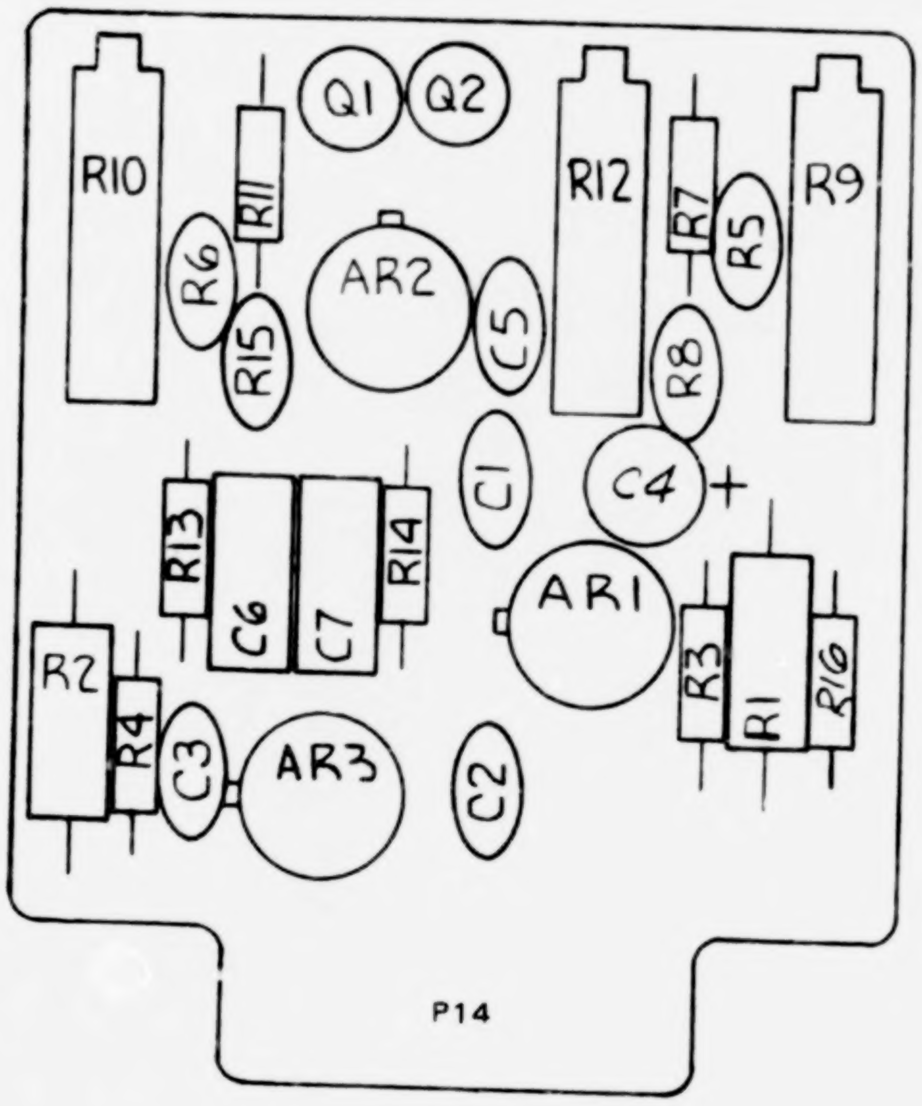
Figure 6.24 - Layout, Ohms Converter (403627)


PART OF P10
+10V REF
GND 1
GND 2
GND 3
GND 4
+20V
-20V
-26V
Q1 2N5245
R1 100K
R3 FSV
CR1 20Z10
R12 5.331K .01%
R13 8K .01%
R5 100K
R11 7.5M
R22 100K
R18 7.5M
R16 39.99K .1%
K1
R25 499K
R27 499K
C7 220 pF
C8 220 pF
C9 33 pF
AR1 301
Q8 200201
Q24 200200
Q25 200200
R26 4.7M 1%
R32 2K
C10 .001
Q11 2N4248
R41 20K
R35 4000.4 .01%
Q12 2N4250
Q15 2N4250
R40 445.71 .05%
R36 100K
R37 100K
R58 47
K3
R57 5.1
R43 100
R44 100.050K .02%
R45 2K
R46 1.0091M .05%
R47 50K
R48 11.088M .1%
R49 20K .05%
R50 5K .02%
R51 100K
R52 19.948M .1%
K4
K5
K6
Q16 MPS-A92
Q17 MPS-A92
Q13 MPS-A92
Q14 MPS-A92
CR9 1N4005
K2 310077
+Ω I
A
CR6
CR7
CR8
CR5
R33 2M
R34 2M
R30 47K 2W
R31 47K 2W
Q10 200200
Q18 200200
R39 2M
R38 2M
Q19 200200
Q20 2N4248
R42 20K
C11 .01
r.01
r.1
r1
r10
r100
r1K
r10K
Q21 2N4248
Q22 2N4248
Q23 2N4248
R53 20K
R54 20K
R55 20K
CR21
CR20
CR19
CR10
CR18
CR17
CR13
CR14
CR16
CR15
CR12
CR11
K1 310077
K3 310113
K4 310077
K5 310077
K6 310077
R6 20K 1%
R7 100 1%
R8 499K 1%
R9 100K
R10 100M
Q3 2N1305
Ω AMP IN
12
R4 100K 2W
C1 100 pF
Q2 200200
R2 10K
CR2 1N9678
R23 20K 1%
R14 20K 1%
R56 50K
R19 499K
R24 499K
C2 .001
C4 .01
C5 33 pF
AR2 301
C6 .01
Q9 2N4248
R28 39K
R29 1.1K
CR3
CR4
Ω AMP OUT
K
Q6A 200201
Q7 200201
Q6B 200201
Q5 200200
Q4 200200
R15 10M 1%
R20 68.1K 1%
R17 10M 1%
R21 511K
C3 .001
SCHEM 432009-F
REF ASSY 403627

NOTE: UNLESS OTHERWISE SPECIFIED

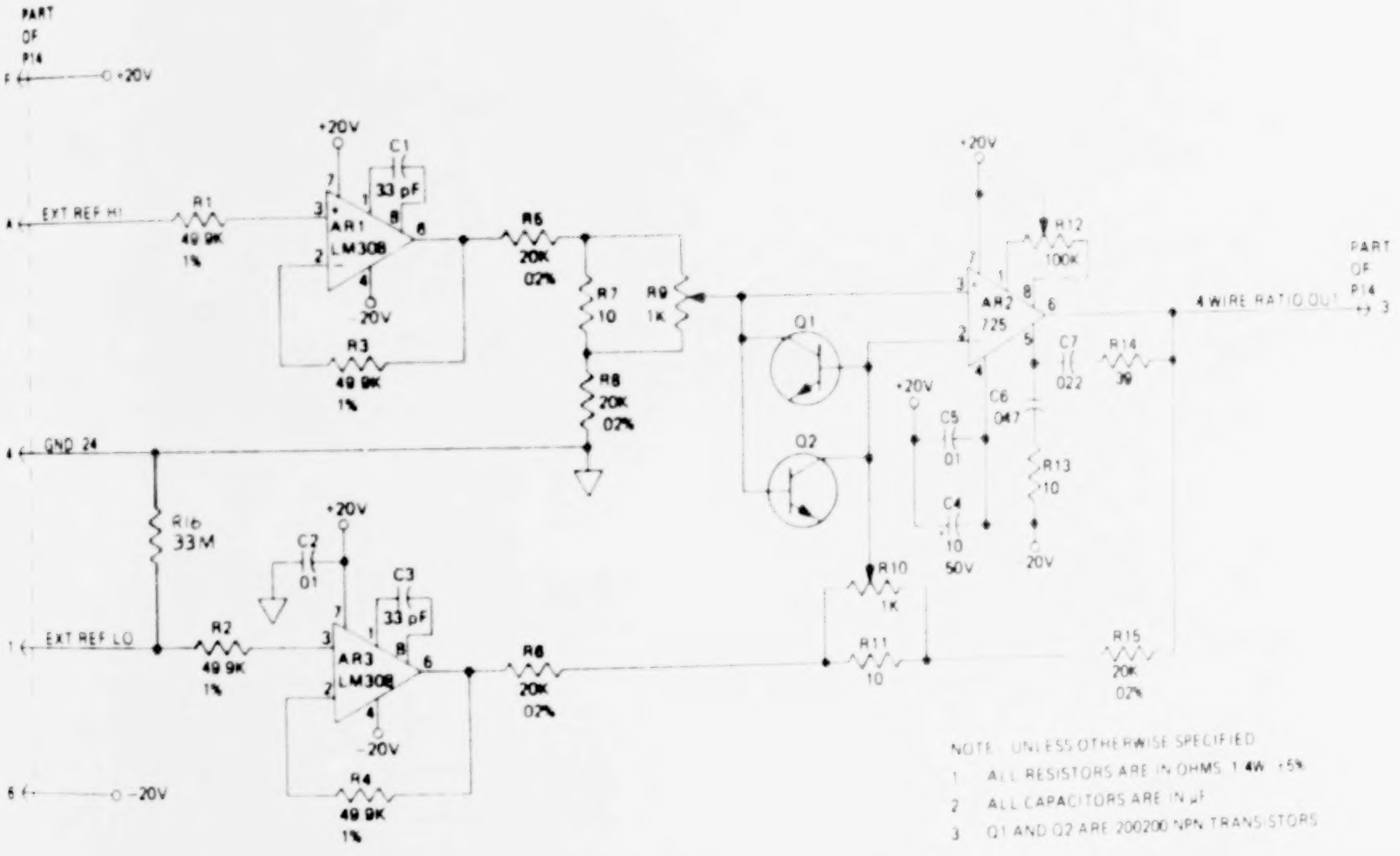
1. RESISTORS ARE IN OHMS, ±5%, 1/4W
2. CAPACITORS ARE IN μF
3. DIODES ARE 018

Figure 6.25 - Schematic, Ohms Converter


Q1
Q2
R10
R11
R12
R7
R5
R9
R6
AR2
C5
R15
R8
C1
C4
+
R13
C6
C7
R14
AR1
R3
R1
R16
R2
R4
C3
AR3
C2
P14

403673-E

Figure 6.26 - Layout, 4-Wire Ratio (403673)


PART OF P14
F
+20V
+20V
C1
33 pF
EXT REF HI
A
R1
49.9K
1%
AR1
LM308
-20V
R3
49.9K
1%
R5
20K
.02%
R7
10
R9
1K
R8
20K
.02%
GND 24
4
R16
33M
+20V
C2
.01
C3
33 pF
EXT REF LO
1
R2
49.9K
1%
AR3
LM308
-20V
R4
49.9K
1%
R6
20K
.02%
B
-20V
Q1
Q2
+20V
R12
100K
AR2
725
C7
.022
R14
39
+20V
C5
.01
C6
.047
R13
10
C4
10
50V
20V
R10
1K
R11
10
R15
20K
.02%
PART OF P14
4 WIRE RATIO OUT
3

NOTE: UNLESS OTHERWISE SPECIFIED
1. ALL RESISTORS ARE IN OHMS, 1/4W ±5%
2. ALL CAPACITORS ARE IN µF
3. Q1 AND Q2 ARE 200200 NPN TRANSISTORS

SCHEM 432026 B
ASSY 403673

Figure 6.27 - Schematic, 4-Wire Ratio

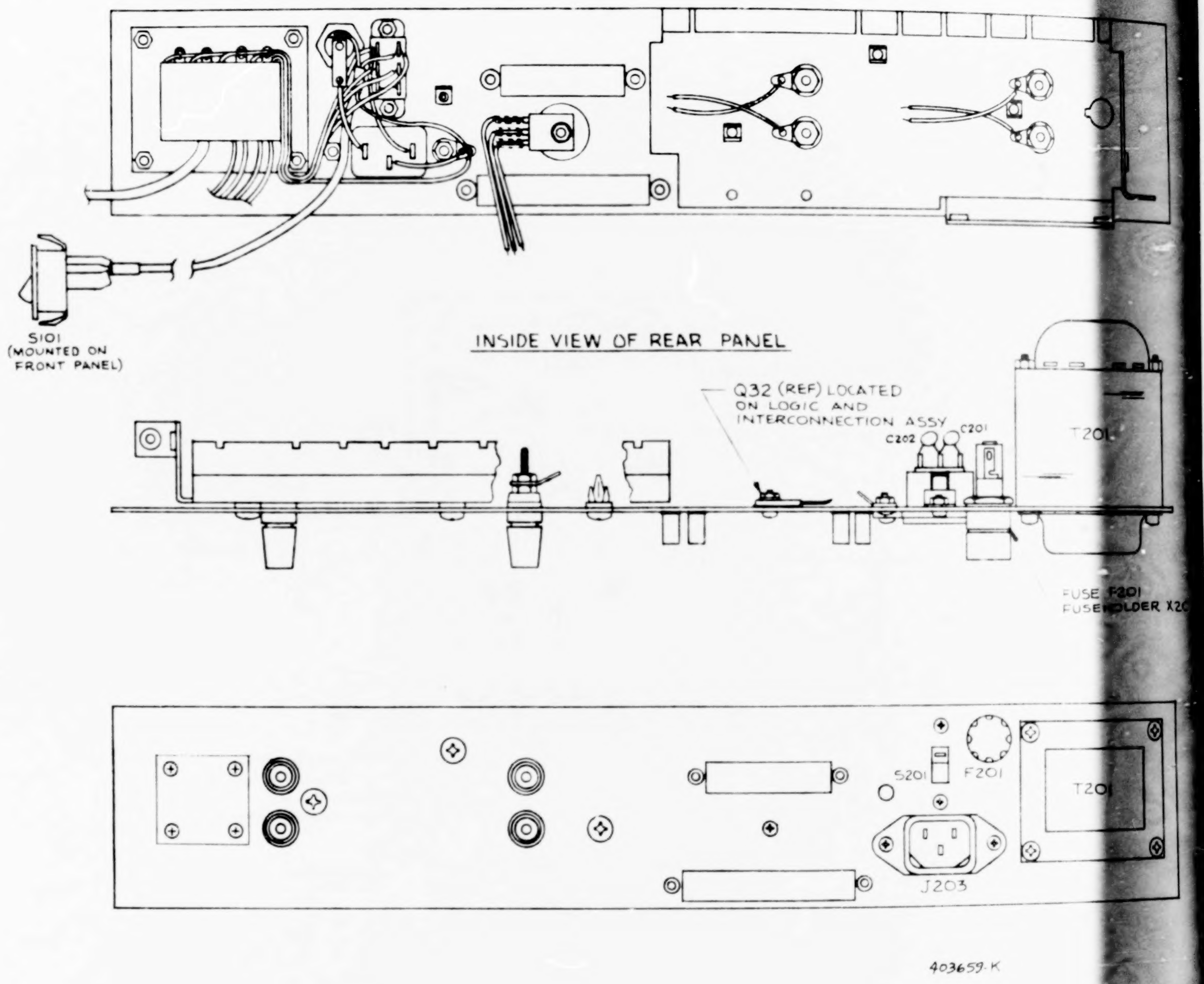
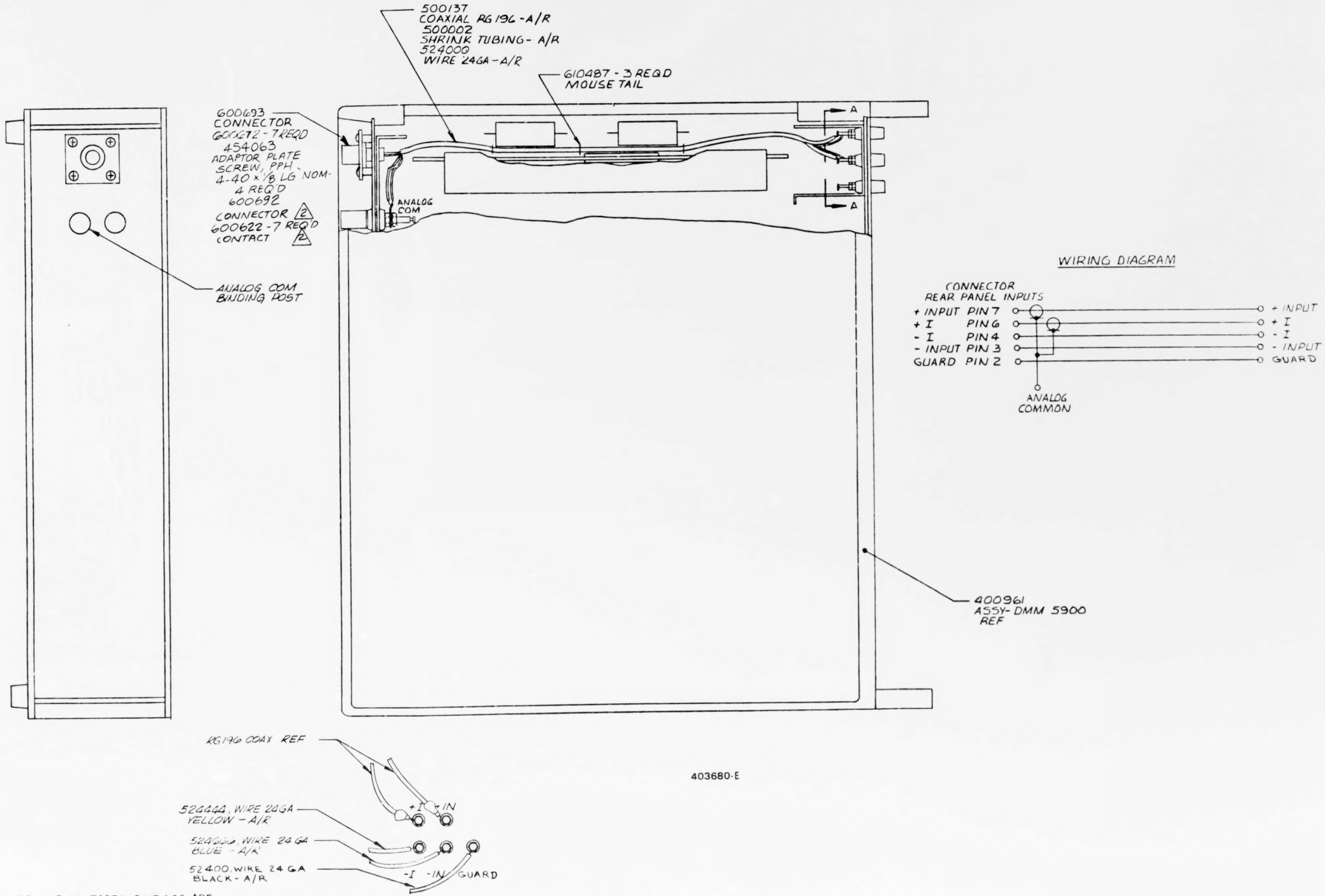

S101
(MOUNTED ON
FRONT PANEL)
INSIDE VIEW OF REAR PANEL
Q32 (REF) LOCATED
ON LOGIC AND
INTERCONNECTION ASSY
C202
C201
T201
FUSE F201
FUSEHOLDER X2
S201
F201
T201
J203
403659-K

Figure 6.28 - Layout, Rear Panel (403659)

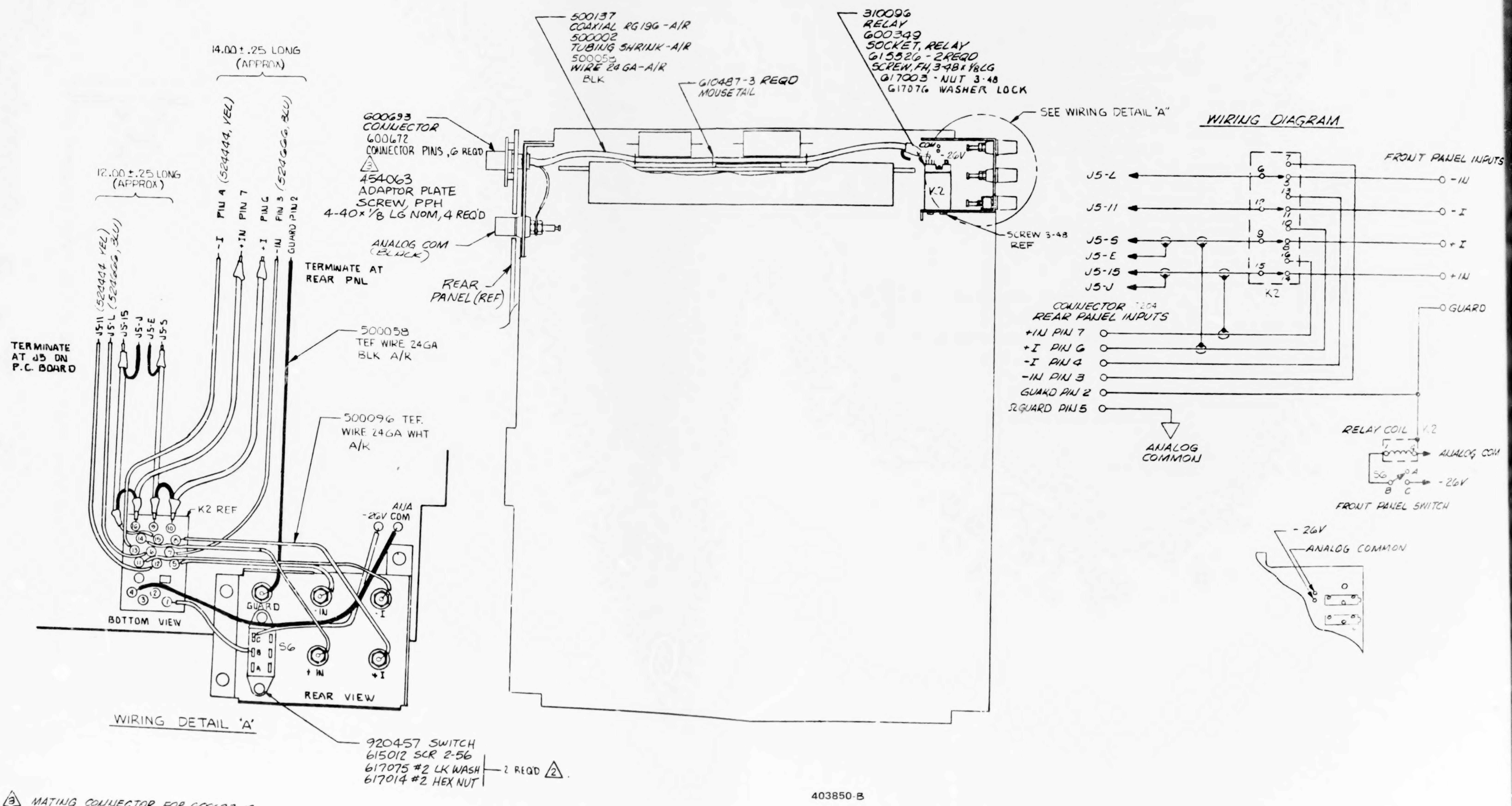

500137
COAXIAL RG 196 - A/R
500002
SHRINK TUBING - A/R
524000
WIRE 24GA - A/R
610487 - 3 REQD
MOUSE TAIL
600693
CONNECTOR
600672 - 7 REQD
454063
ADAPTOR PLATE
SCREW, PPH
4-40 × 1/8 LG NOM-
4 REQ'D
600692
CONNECTOR 2
600622 - 7 REQ'D
CONTACT 2
ANALOG COM
A
A
ANALOG COM
BINDING POST
WIRING DIAGRAM
CONNECTOR
REAR PANEL INPUTS
+ INPUT PIN 7
+ I PIN 6
- I PIN 4
- INPUT PIN 3
GUARD PIN 2
+ INPUT
+ I
- I
- INPUT
GUARD
ANALOG
COMMON
400961
ASSY- DMM 5900
REF
RG196 COAX REF
524444, WIRE 24GA
YELLOW - A/R
524666, WIRE 24 GA
BLUE - A/R
52400, WIRE 24 GA
BLACK - A/R
+ I
+ IN
- I
- IN
GUARD
VIEW A-A
403680-E

2 CONNECTOR 600692 AND CONTACTS 600622 ARE THE MATING PARTS FOR 600693 CONNECTOR AND 600672 CONTACTS WHICH ARE MOUNTED ON THE REAR SHIELD

1. ASSEMBLY PROCEDURES AND PROCESSES TO CONFORM TO DANA WORKMANSHIP STANDARDS.

NOTES: UNLESS OTHERWISE SPECIFIED

Figure 6.29 - Layout, Parallel Front-Rear Input (–1)


500137
COAXIAL RG196-A/R
500002
TUBING SHRINK-A/R
500058
WIRE 24 GA-A/R
BLK
610487-3 REQD
MOUSETAIL
310096
RELAY
600349
SOCKET, RELAY
615526-2REQD
SCREW, FH, 3-48 x 1/8 LG
617003-NUT 3-48
617076 WASHER LOCK
SEE WIRING DETAIL "A"
600693
CONNECTOR
600672
CONNECTOR PINS, 6 REQD
3
454063
ADAPTOR PLATE
SCREW, PPH
4-40 x 1/8 LG NOM, 4 REQ'D
ANALOG COM
(BLACK)
REAR PANEL (REF)
COM
-26V
K2
SCREW 3-48
REF
14.00 ± .25 LONG
(APPROX)
12.00 ± .25 LONG
(APPROX)
-I PIN 4 (524444, YEL)
+IN PIN 7
+I PIN 6
-IN PIN 3 (524666, BLU)
GUARD PIN 2
TERMINATE AT
REAR PNL
J5-11 (524444 YEL)
J5-L (524666, BLU)
J5-15
J5-J
J5-E
J5-5
TERMINATE
AT J5 ON
P.C. BOARD
500058
TEF WIRE 24GA
BLK A/R
500096 TEF.
WIRE 24GA WHT
A/R
K2 REF
-26V
ANA COM
GUARD
-IN
-I
+IN
+I
S6
C
B
A
BOTTOM VIEW
REAR VIEW
WIRING DETAIL 'A'
920457 SWITCH
615012 SCR 2-56
617075 #2 LK WASH
617014 #2 HEX NUT
2 REQD 2
WIRING DIAGRAM
FRONT PANEL INPUTS
J5-L
J5-11
J5-5
J5-E
J5-15
J5-J
-IN
-I
+I
+IN
K2
CONNECTOR J204
REAR PANEL INPUTS
+IN PIN 7
+I PIN 6
-I PIN 4
-IN PIN 3
GUARD PIN 2
ΩGUARD PIN 5
GUARD
ANALOG COMMON
RELAY COIL K2
ANALOG COM
-26V
S6
A
B
C
FRONT PANEL SWITCH
-26V
ANALOG COMMON
403850-B

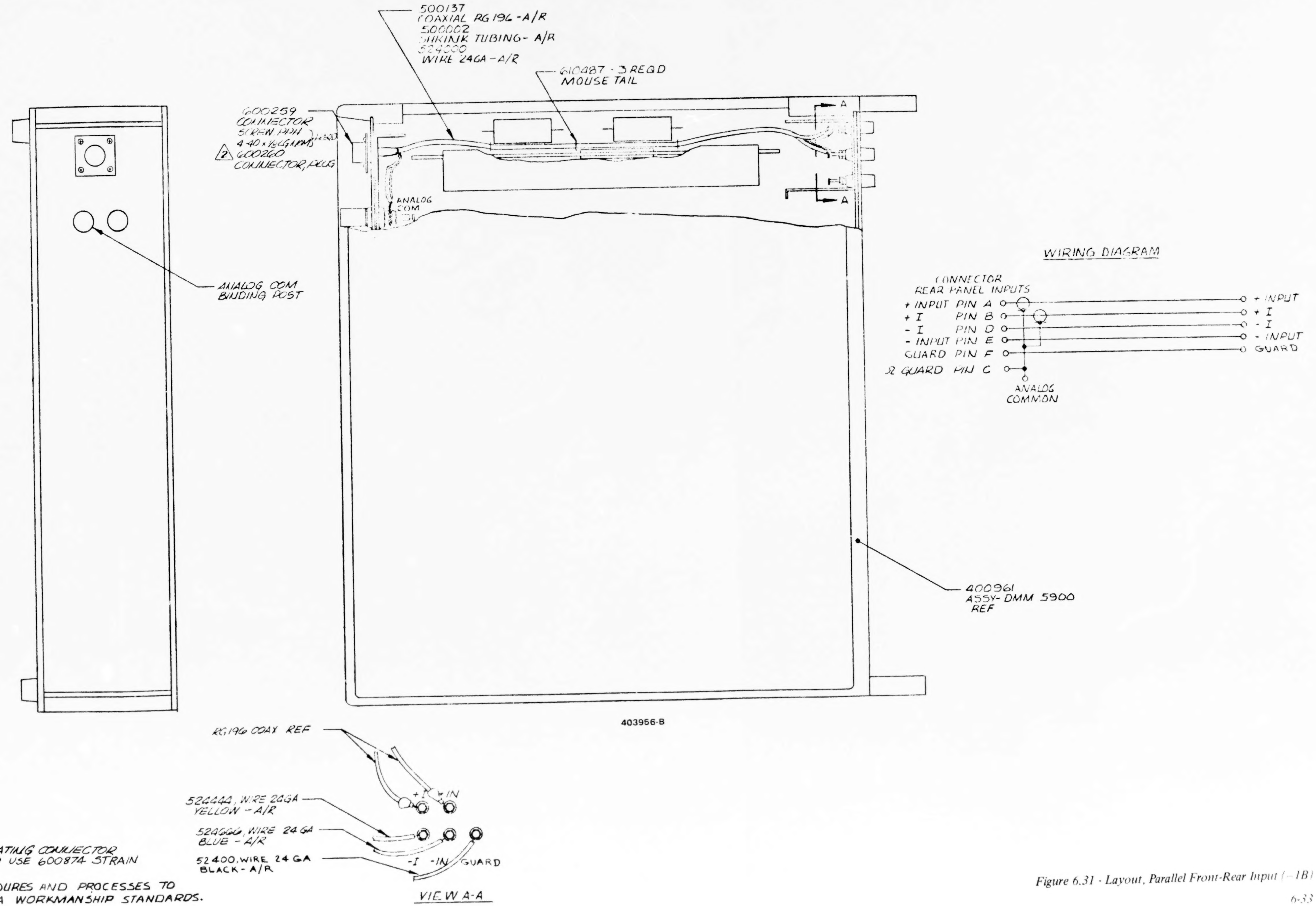
3 MATING CONNECTOR FOR 600693 IS 600692, FEMALE PINS ARE 600622

2 USE LOCTITE 920056 ON SCREWS

1. ASSEMBLY PROCEDURES AND PROCESSES TO CONFORM TO DANA WORKMANSHIP STANDARDS

NOTES: UNLESS OTHERWISE SPECIFIED

Figure 6.30 - Layout, Switchable Front-Rear Input (–1S)

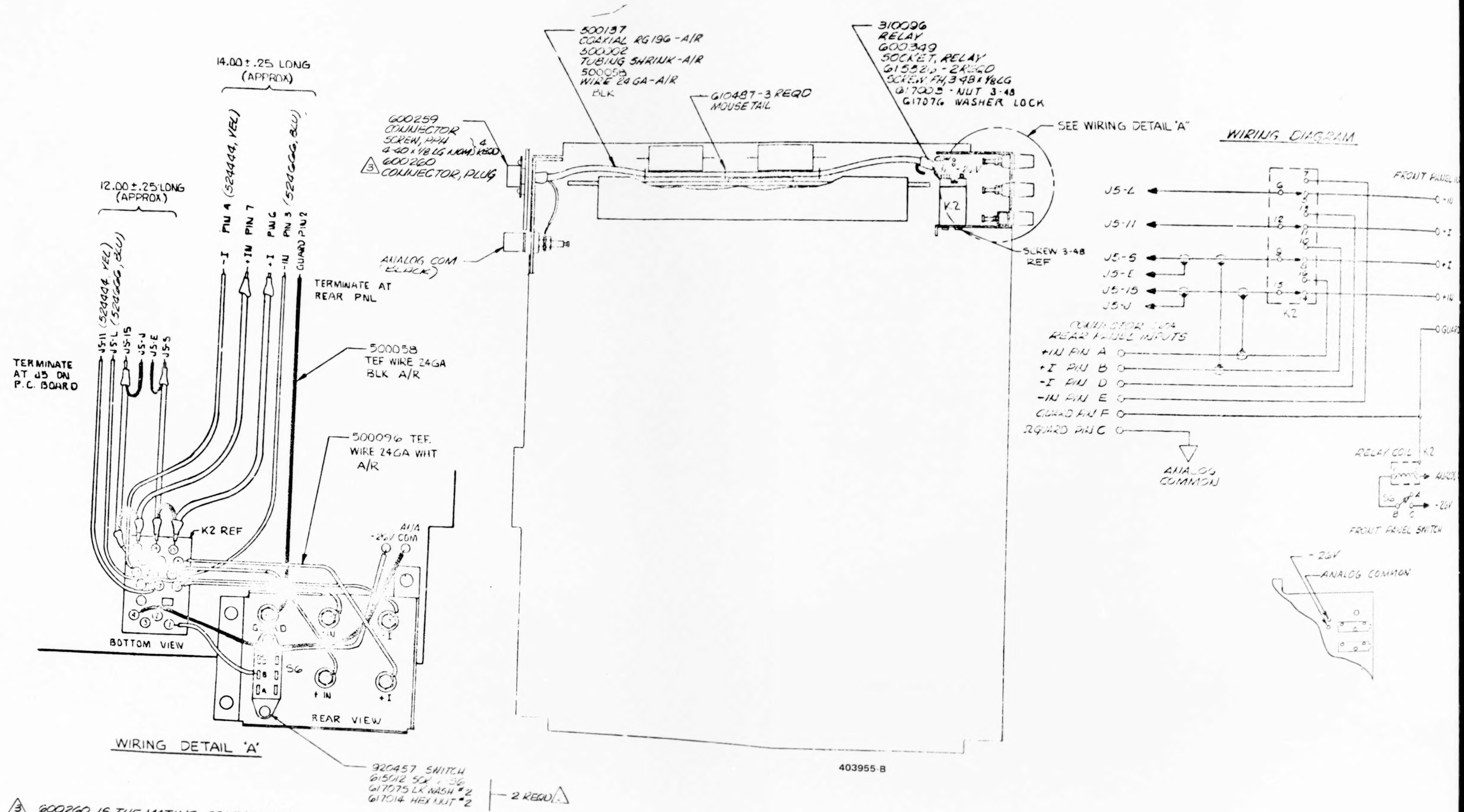

500137
COAXIAL RG196 - A/R
500002
SHRINK TUBING - A/R
524000
WIRE 24GA - A/R
610487 - 3 REQD
MOUSE TAIL
600259
CONNECTOR
SCREW, PPH
4 40 x 1/8 LG NMS
2 600260
CONNECTOR, PLUG
ANALOG
COM
A
A
ANALOG COM
BINDING POST
WIRING DIAGRAM
CONNECTOR
REAR PANEL INPUTS
+ INPUT PIN A
+ I PIN B
- I PIN D
- INPUT PIN E
GUARD PIN F
Ω GUARD PIN C
ANALOG
COMMON
+ INPUT
+ I
- I
- INPUT
GUARD
400961
ASSY- DMM 5900
REF
403956-B
RG196 COAX REF
524444, WIRE 24GA
YELLOW - A/R
524666, WIRE 24 GA
BLUE - A/R
52400, WIRE 24 GA
BLACK - A/R
+ I
+ IN
-I
-IN
GUARD
VIEW A-A

2. 600260 IS THE MATING CONNECTOR FOR 600259. ALSO USE 600874 STRAIN RELIEF.

1. ASSEMBLY PROCEDURES AND PROCESSES TO CONFORM TO DANA WORKMANSHIP STANDARDS.

NOTES: UNLESS OTHERWISE SPECIFIED

Figure 6.31 - Layout, Parallel Front-Rear Input (-1B)


500137
COAXIAL RG196-A/R
500002
TUBING SHRINK-A/R
500058
WIRE 24 GA-A/R
BLK
610487-3 REQD
MOUSE TAIL
310096
RELAY
600349
SOCKET, RELAY
615526-2 REQD
SCREW, FH, 3-48 x 1/8 LG
617003 - NUT 3-48
617076 WASHER LOCK
SEE WIRING DETAIL 'A'
600259
CONNECTOR
SCREW, PPH
4-40 x 1/8 LG NOM } 4 REQD
3
600260
CONNECTOR, PLUG
ANALOG COM
(BLACK)
K2
SCREW 3-48
REF
14.00 ± .25 LONG
(APPROX)
12.00 ± .25 LONG
(APPROX)
-I PIN 4 (524444, YEL)
+IN PIN 7
PWG
+I PIN 3 (524666, BLU)
-IN
GUARD PIN 2
TERMINATE AT
REAR PNL
J5-11 (524444 YEL)
J5-L (524666, BLU)
J5-15
J5-J
J5-E
J5-5
TERMINATE
AT J5 ON
P.C. BOARD
500058
TEF WIRE 24GA
BLK A/R
500096 TEF.
WIRE 24GA WHT
A/R
K2 REF
AUA
-26V COM
GUARD
-IN
-I
S6
C
B
A
+IN
+I
BOTTOM VIEW
REAR VIEW
WIRING DETAIL 'A'
920457 SWITCH
615012 SCR - 56
617075 LK WASH #2
617014 HEX NUT #2
2 REQD
WIRING DIAGRAM
J5-L
J5-11
J5-5
J5-E
J5-15
J5-J
7
6
3
13
12
11
10
9
8
16
15
14
K2
FRONT PANEL IN
-IN
-I
+I
+IN
GUARD
CONNECTOR J204
REAR PANEL INPUTS
+IN PIN A
+I PIN B
-I PIN D
-IN PIN E
GUARD PIN F
ΩGUARD PIN C
ANALOG
COMMON
RELAY COIL K2
ANALOG
S6
A
B
C
-26V
FRONT PANEL SWITCH
-26V
ANALOG COMMON
403955-B

3. 600260 IS THE MATING CONNECTOR FOR 600259. ALSO USE 600874 STRAIN RELIEF.

2. USE LOCTITE 920056 ON SCREWS

1. ASSEMBLY PROCEDURES AND PROCESSES TO
CONFORM TO DANA WORKMANSHIP STANDARDS

NOTES: UNLESS OTHERWISE SPECIFIED

Figure 6.32 - Layout, Switchable Front-Rear Input (–1SB)